
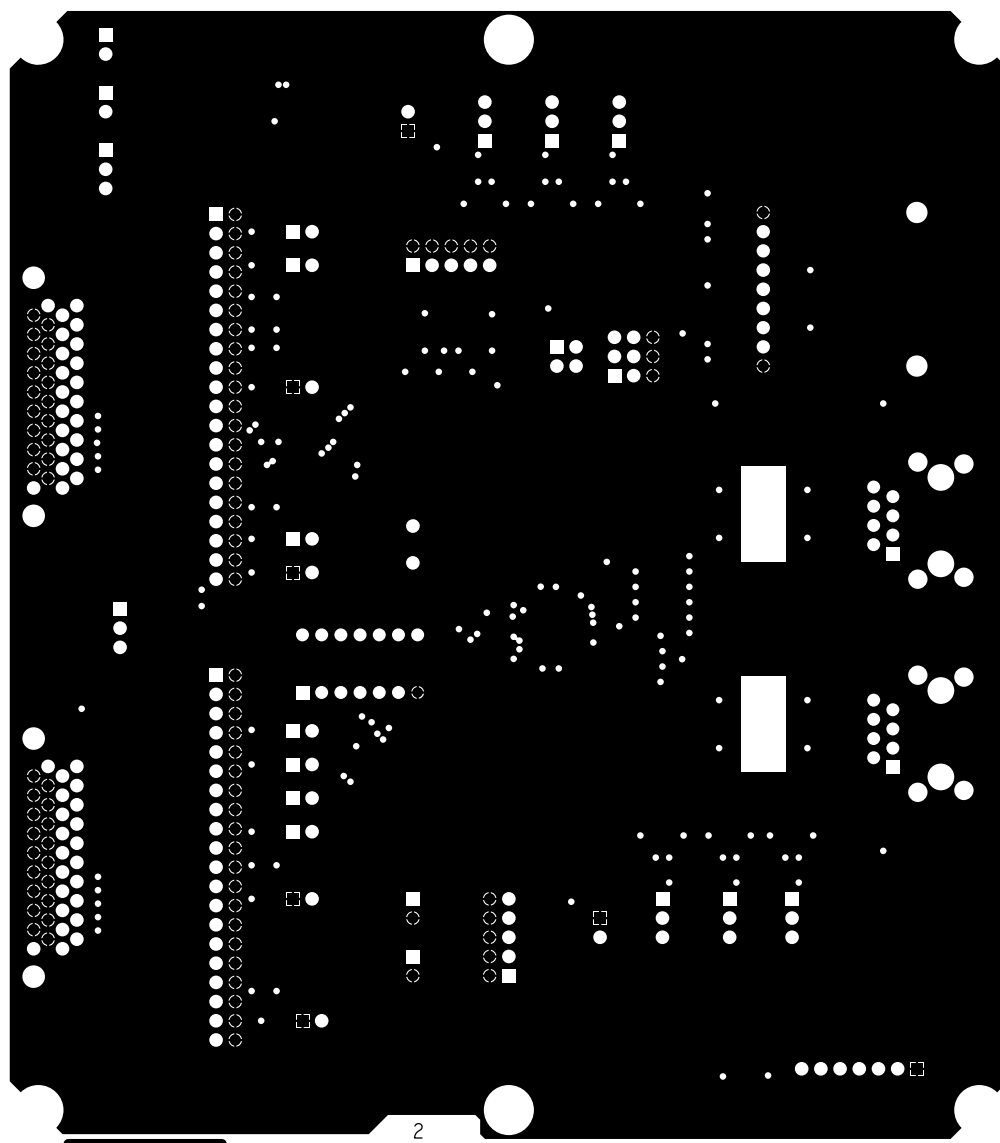

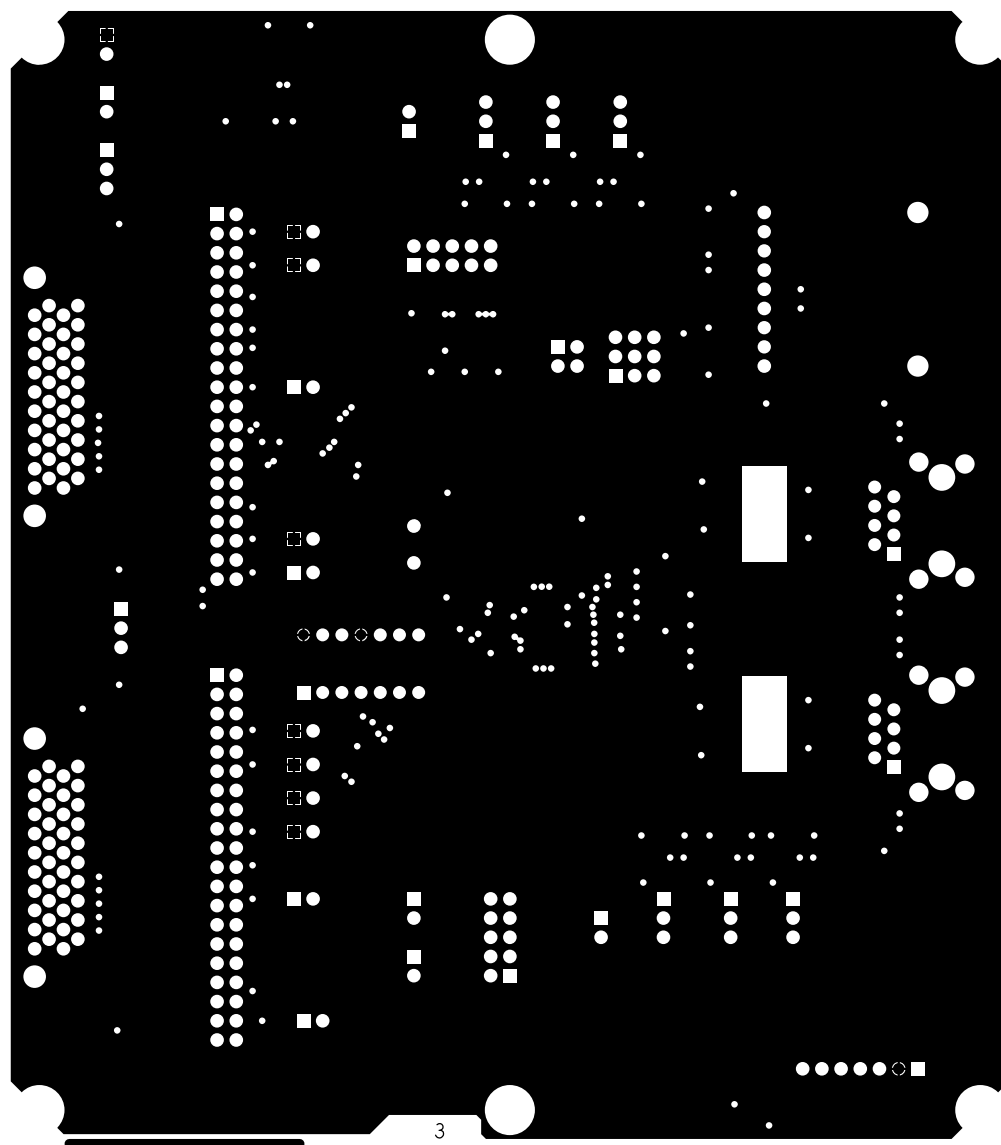



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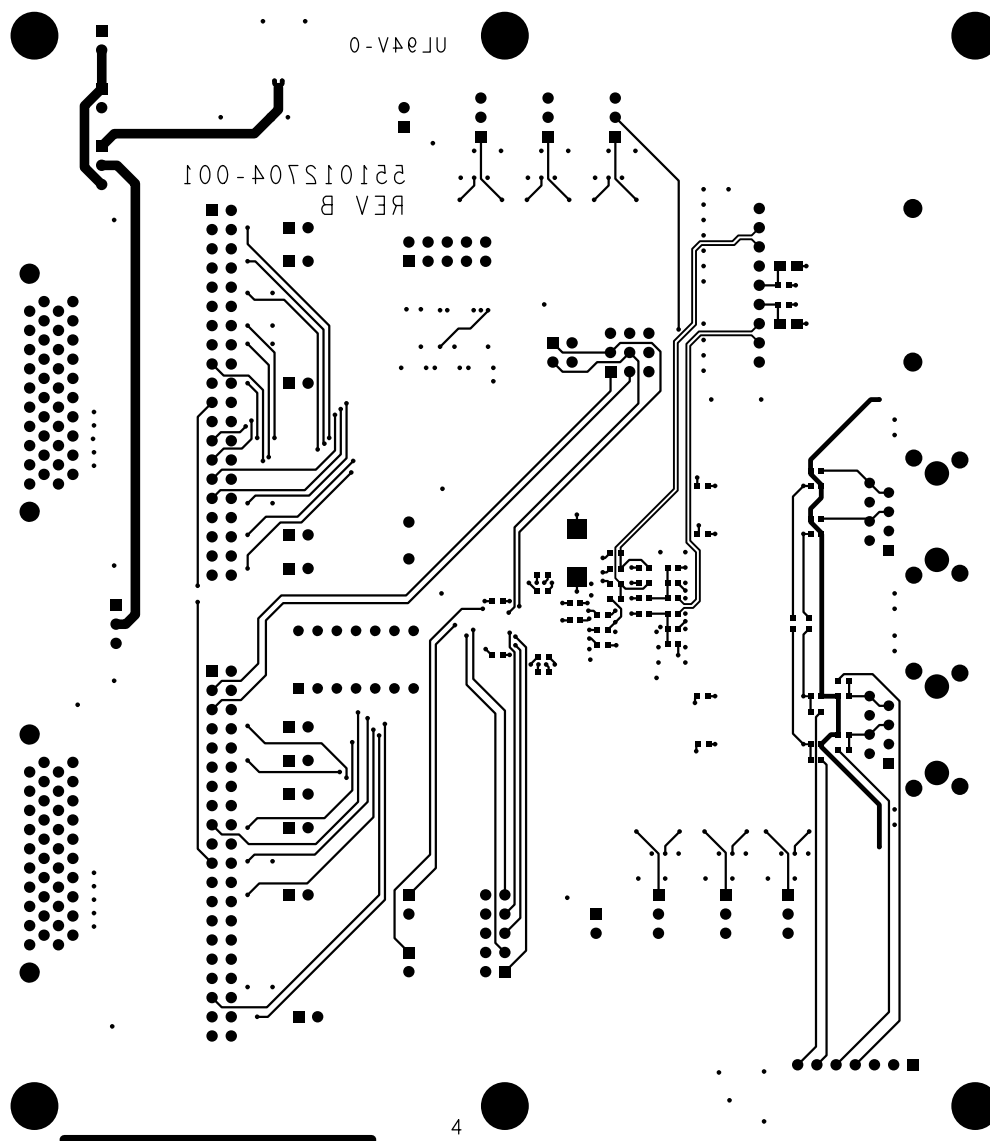
 National Semiconductor™ INTERFACE DIVISION	ALL ARTWORK VIEWED FROM TOP SIDE		DATE: DECEMBER 16, 2005	
	BOARD NAME: DP83849 DUAL PHYTER DEMO II		BOARD NO: 880012704-001	REV B
	LAYER NAME: TOP ETCH		LAYER NO: 1	SHEET 1 of 8



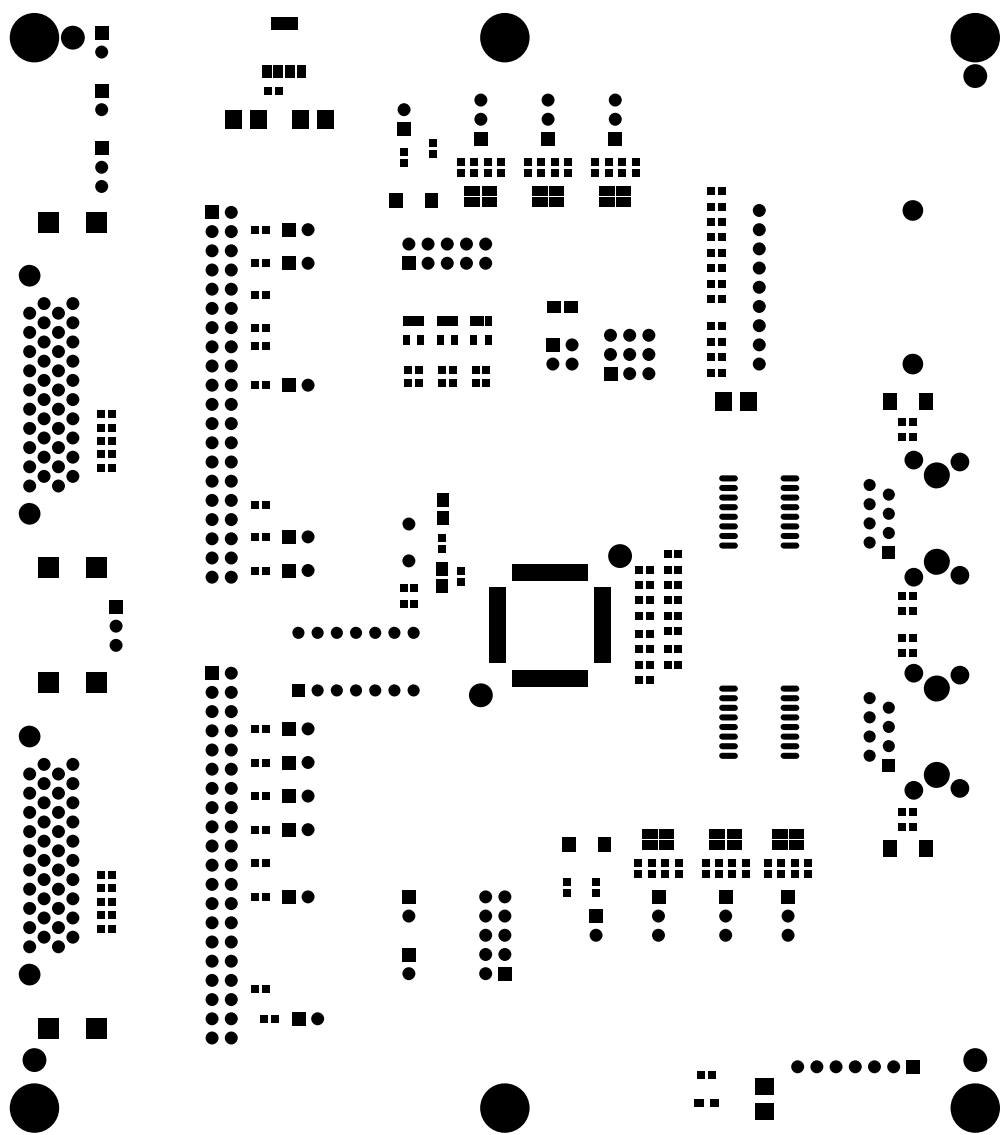
 National Semiconductor™ INTERFACE DIVISION	ALL ARTWORK VIEWED FROM TOP SIDE		DATE: DECEMBER 16, 2005	
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	LAYER NAME: GND		LAYER NO: 2	SHEET 2 of 8




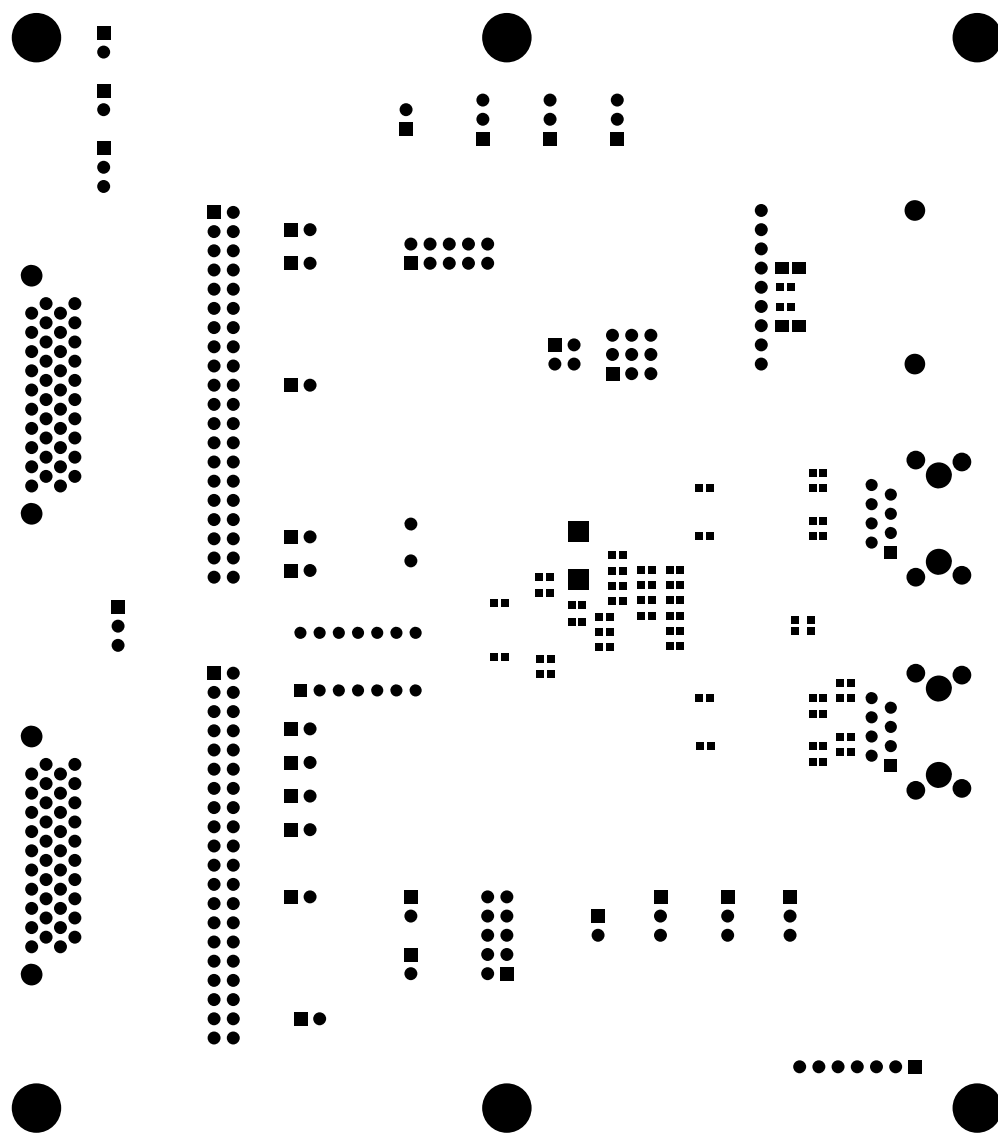
 National Semiconductor™ INTERFACE DIVISION	ALL ARTWORK VIEWED FROM TOP SIDE		DATE: DECEMBER 16, 2005	
	BOARD NAME: DP83849 DUAL PHYTER DEMO II		BOARD NO: 880012704-001	REV B
	LAYER NAME: 3V3		LAYER NO: 3	SHEET 3 of 8




 National Semiconductor™ INTERFACE DIVISION	ALL ARTWORK VIEWED FROM TOP SIDE		DATE: DECEMBER 16, 2005	
	BOARD NAME: DP83849 DUAL PHYTER DEMO II		BOARD NO: 880012704-001	REV B
	LAYER NAME: BOTTOM ETCH		LAYER NO: 4	SHEET 4 of 8



 National Semiconductor™ INTERFACE DIVISION	ALL ARTWORK VIEWED FROM TOP SIDE		DATE: DECEMBER 16, 2005	
	BOARD NAME: DP83849 DUAL PHYTER DEMO II		BOARD NO: 880012704-001	REV B
	LAYER NAME: TOP SOLDER MASK		LAYER NO:	SHEET 5 of 8



 National Semiconductor™ INTERFACE DIVISION	ALL ARTWORK VIEWED FROM TOP SIDE		DATE: DECEMBER 16, 2005	
	BOARD NAME: DP83849 DUAL PHYTER DEMO II		BOARD NO: 880012704-001	REV B
	LAYER NAME: BOTTOM SOLDER MASK		LAYER NO:	SHEET 6 of 8

