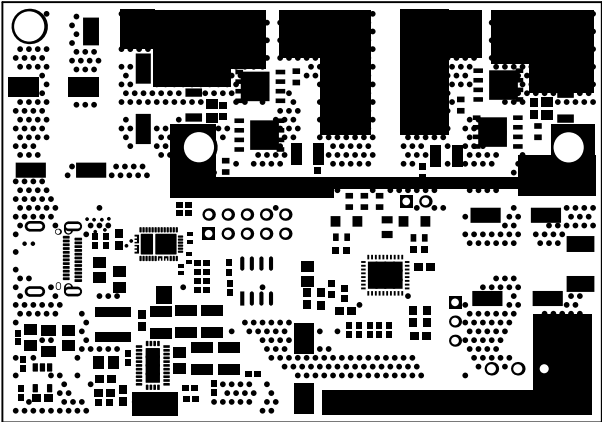
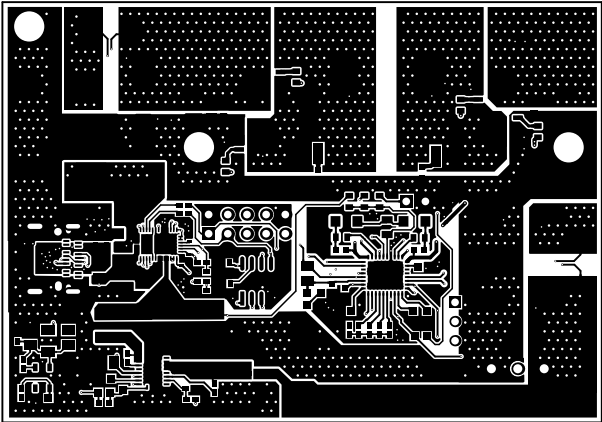


ALL MEASUREMENTS FROM TOP SURFACE		DATE: 10/10/2020	BY: J. C.	SEALED BY: J. C.	NOT TO SCALE
LAND AREA: 1.00	Top Surface				
WATER AREA: 1.00	Top Surface	WATER AREA: 1.00	WATER AREA: 1.00	WATER AREA: 1.00	WATER AREA: 1.00



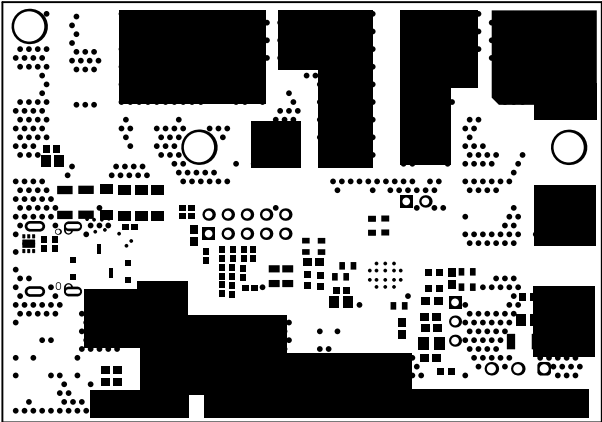
ALL RIGHTS RESERVED FROM THE DATE	DATE OF PRODUCTION	FILE	CD	FILE	NOT IN ORIGINAL FORMAT
LAST NAME - THE NAME					
FIRST NAME - THE NAME NAME	REPRODUCTION - 100% COPY			TOTAL REPRODUCTION	



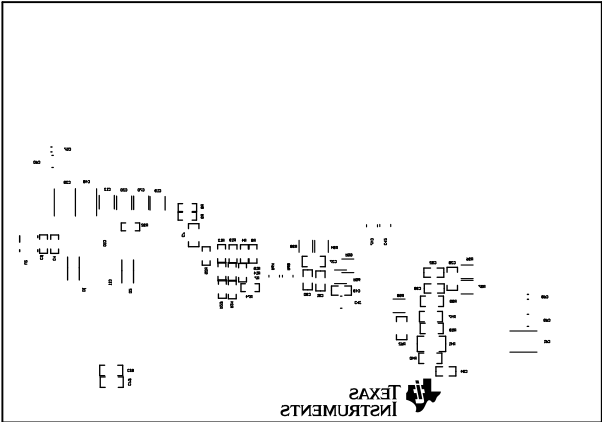
Layer	Name	Material	Thickness	Constant
	Top Overlay			
	Top Solder	Solder Resist	0.010mm	3.5
1	Top Layer		0.036mm	
	Dielectric 1	FR-4 High Tg	0.203mm	4.2
2	Signal Layer 1		0.036mm	
	Dielectric 2	FR-4 High Tg	1.016mm	4.2
3	Signal Layer 2		0.036mm	
	Dielectric 3	FR-4 High Tg	0.203mm	4.2
4	Bottom Layer		0.036mm	
	Bottom Solder	Solder Resist	0.010mm	3.5
	Bottom Overlay			

Total board thickness: 1.585mm

ALL DIMENSIONS UNLESS NOTED OTHERWISE	DESIGN BY: [REDACTED]	DATE: [REDACTED]	REV: [REDACTED]	DATE: [REDACTED]	DESIGN BY: [REDACTED]
LAYER 1: [REDACTED]	Top Layer				
LAYER 2: [REDACTED]	Top Layer	DESIGN BY: [REDACTED]	DATE: [REDACTED]	DESIGN BY: [REDACTED]	DATE: [REDACTED]



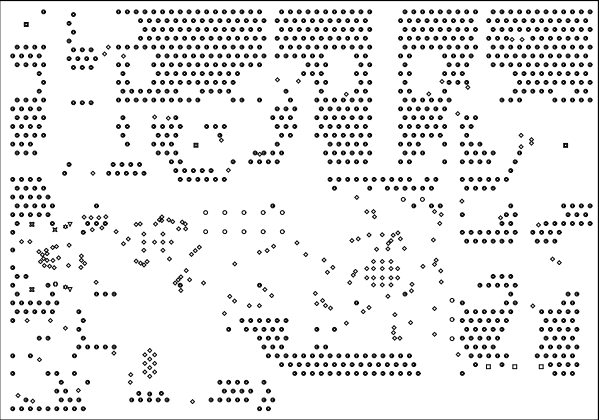
ALL EMPLOYEES SHOULD FOLLOW THE RULES	NAME	POSITION	SEX	AGE	DATE	TIME
NAME	POSITION					
NAME	POSITION	DATE	TIME	DATE	TIME	



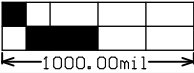
ALL RIGHTS RESERVED FOR THE SITE	NAME *	PHONE *	SEX *	AGE *	DATE *	NOT TO BE WRITTEN ON
LAST NAME *	FIRST NAME *					
PLAT NAME *	ADDRESS *			STATE *	CITY *	

Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
◇	196	0.200mm (7.87mil)	PTH	Round	Top Layer - Bottom Layer	
⦿	999	0.330mm (13.00mil)	PTH	Round	Top Layer - Bottom Layer	
⊗	1	0.500mm (19.69mil)	NPTH	Slot	Top Layer - Bottom Layer	+/-0.030mm
⊗	1	0.600mm (23.62mil)	NPTH	Round	Top Layer - Bottom Layer	+/-0.030mm
▽	2	0.600mm (23.62mil)	PTH	Slot	Top Layer - Bottom Layer	+/-0.050mm
⊗	2	0.700mm (27.56mil)	PTH	Slot	Top Layer - Bottom Layer	+/-0.050mm
⊗	2	0.700mm (27.56mil)	PTH	Slot	Top Layer - Bottom Layer	+/-0.050mm
○	15	1.016mm (40.00mil)	PTH	Round	Top Layer - Bottom Layer	
□	3	1.200mm (47.24mil)	PTH	Round	Top Layer - Bottom Layer	
■	3	4.000mm (157.48mil)	PTH	Round	Top Layer - Bottom Layer	
	1224 Total					

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size + Slot length as defined in the PCB layout



ALL DIMENSIONS UNLESS NOTED OTHERWISE	DATE: 10/10/2024	REV: 02	DESIGN: 10/10/2024
DESIGN: 10/10/2024			
DATE: 10/10/2024	DESIGN: 10/10/2024	DESIGN: 10/10/2024	DESIGN: 10/10/2024



ALL MEASUREMENTS MUST BE MADE		DATE	TIME	BY	NO.	DATE	TIME	BY	NO.
LAYER NO.		REMARKS							
LAYER NO.		REMARKS				REMARKS			