

ARM MPU Tamper Test Board

- See the Hardware User Guide for board details
- See the Errata Document for board issues
- See the PCB Build Specification for PCB Details

ARM MPU

Texas Instruments, Inc.

ARM MPU Business Unit
12500 TI Blvd
Dallas, TX 75243

TEXAS
INSTRUMENTS

Final

AM437x Tamper Test Board

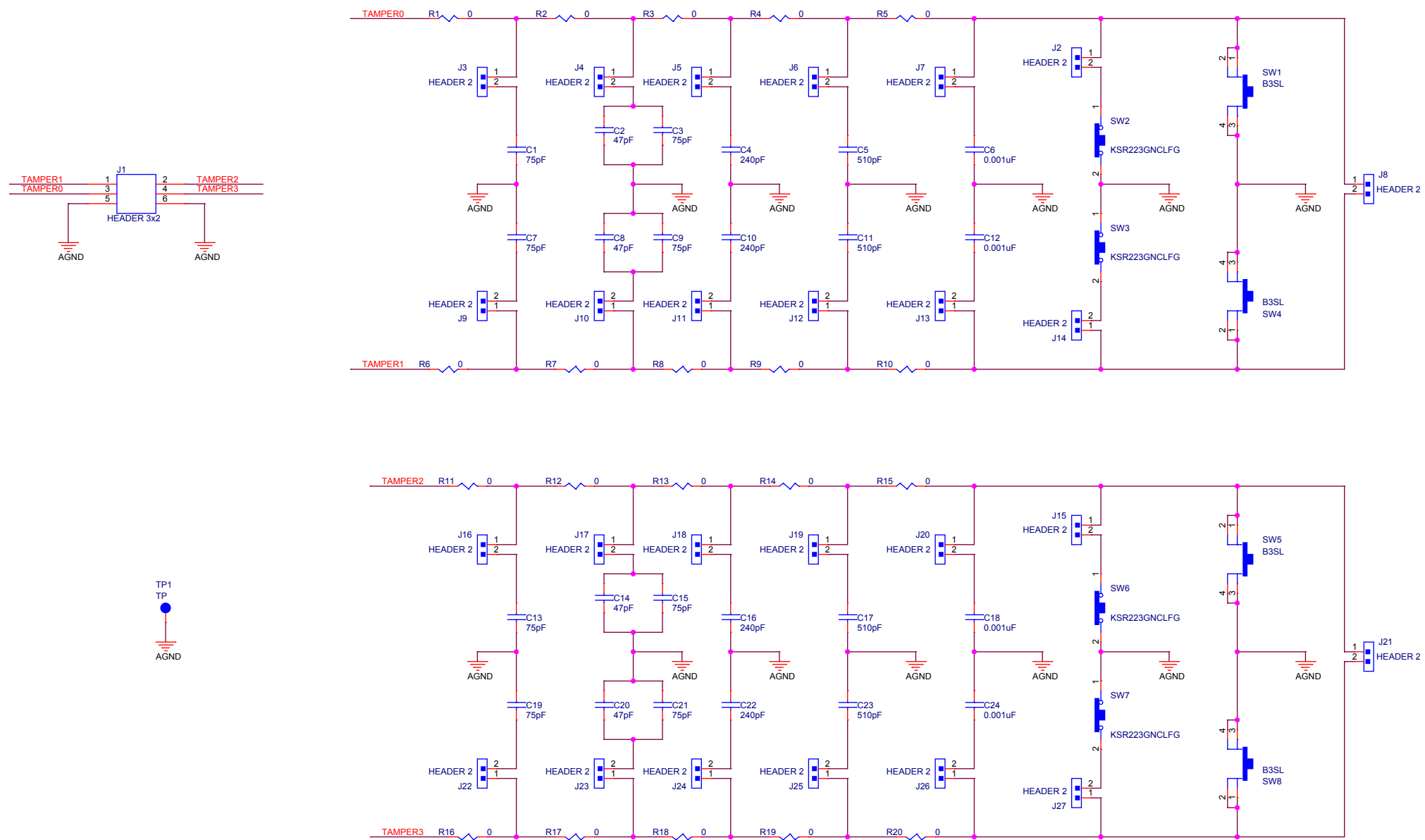
Part of TMDXEVM438X

Title Page

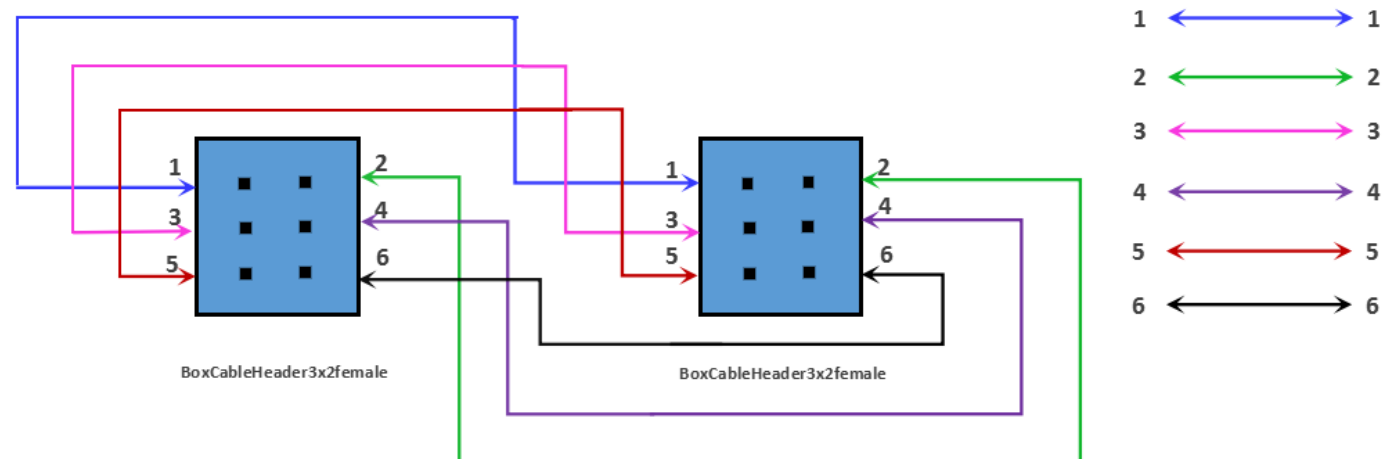
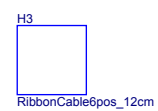
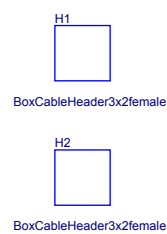
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TAMPER BOARD



TAMPER CABLE CONNECTION



HARDWARE SCHEMATICS

LOGOs & LABELs

PCB
LOGO

Texas Instruments

PCB
LOGO

RoHS Exempt

PCB
LOGO

For Evaluation only; not FCC approved for resale

PCB1

AM437x Tamper Board Bare PCB

CBL1

TamperBoardCable

Board Serial No.

PCB Label

Size: 0.7" x 0.2"

LBL1

Label Assembly Note

ZZ1

The boards and components must be baked before assembly

Label Assembly Note

ZZ2

Provide serial numbers to the assembled boards for identification

Label Assembly Note

ZZ3

Please carry out the cold points check verification and provide the report for each assembled board

Label Assembly Note

ZZ4

The assembled board are wrapped in ESD Covers(individual) and packed securely before shipment.

Label Assembly Note

ZZ5

All MSL components should be baked as per JEDEC standard

Label Assembly Note

ZZ6

PCB should be baked at 120 degree for 8 hours

Label Assembly Note

ZZ7

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Label Assembly Note

ZZ8

These assemblies are ESD sensitive, ESD precautions shall be observed.

Label Assembly Note

ZZ9

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

MOUNTING HOLES

MH1

MH2

MH3

MH4

FIDUCIALs

FID1

FID2

FID3

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Hardware Schematics

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