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Orderable: LM5180EVM-DUAL	Designed for: Public Release	Mod. Date: 2/22/2019
TID #: N/A	Project Title: LM5180EVM-DUAL	
Number: BSR052	Rev: A	Sheet Title: LM5180EVM-DUAL SCHEMATIC
SVN Rev: 425	Assembly Variant: 001	Sheet: 1 of 2
Drawn By: K Nielson	File: BSR052A.SchDoc	Size: B
Engineer: T Hegarty	Contact: http://www.ti.com/support	

H1
NY PMS 440 0025 PH

H2
NY PMS 440 0025 PH

H3
NY PMS 440 0025 PH

H4
NY PMS 440 0025 PH

H5

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H8

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FID1 FID2 FID3 FID4 FID5 FID6

PCB Number: BSR052
PCB Rev: A

Logo1
PCB
LOGO
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LOGO2

Logo3
PCB
LOGO
FCC disclaimer

Logo4
PCB
LOGO
WEEE logo

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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TID #: N/A	Project Title: LM5180EVM-DUAL	
Number: BSR052	Rev: A	Sheet Title: LM5180EVM-DUAL HARDWARE
SVN Rev: 422	Assembly Variant: 001	Sheet: 3 of 3
Drawn By: K Nielson	File: BSR052A_Hardware_SchDoc	Size: B
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