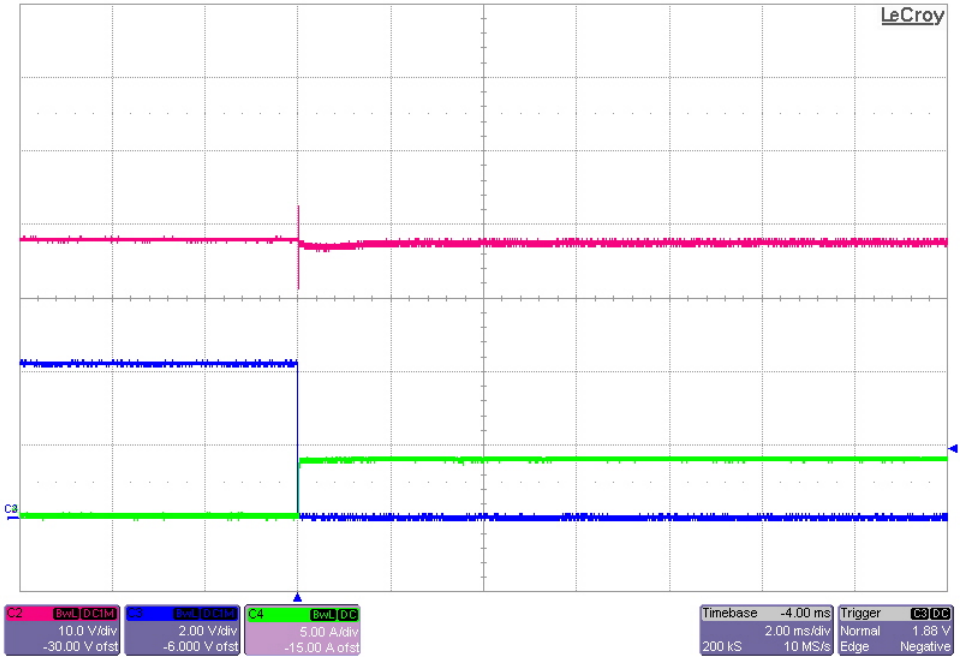
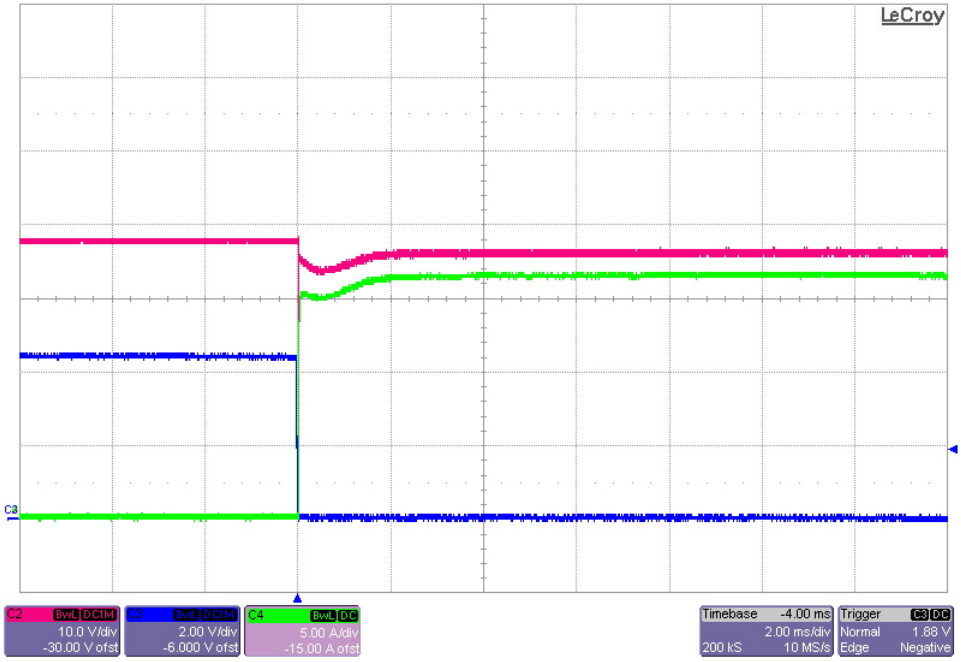
1 Enable with DC voltage applied

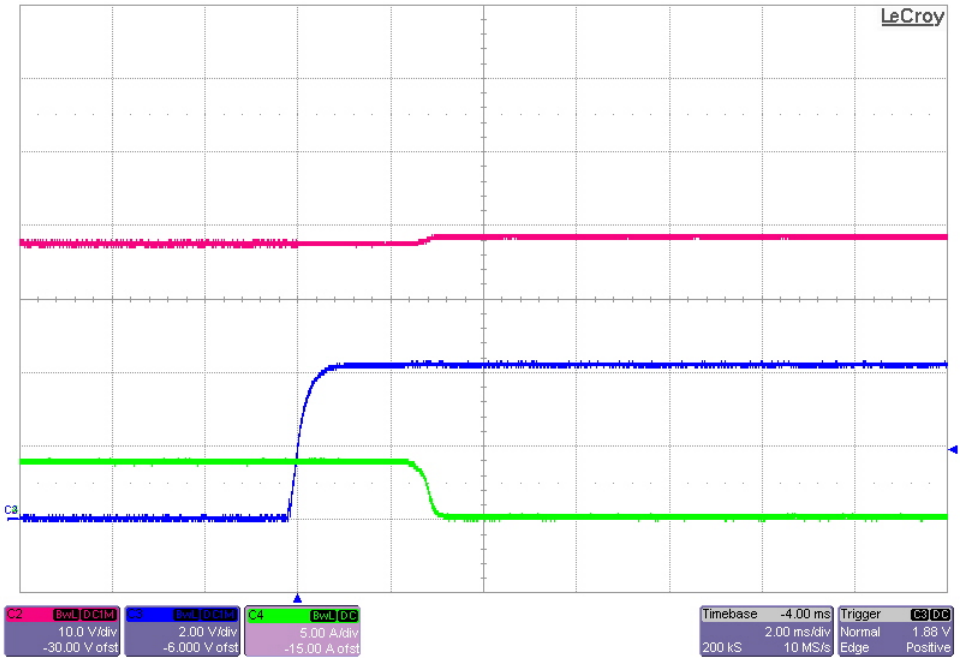
The photo below shows the output current (Green) and DC input voltage (Red) after the ENABLE signal is grounded (Blue). The input voltage was set to 38Vdc and the load was 4Adc.
(Vin is 10V/DIV, EN is 2V/DIV, Iout is 5A/DIV, 2mS/DIV)



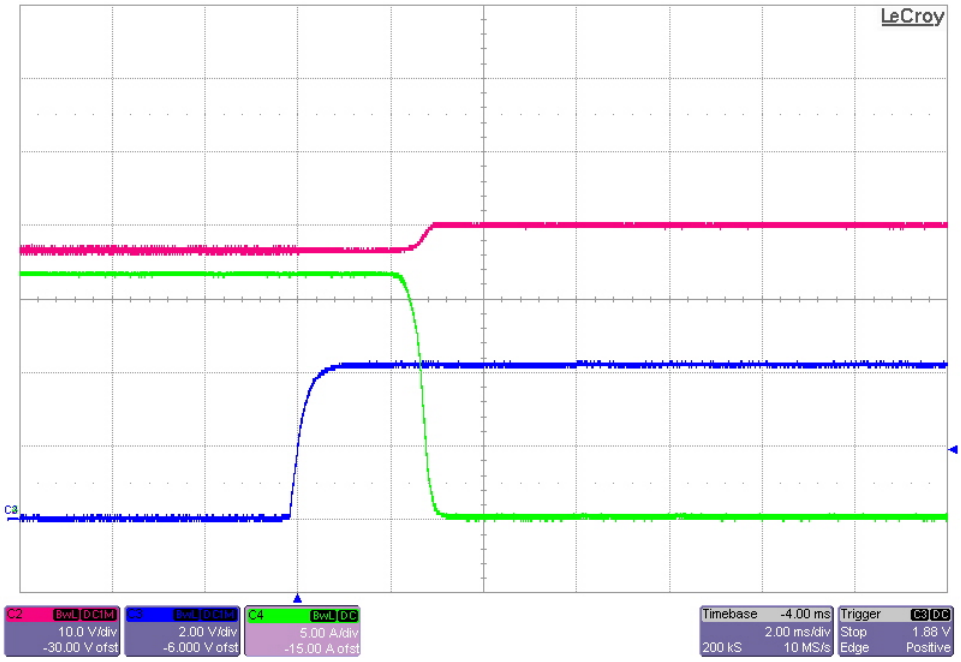
The photo below shows the output current (Green) and DC input voltage (Red) after the ENABLE signal is grounded (Blue). The input voltage was set to 38Vdc and the load was 16.5Adc.
(Vin is 10V/DIV, EN is 2V/DIV, Iout is 5A/DIV, 2mS/DIV)



The photo below shows the output current (Green) and DC input voltage (Red) after the ENABLE signal is opened (Blue). The input voltage was set to 38Vdc and the load was 4Adc.
(Vin is 10V/DIV, EN is 2V/DIV, Iout is 5A/DIV, 2mS/DIV)

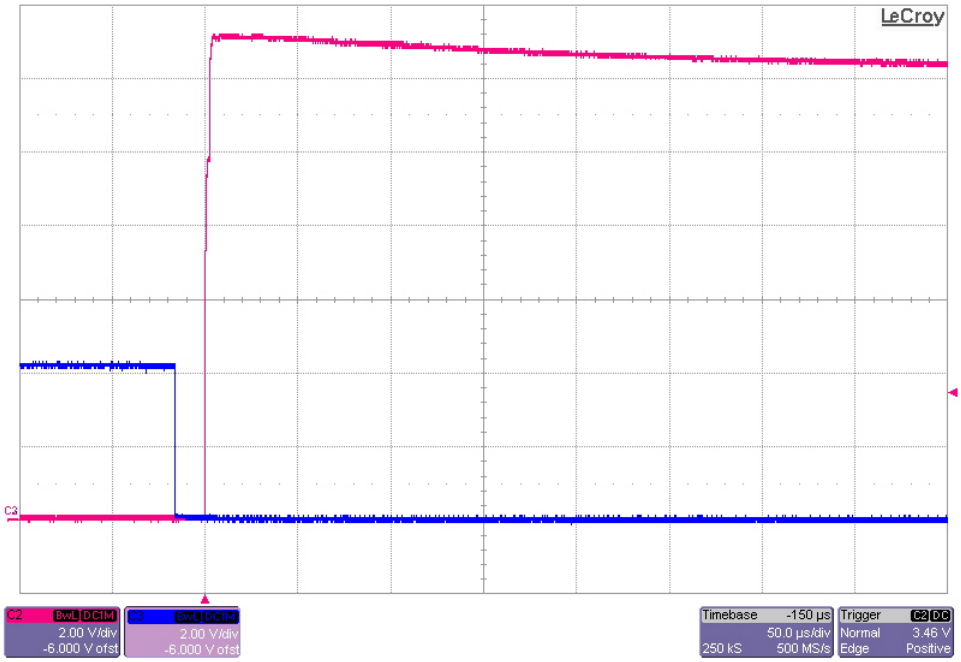


The photo below shows the output current (Green) and DC input voltage (Red) after the ENABLE signal is opened (Blue). The input voltage was set to 38Vdc and the load was 16.5Adc.
(Vin is 10V/DIV, EN is 2V/DIV, Iout is 5A/DIV, 2mS/DIV)

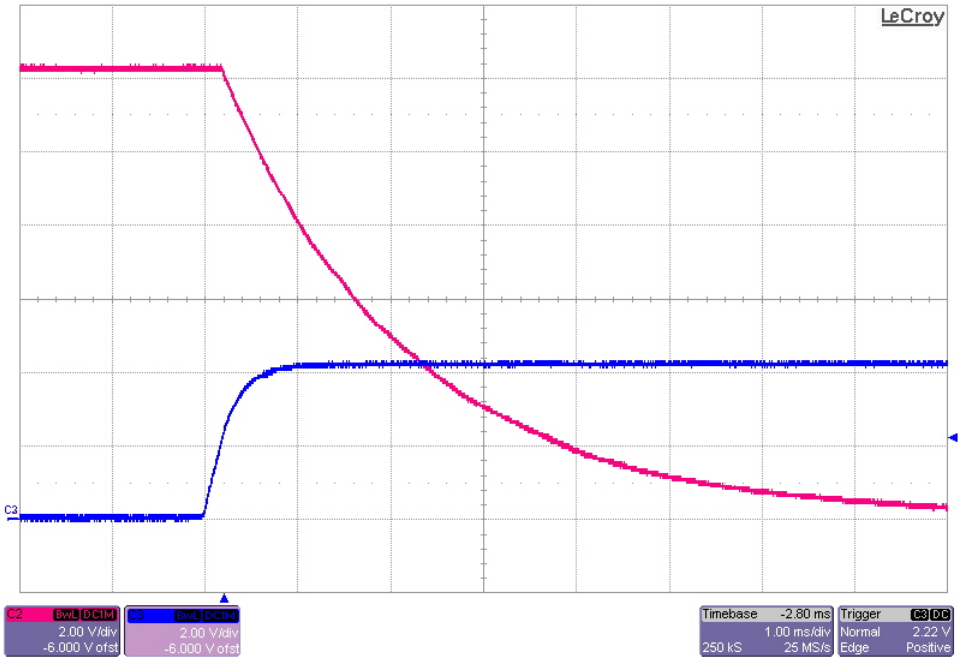


2 Enable to Vgs delay

The photo below shows the FET gate-to-source voltage (Red) after the ENABLE signal is grounded (Blue). The input bias voltage was set to 12Vdc. (2V/DIV, 50uS/DIV)

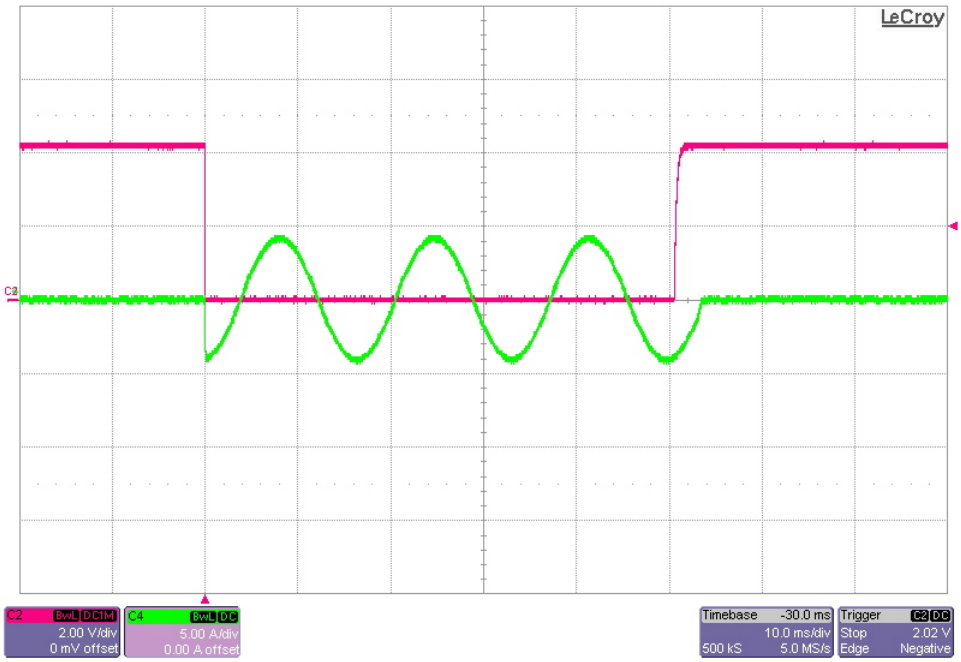


The photo below shows the FET gate-to-source voltage (Red) after the ENABLE signal is opened (Blue). The input bias voltage was set to 12Vdc. (2V/DIV, 1mS/DIV)

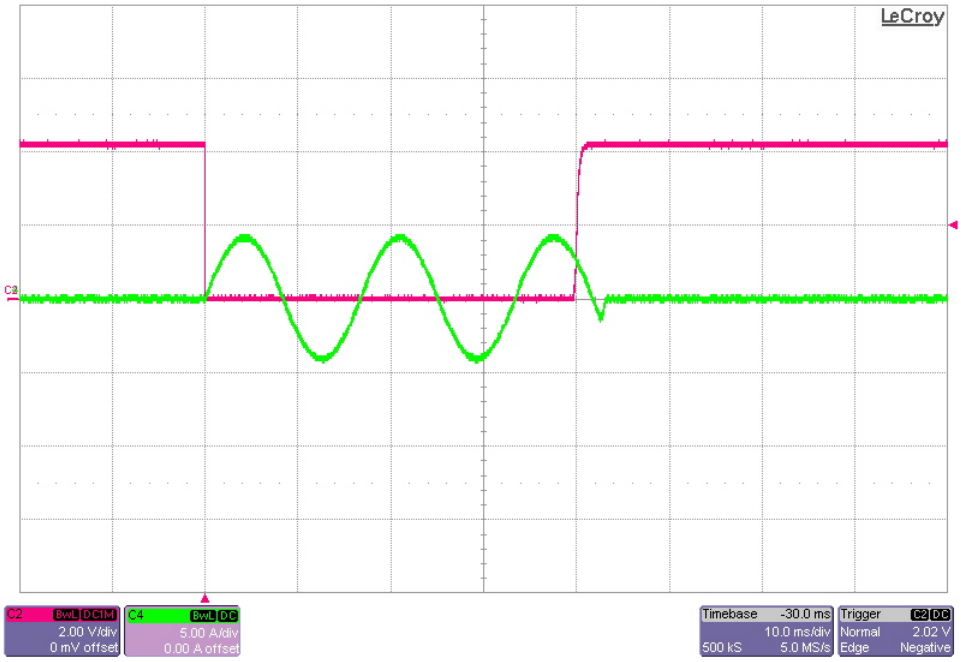


3 Enable with AC voltage applied

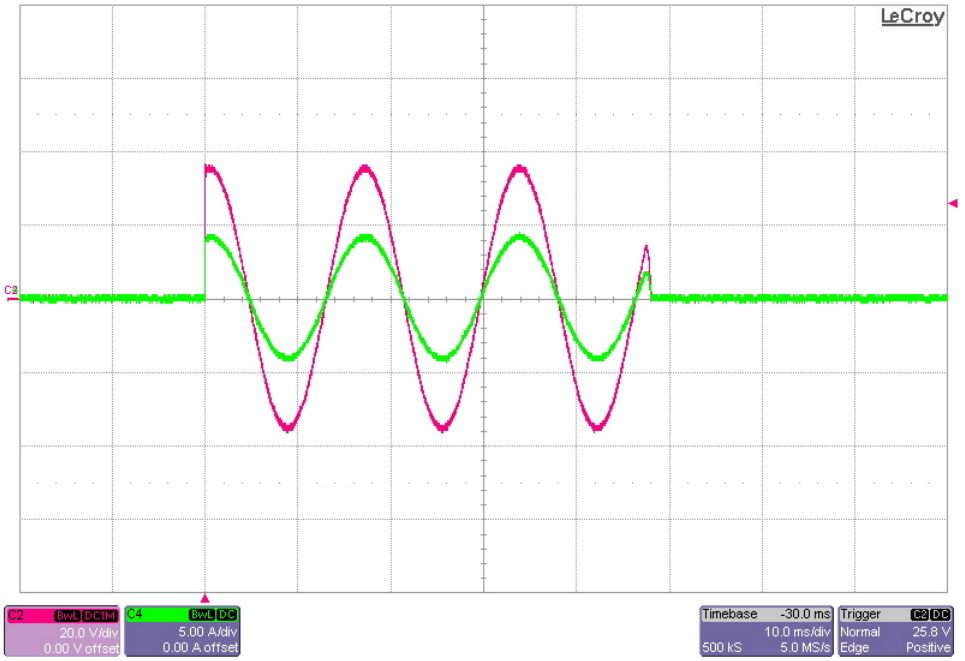
The photo below shows the AC output current (Green) after the ENABLE signal (Red) transitions on and off. The input voltage was set to 26Vac and the load was 3Arms.
(2V/DIV, 5A/DIV, 10mS/DIV)



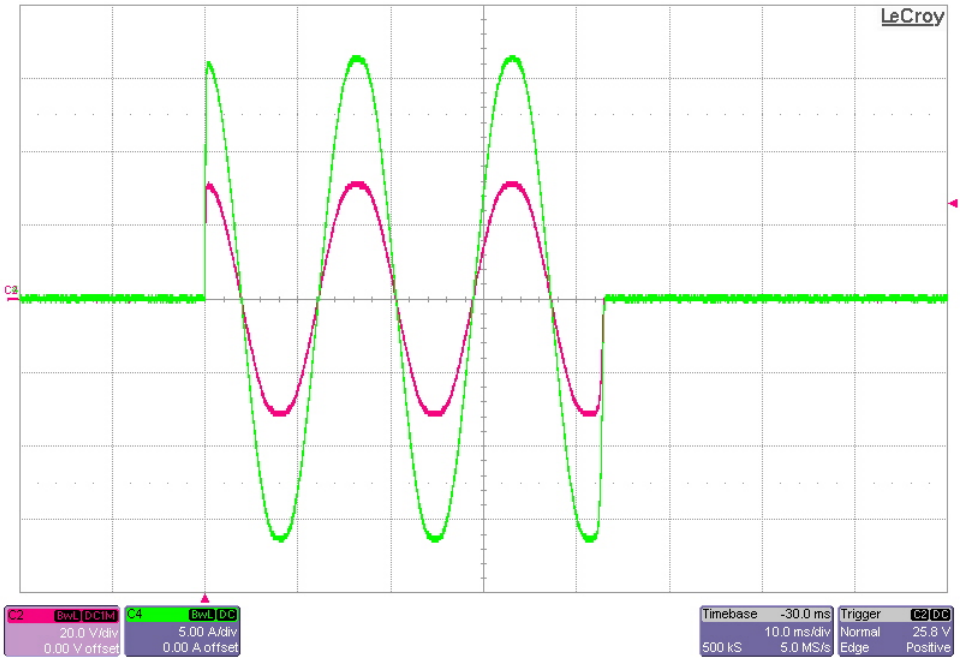
The photo below shows the AC output current (Green) after the ENABLE signal (Red) transitions on and off. The input voltage was set to 26Vac and the load was 3Arms.
(2V/DIV, 5A/DIV, 10mS/DIV)



The photo below shows the AC output current (Green) and the output voltage (Red) as the ENABLE signal is transitioned on and off. The input voltage was set to 26Vac and the load was 3Arms.
(20V/DIV, 5A/DIV, 10mS/DIV)

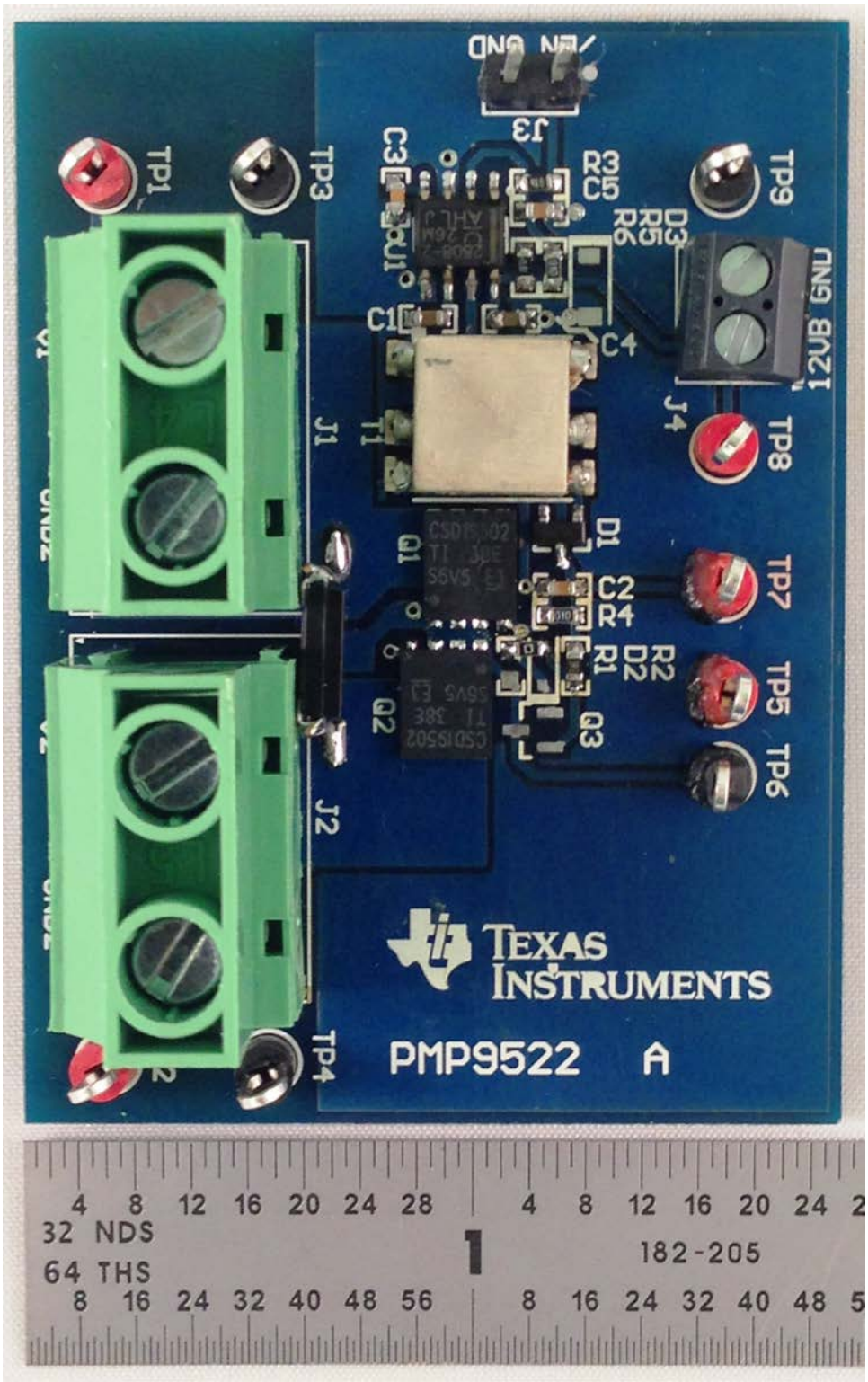


The photo below shows the AC output current (Green) and the output voltage (Red) as the ENABLE signal is transitioned on and off. The input voltage was set to 26Vac and the load was set to 16.5Apk.
(20V/DIV, 5A/DIV, 10mS/DIV)



4 Photo

The photo below shows the PMP9522 REVB assy (with mods).



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