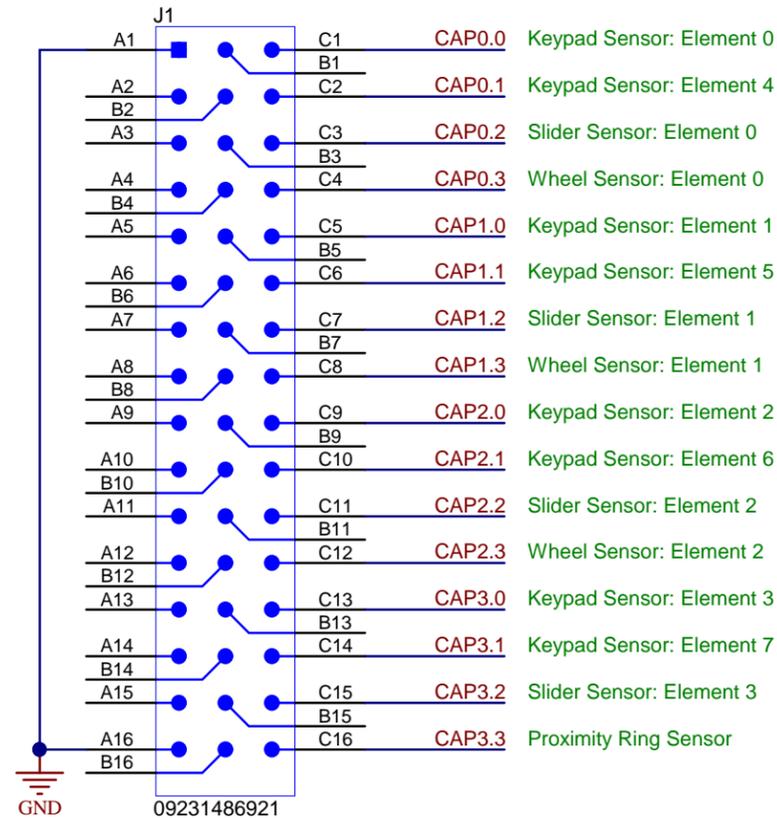


## Captivate Development Kit: Processor PCB to Panel PCB Connector



### Hardware



### Logos



ZZ1

**Assembly Note**

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ2

**Assembly Note**

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ3

**Assembly Note**

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

ZZ4

**Assembly Note**

Overlay will be installed during final test

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Orderable: <a href="#">CAPTIVATE-BSWP</a>	Designed for: <a href="#">Public Release</a>	Mod. Date: 8/27/2018
TID #: <a href="#">N/A</a>	Project Title: <a href="#">CAPTIVATE-BSWP</a>	
Number: <a href="#">MCU031</a>	Rev: <a href="#">C</a>	Sheet Title: <a href="#">TICAPEVM_BSWP Capacitive Touch Panel PCB</a>
SVN Rev: <a href="#">Version control disabled</a>	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">1 of 1</a>
Drawn By: <a href="#">W. Schnoor</a>	File: <a href="#">MCU031C_CAPTIVATE-BSWP.SchDoc</a>	Size: Letter
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