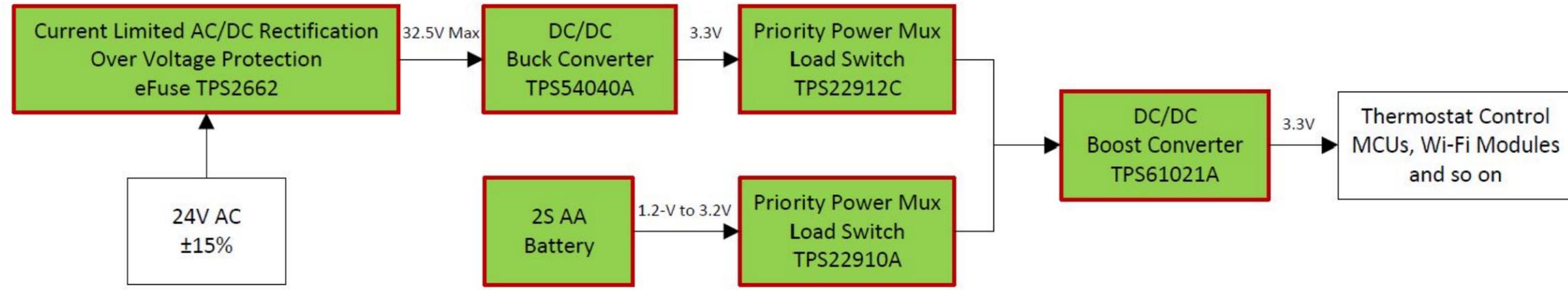


Revision History

Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

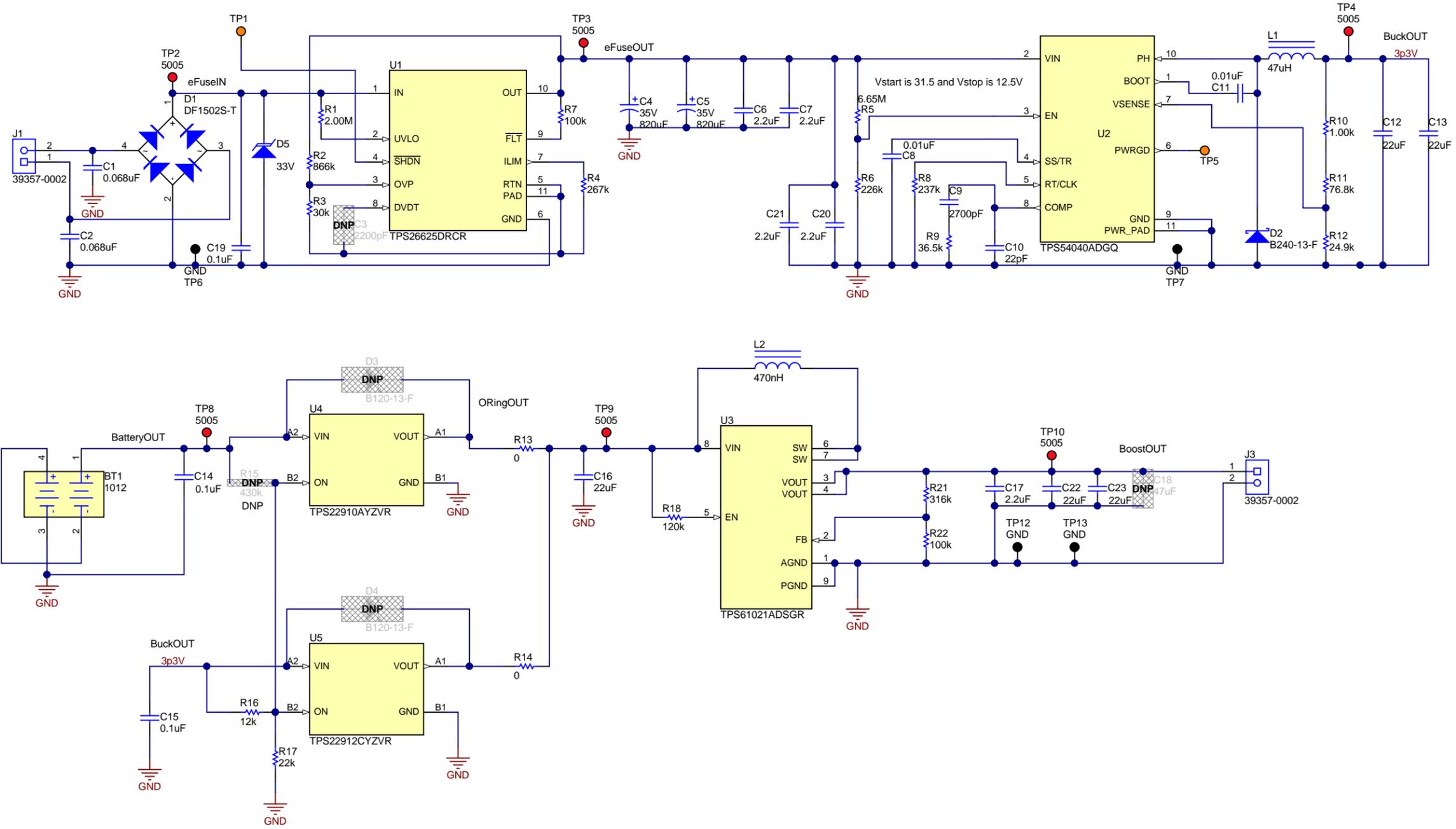


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TID #: TIDA-01574	Project Title: Power Stage Reference Design for Power Stealing	
Number: TIDA-01574	Rev: E1	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 1 of 3
Drawn By:	File: TIDA-01574_CoverSheet.SchDoc	Size: B
Engineer: Karikalan Selvaraj	Contact: http://www.ti.com/support	



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Drawn By:	File: TIDA-01574.SchDoc	Size: B
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H1 SJ-5303 (CLEAR) H2 SJ-5303 (CLEAR) H3 SJ-5303 (CLEAR) H4 SJ-5303 (CLEAR)

DNP FID1 DNP FID2 DNP FID3

PCB Number: TIDA-01574
PCB Rev: E1

PCB LOGO Pb-Free Symbol PCB LOGO FCC disclaimer

ZZ2 Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3 Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4 Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable:	Designed for: Public Release	Mod. Date: 12/15/2017
TID #: TIDA-01574	Project Title: Power Stage Reference Design for Power Stealing	
Number: TIDA-01574	Rev: E1	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 3 of 3
Drawn By:	File: TIDA-01574_Hardware.SchDoc	Size: B
Engineer: Karikalan Selvaraj	Contact: http://www.ti.com/support	

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