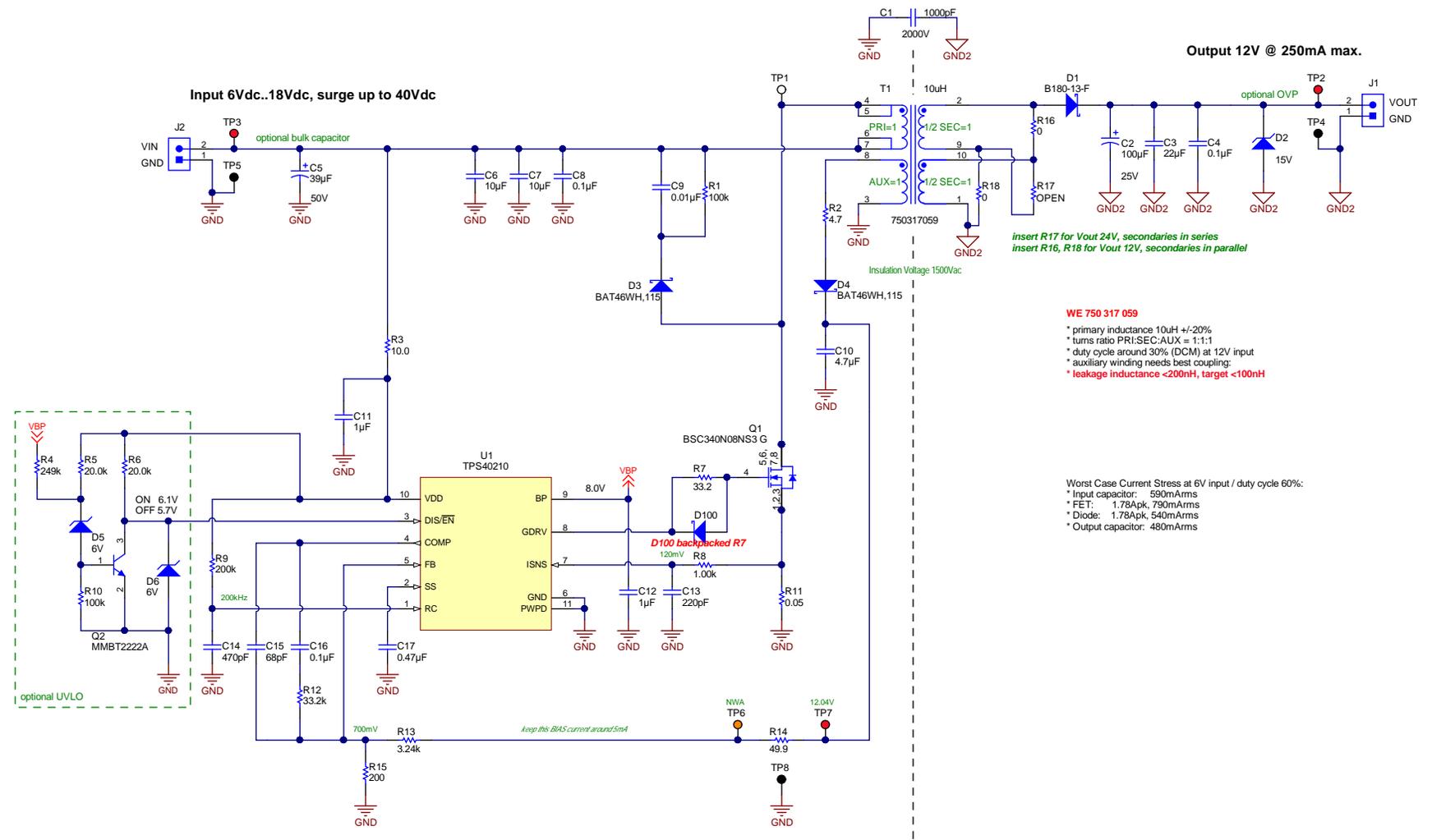


Revision History	
Revision	Notes
A	*** built on PCB 30308 A ***
B	*** adjusted Vout, adjusted UVLO

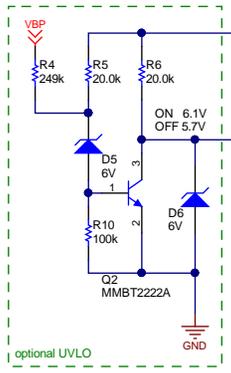


**WE 750 317 059**

- \* primary inductance 10µH +/-20%
- \* turns ratio PRI:SEC:AUX = 1:1:1
- \* duty cycle around 30% (DCM) at 12V input
- \* auxiliary winding needs best coupling
- \* leakage inductance <200nH, target <100nH

Worst Case Current Stress at 6V input / duty cycle 60%:

- \* Input capacitor: 590mArms
- \* FET: 1.78Apk, 790mA rms
- \* Diode: 1.78Apk, 540mA rms
- \* Output capacitor: 480mA rms



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Orderable: EVM orderable	Designed for: Public Release	Mod. Date: 10/13/2017
TID #: TID	Project Title: Isolated 3W PSR DCM Flyback	
Number: PMP30336	Rev: B	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: [No Variations]	Sheet: 1 of 1
Drawn By:	File: PMP30336RevB.SchDoc	Size: A3
Engineer: B. Geck	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	<a href="http://www.ti.com">http://www.ti.com</a>



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