

Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

A

A

B

B

C

C

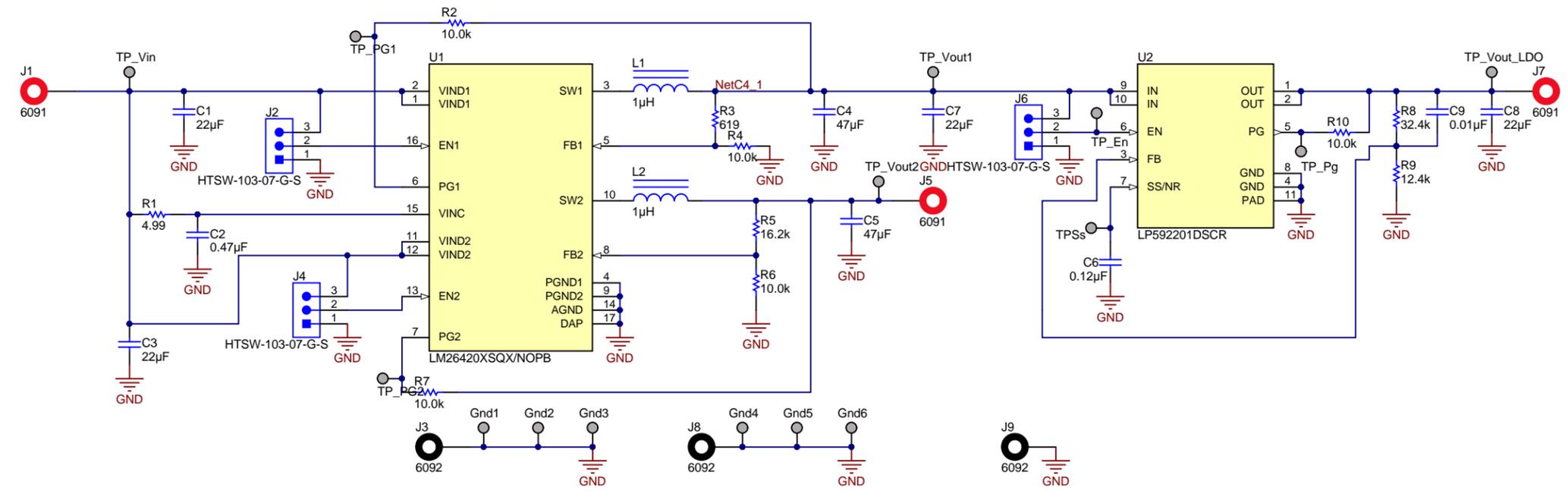
D

D

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Orderable:	Designed for: <b>Public Release</b>	Mod. Date: 16.3.2017
TID #: <b>N/A</b>	Project Title: <b>TIDA-01322</b>	
Number: <b>TIDA-01322</b>	Rev: <b>E1</b>	Sheet: <b>1</b> of <b>3</b>
SVN Rev: <b>Not in version control</b>	Assembly Variant: <b>001</b>	Size: <b>B</b>
Drawn By:	File: <b>LP5922_Design_CoverSheet.SchDoc</b>	http://www.ti.com
Engineer: <b>Jari Niemelä</b>	Contact: <b>http://www.ti.com/support</b>	© Texas Instruments 2016





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Orderable:	Designed for: Public Release	Mod. Date: 16.3.2017
TID #: N/A	Project Title: TIDA-01322	
Number: TIDA-01322	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 3
Drawn By:	File: LP5922_Design_BlankSheet.SchDoc	Size: B
Engineer: Jari Niemelä	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	





PCB Number: TIDA-01322  
PCB Rev: E1

PCB LOGO  
Pb-Free Symbol

PCB LOGO  
FCC disclaimer

Variant/Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

LBL1  
PCB Label  
Size: 0.65" x 0.20"

ZZ1  
Label Assembly Note  
This Assembly Note is for PCB labels only

ZZ2  
Assembly Note  
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3  
Assembly Note  
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4  
Assembly Note  
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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TID #: N/A	Project Title: TIDA-01322		
Number: TIDA-01322	Rev: E1	Sheet: 3 of 3	
SVN Rev: Not in version control	Assembly Variant: 001	File: LP5922_Design_EVM_Hardware.SchDoc	
Drawn By: Jari Niemelä	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	Size: B	

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