



PCB Number: TIDA-01230  
PCB Rev: A

PCB  
LOGO  
TI\_Logo

PCB  
LOGO  
Pb-Free Symbol

ZZ2  
**Assembly Note**  
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3  
**Assembly Note**  
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4  
**Assembly Note**  
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: ISOW7842RS232EVM	Designed for: Public Release	Mod. Date: 12/21/2016	 <a href="http://www.ti.com">http://www.ti.com</a> © Texas Instruments 2015
TID #: TIDA-01230	Project Title: TIDA-01230		
Number: TIDA-01230	Rev: A	Sheet Title: Hardware Sheet	
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 3 of 3	
Drawn By: Anand Reghunathan	File: TIDA-01230_Hardware.SchDoc	Size: B	
Engineer: Anand Reghunathan	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>		

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