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Orderable: TPS63070EVM-693	Designed for: Public Release	Mod. Date: 5/17/2016
TID #: N/A	Project Title: TPS63070	
Number: PMP9796	Rev: A	Sheet: 2 of 3
SVN Rev: Version control disabled	Assembly Variant: 001	Size: B
Drawn By:	File: PMP9796_A.SchDoc	
Engineer: Michael Helminger	Contact: http://www.ti.com/support	





PCB Number: PMP9796
PCB Rev: A

PCB LOGO
Texas Instruments

PCB LOGO
Pb-Free Symbol

PCB LOGO
FCC disclaimer

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Number: PMP9796	Rev: A	Sheet: 3 of 3	
SVN Rev: Version control disabled	Assembly Variant: 001	Size: B	
Drawn By: Michael Helminger	File: PMP9796_A_Hardware.SchDoc	Contact: http://www.ti.com/support	

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