



2Z @ 4kHz, 2P @ 400kHz
Fco 35kHz, PM 70degs

- NOTES:**
- 1) R19 for test purposes only
 - 2) for smallest size use:
Vishay IHLP-5050FD-01, 10uH
Pcore 73mW, Pdc 1.11W, Pac 88mW
total losses @ 8Amps: 1.272W (dT +27K)

- ASSEMBLY:**
- 1) L2 does not meet pattern
 - 2) C16, C21 does not meet pattern
 - 3) D101 backpacked R5
 - 4) C101 backpacked R2
 - 5) built on PCB 10117

Revision History	
Revision	Notes
A	*** preliminary ***

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Orderable: EVM_orderable	Designed for: Public Release	Mod. Date: 7/15/2016
TID #: TID	Project Title: High Temperature Preregulator	
Number: PMP30101	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: [No Variations]	Sheet: 1 of 1
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