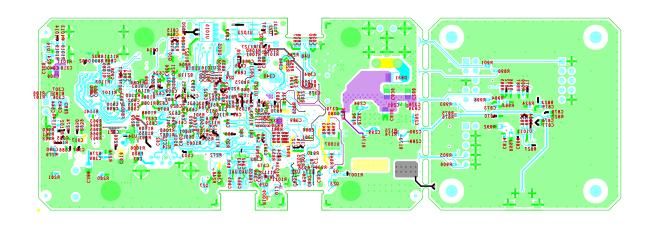


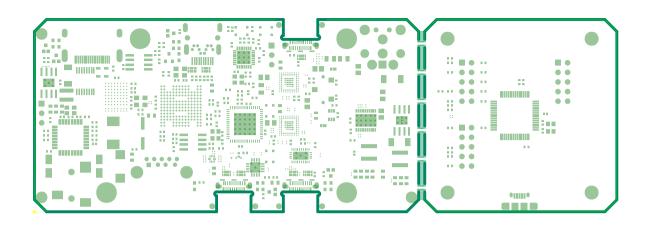
BOARD NAME: USB TYPE C DOCK		KSID: 16871	
ALL ARTWORK LAYERS VIEWED FROM TOP	LAYER DESCRIPT LAYER 1	ION: - TOP SIDE	

ART FILM - BOTTOM

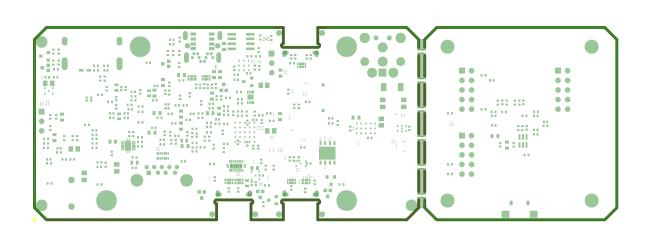


BOARD NAME: USB TYPE C DOCK		KSID: 16871	JOB NUMBER: 129072
ALL ARTWORK LAYERS VIEWED FROM TOP	LAYER DESCRIPT LAYER 1	ION: O - BOTTOM SIDE	

ART FILM - SMASK_TOP



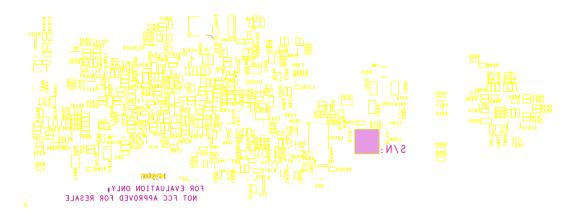
BOARD NAME: USB TYPE C DOCK		KSID: 16871	
ALL ARTWORK LAYERS VIEWED FROM TOP	LAYER DESCRIPTION: SOLDERMASK TOP		



BOARD NAME: USB TYPE C DOCK		KSID: 16871	
ALL ARTWORK LAYERS VIEWED FROM TOP	LAYER DESCRIPT SOLDERM	ION: ASK BOTTOM	

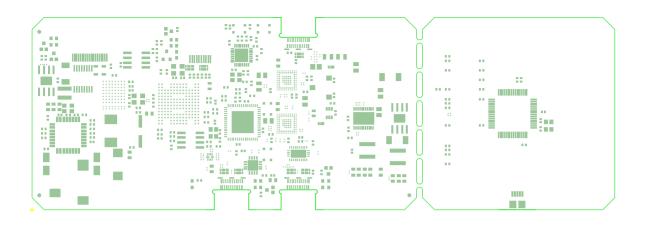


BOARD NAME: USB TYPE C DOCK		KSID: 16871	
ALL ARTWORK LAYERS VIEWED FROM TOP	LAYER DESCRIPTION: SILKSCREEN TOP		



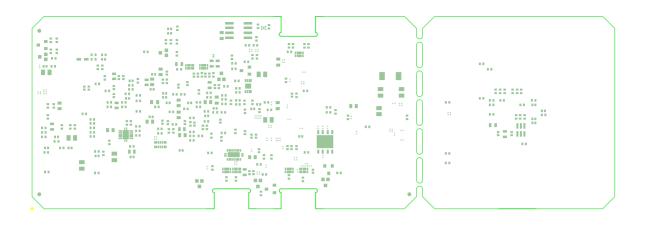
BOARD NAME: USB TYPE C DOCK		KSID: 16871	
ALL ARTWORK LAYERS VIEWED FROM TOP	LAYER DESCRIPT SILKSCR	ION: EEN BOTTOM	

ART FILM - PMASK_TOP

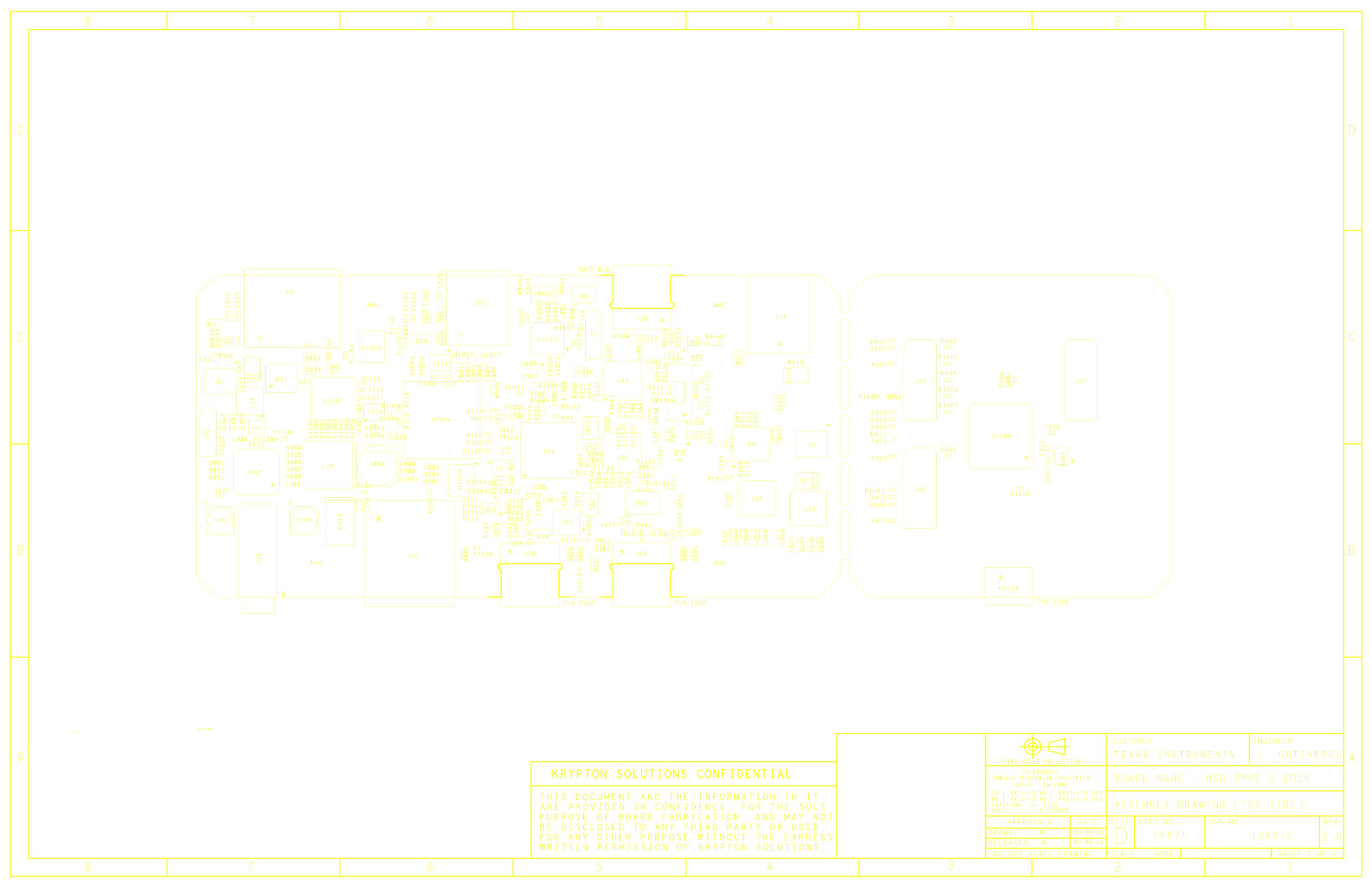


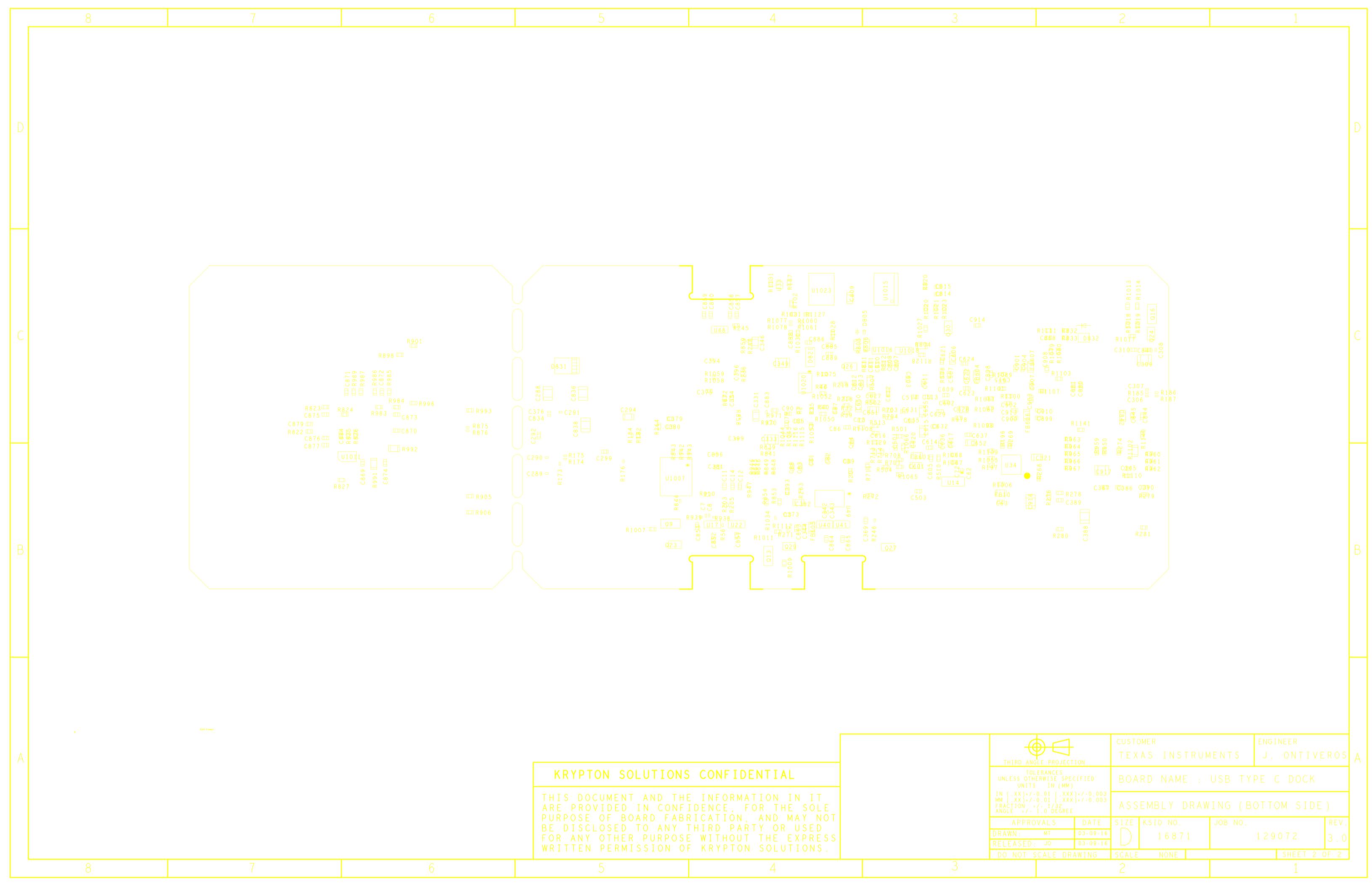
BOARD NAME: USB TYPE C DOCK		KSID: 16871	
ALL ARTWORK LAYERS VIEWED FROM TOP	LAYER DESCRIPTION: PASTEMASK TOP		

ART FILM - PMASK_BOT



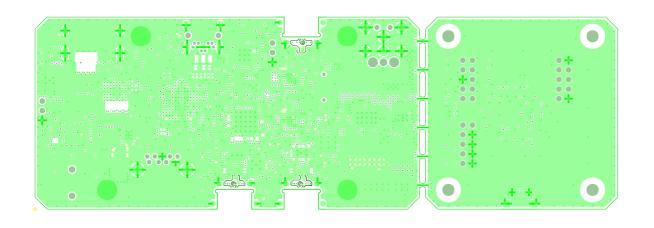
BOARD NAME: USB TYPE C DOCK		KSID: 16871	
ALL ARTWORK LAYERS VIEWED FROM TOP	LAYER DESCRIPTION: PASTEMASK BOTTOM		



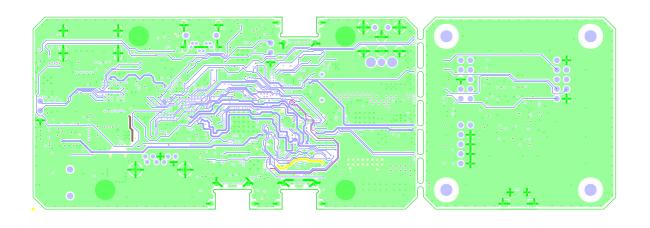


ART FILM - ASSY

DESIGN INFORMATION TABLE 1: BOARD SIZE (REFER ALSO ARRAY/PANEL PROFILING INFORMATION) LAYER SINGLE ENDED NUMBER OF LAYERS: 10 LAYER DIFFIRENTIAL PAIR | DIFF TRACE | DIFF TRACE SINGLE ENDED REFERENCE NAME IMPEDANCE (OHMS) WIDTH SPACING IMPEDANCE (OHMS) | TRACE WIDTH LAYER MIN. TRACK WIDTH: 4.0 MIL 0.006 0.0050" 100 +/- 10% MIN. CLEARANCE : 3.9 MIL LAYER 3 * SOLDERMASK-0.4MIL 0.4 MIL TOP LAYER 1 0.004" 0.0050"" 0.0045" L1: COPPER_1/20Z_PLATED 1.4 MIL 85 +/- 10% 50 +/- 10% LAYER 2 MIN. VIA PAD/DRILL: 12.0/6.0 MIL (BLIND) * FILL 0.080 3.15 MIL LAYER 2 L2_GND L2: COPPER_1.00Z 1.4 MIL * CORE 0.125 4.92 MIL MIN. ANNULAR RING 2 MIL EXTERNAL PER IPC-6012C CLASS 2 0.0045" 0.005" 50 +/- 10% 0.005" LAYER 2 LAYER 3 L3_SIG 85 +/- 10% REGISTRATION TOLERANCES: METAL +/- X MIL, HOLES +/- X MIL L3: COPPER_1.00Z 1.4 MIL X YES - SEE TABLE 1 LAYER 4 L4_3P3V IMPEDANCE CONTROL: NONE * FILL_0.300 11.81 MIL MIXED DIELECTRIC: X NO LAYER 5 L5_VBUS_GND YES LAMINATE MATERIAL: LAYER 6 L6_1P2V_GND L4: COPPER_1.00Z 1.4 MIL X FR-4 HIGH Tg ROGERS 4350B OTHER: * CORE_0.125 4.92 MIL LAYER 7 L7_5V THICKNESS: L5: COPPER 1.2 MIL L8_SIG 85 +/- 10% 0.0045" 0.005" 50 +/- 10% 0.005" LAYER 6 LAYER 9 * FILL_0.125 4.92 MIL X 0.069" +/-10% 0.093" +/-10% OTHER: LAYER 9 L9_GND L6: COPPER 1.2 MIL BOW & TWIST: TOLERANCE: * CORE_0.125 4.92 MIL 100 +/- 10% 0.006 0.0050" LAYER 8 LAYER 10 BOTTOM X IPC-6012C TYPE 3 CLASS 2 X IPC-6012C TYPE 3 CLASS 2 0.0050" 0.0045" 85 +/- 10% 0.004" 50 +/- 10% LAYER 9 L7: COPPER_1.00Z 1.4 MIL OTHER +/-OTHER +/-* FILL_0.300 11.81 MIL COPPER THICKNESS (FINISHED) SPECIAL FAB NOTES: 1/4 OZ. 1/2 OZ. X 1.0 OZ. 2.0 OZ. 2: ALL 6.01 MIL VIAS NEED TO BE FILLED USING NON-CONDUCTIVE EPOXY L8: COPPER 1.00Z 1.4 MIL AND PLATED SMOOTH (TOP SIDE ONLY). INNER SIGNAL: 1/4 OZ. 1/2 OZ. X 1.0 OZ. 2.0 OZ. * CORE_0.125 4.92 MIL 3:ALL 8 MIL VIAS TO BE TENTED ON BOTH SIDES. INNER PLANE: 1/4 OZ. 1/2 OZ. X 1.0 OZ. 2.0 OZ. L9: COPPER_1.00Z 1.4 MIL * FILL_0.080 3.15 MIL L10: COPPER_1/20Z_PLATED 1.4 MIL OTHER: * SOLDERMASK-0.4MIL 0.4 MIL DESIGN CROSS SECTION CHART BOARD FINISH: TOTAL THICKNESS 68.92 MIL DRILL CHART: TOP to L2 SILKSCREEN: X TOP X BOTTOM ALL UNITS ARE IN MILS SIZE TOLERANCE PLATED QTY FIGURE SILKSCREEN COLOR: X WHITE OTHER PLATED 46 6.01 + 3 . 0 / - 6 . 0 SOLDERMASK: (PER IPC-6011,6012) X TOP X BOTTOM DRILL CHART: TOP to BOTTOM ALL UNITS ARE IN MILS SOLDERMASK COLOR: GREEN BLUE X OTHER: BLACK FIGURE TOLERANCE PLATED | QTY SIZE PLATED 1507 8.0 + 3 . 0 / - 8 . 0 THROUGH-HOLE VIA TREATMENT: 10.0 + 3 . 0 / - 10 . 0 PLATED 74 0 OPEN X TENTED IDENTIFIER: ALL 8 MIL VIAS PLATED 10 0 19.69 + 3 . 0 / - 3 . 0 VIA FILL USING NON-CONDUCTIVE EPOXY: 25.59 +1.97/-1.97 PLATED A 28.0 + 3 . 0 / - 3 . 0 PLATED X YES IDENTIFIER: ALL 6.01 MIL VIAS + 3 . 0 / - 3 . 0 THIEVING ALLOWED: YES 51.0 + 3 . 0 / - 3 . 0 PLATED 2 63.0 + 3 . 0 / - 3 . 0 PLATED 4 + BOARD SURFACE TREATMENT 67.0 + 3 . 0 / - 3 . 0 PLATED 2 + 3 . 0 / - 3 . 0 91.0 PLATED X NICKEL/GOLD (ENIG) ORGANIC (OSP) HASL ENEPIG \Diamond 125.0 +2.0/-2.0 PLATED 4 $\langle A \rangle$ OTHER IMMERSION TIN 12.0 NON-PLATED 12 + 2 . 0 / - 2 . 0 я. NON-PLATED 35.0 + 2 . 0 / - 2 . 0 V FOR WIRE BONDING: 51.0 NON-PLATED +2.0/-2.0 L 55.0 +2.0/-2.0 NON-PLATED SOFT GOLD PER SUPPLIED ARTWORK Q T I 67.0 NON-PLATED + 2 . 0 / - 2 . 0 ADDITIONAL REQUIREMENTS: G 125.0 +2.0/-2.0 NON-PLATED 4 33.46x25.59 +3.15/-3.15 PLATED X MICRO-SECTION X TDR REPORTS/COUPONS X ELECTRICAL TEST + 1 . 9 7 / - 1 . 9 7 PLATED 43.11x19.69 X CERTIFICATES OF COMPLIANCE X RoHS X UL 94V-0 +1.97/-1.97 PLATED 43.31x19.69 59.06x32.48 +3.15/-3.15 PLATED VENDOR MARKING: 66.93x35.43 + 3 . 15 / - 3 . 15 PLATED 70.87x27.56 +3.15/-314.96 PLATED VENDOR DATE CODE, UL, LOGO: ETCH X SILKSCREEN BREAK-AWAY T A 86.61x27.56 + 3 . 9 4 / - 3 . 9 4 PLATED X IN AN OPEN AREA WHERE INDICATED (SEE DRAWING) PLATED + 1 . 9 7 / - 1 . 9 7 106.3x35.43 KRYPTON SOLUTIONS CONFIDENTIAL FABRICATION DRAWING SHEET 1 OF 1

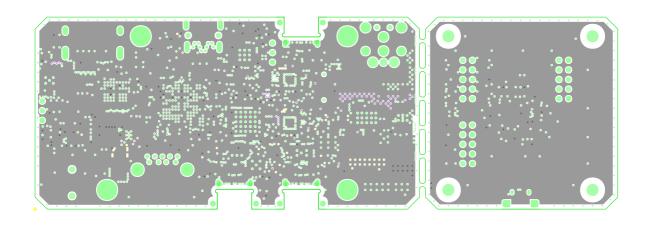


BOARD NAME: USB TYPE C DOCK		KSID: 16871	
ALL ARTWORK LAYERS VIEWED FROM TOP	LAYER DESCRIPT LAYER 2	ION: - GND PLANE	



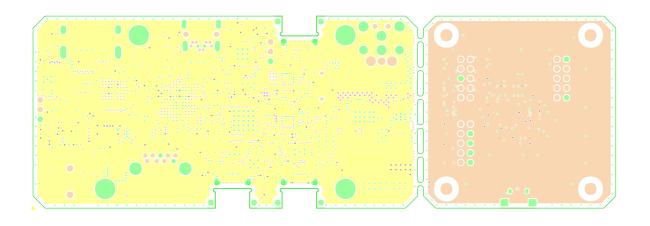
BOARD NAME: USB TYPE C DOCK		KSID: 16871	
ALL ARTWORK LAYERS VIEWED FROM TOP	LAYER DESCRIPTION: LAYER 3 - SIG		

ART FILM - L4_PWR

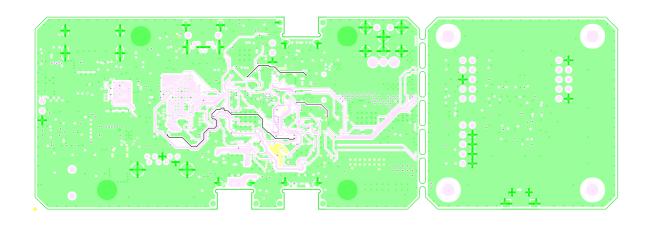


BOARD NAME: USB TYPE C DOCK		KSID: 16871	
ALL ARTWORK LAYERS VIEWED FROM TOP	LAYER DESCRIPT LAYER	ION: 4 - 3P3V	

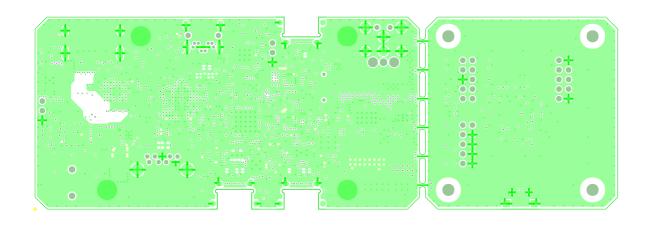
ART FILM - L7_PWR



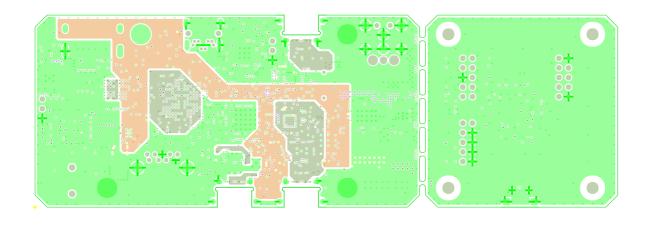
BOARD NAME: USB TYPE C DOCK		KSID: 16871	
ALL ARTWORK LAYERS VIEWED FROM TOP	LAYER DESCRIPTION: LAYER 5 - PWR		



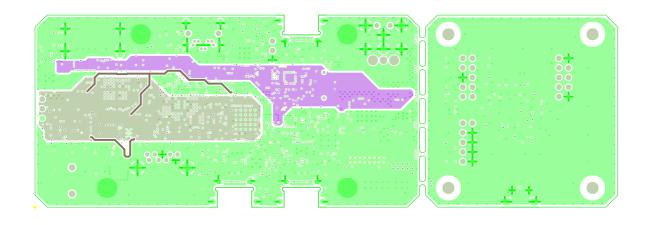
BOARD NAME: USB TYPE C DOCK		KSID: 16871	
ALL ARTWORK LAYERS VIEWED FROM TOP	LAYER DESCRIPTION: LAYER 8 - SIG		



BOARD NAME: USB TYPE C DOCK		KSID: 16871	
ALL ARTWORK LAYERS VIEWED FROM TOP	LAYER DESCRIPTION: LAYER 9 - GND PLANE		



BOARD NAME: USB TYPE C DOCK		KSID: 16871	
ALL ARTWORK LAYERS VIEWED FROM TOP			



BOARD NAME: USB TYPE C DOCK		KSID: 16871	
ALL ARTWORK LAYERS VIEWED FROM TOP			

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