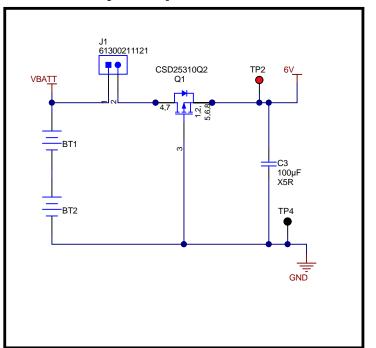
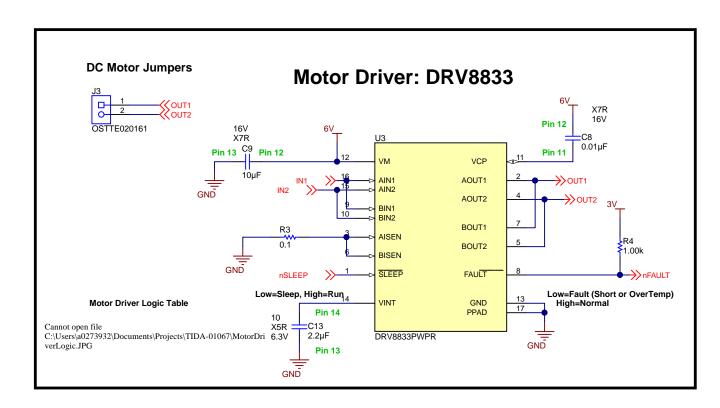
Revision History ECN# Approved by **Smart-Lock Block Diagram** Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its http://www.ti.com licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application. © Texas Instruments 2016

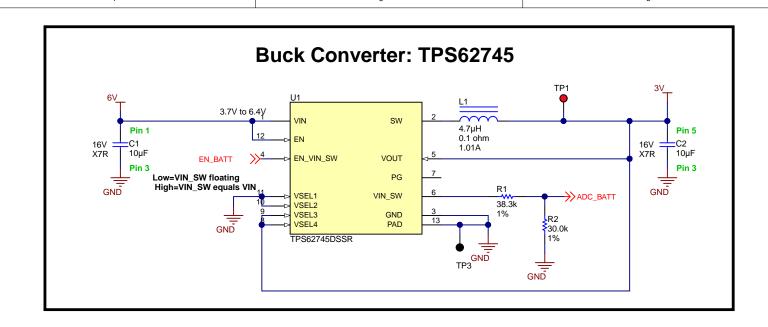
Battery Jumper/Protection

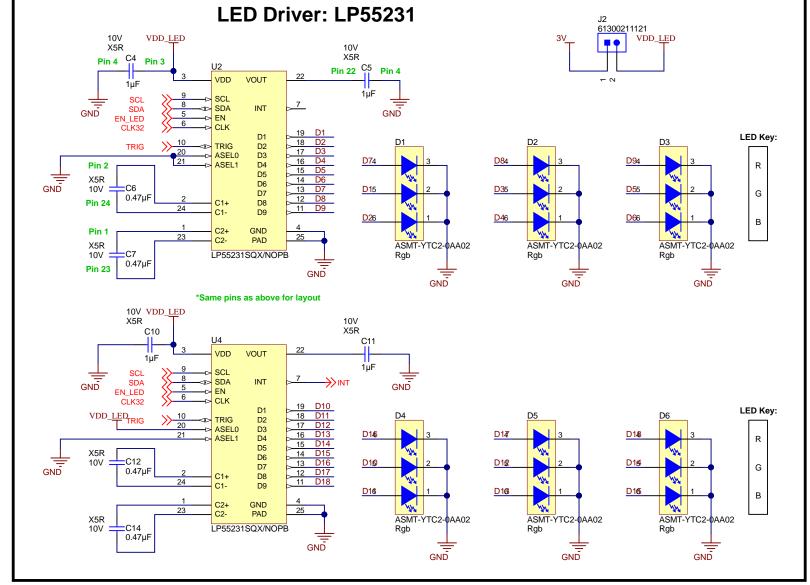




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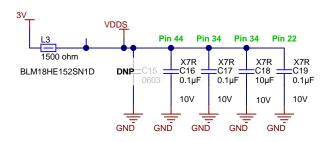
Designed for: Public Release Project Title: TIDA-00757 Smart-Lock Orderable: N/A TID #: TIDA-00757 TEXAS INSTRUMENTS Number: TIDA-00757 Rev: B Sheet Title: Schemati SVN Rev: Version control disabled File: TIDA-00757 Smart-Lock_Sheet1.SchDoc Drawn By: Aramis P. Alvarez

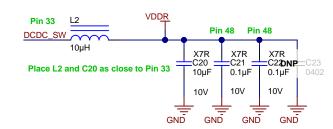
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BLE/MCU: CC2640R2

LPRF Bypass Capacitors & DC-DC Passives

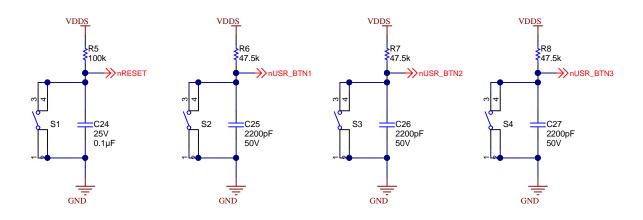
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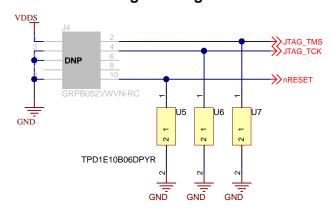


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Reset Switch and User Buttons



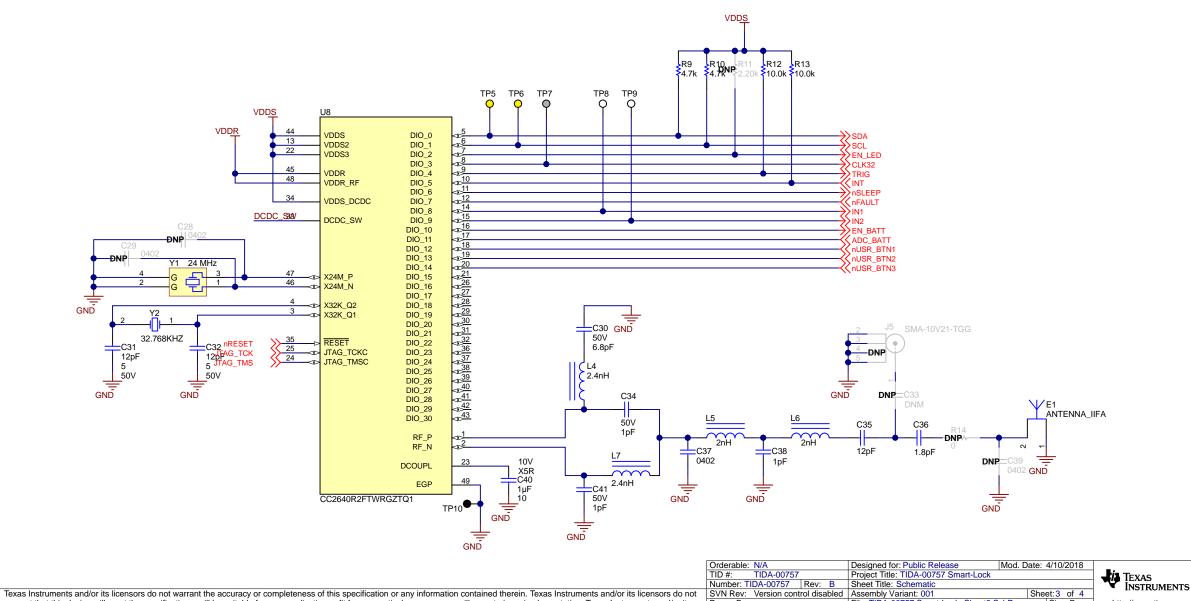
JTAG Programming Interface



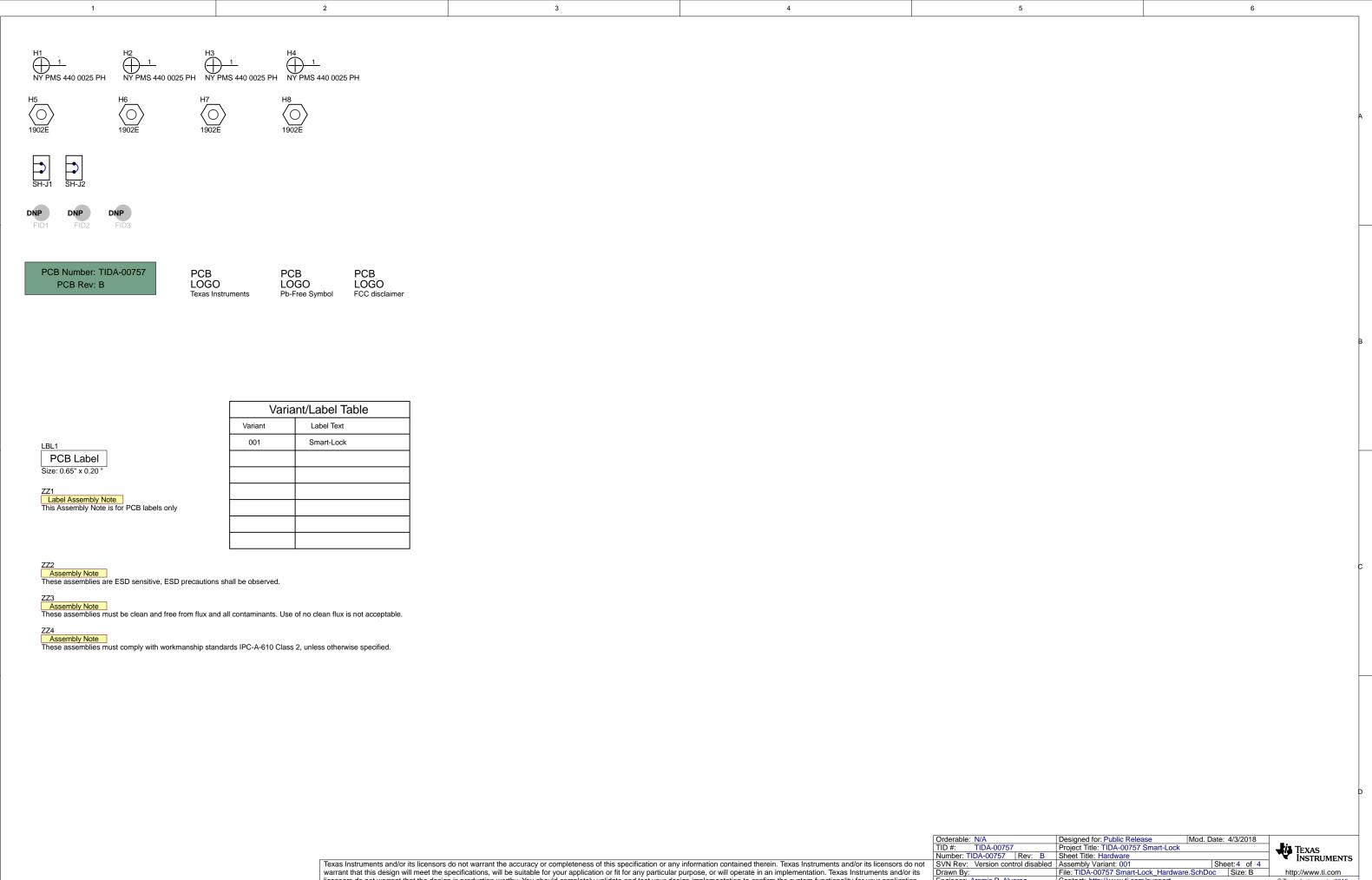
I2C Pullup Resistors

File: TIDA-00757 Smart-Lock_Sheet2.SchDoc

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