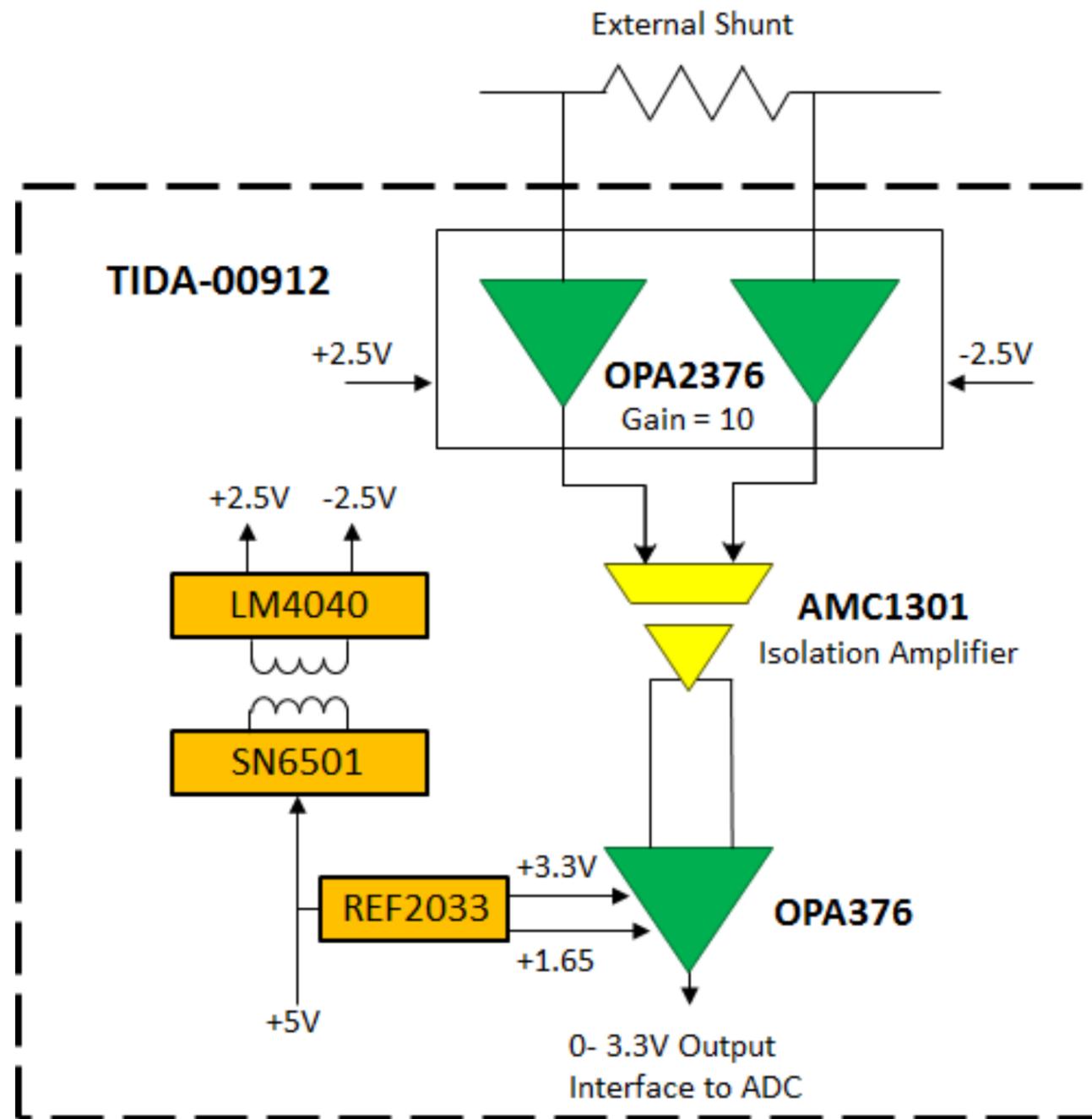


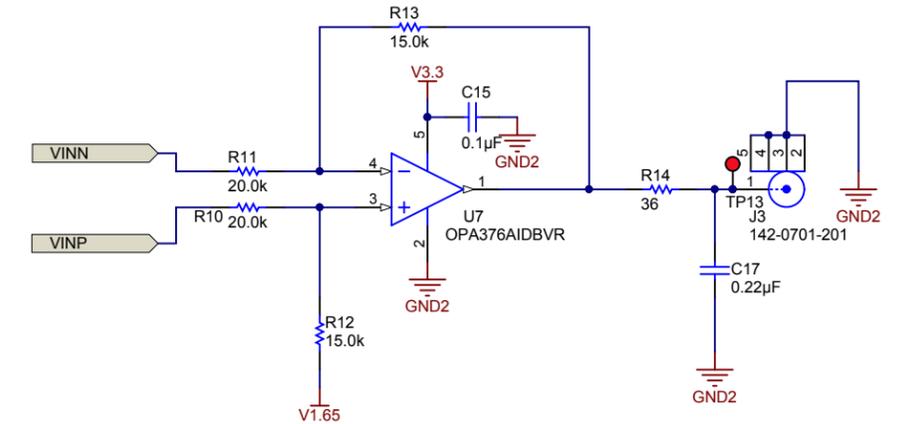
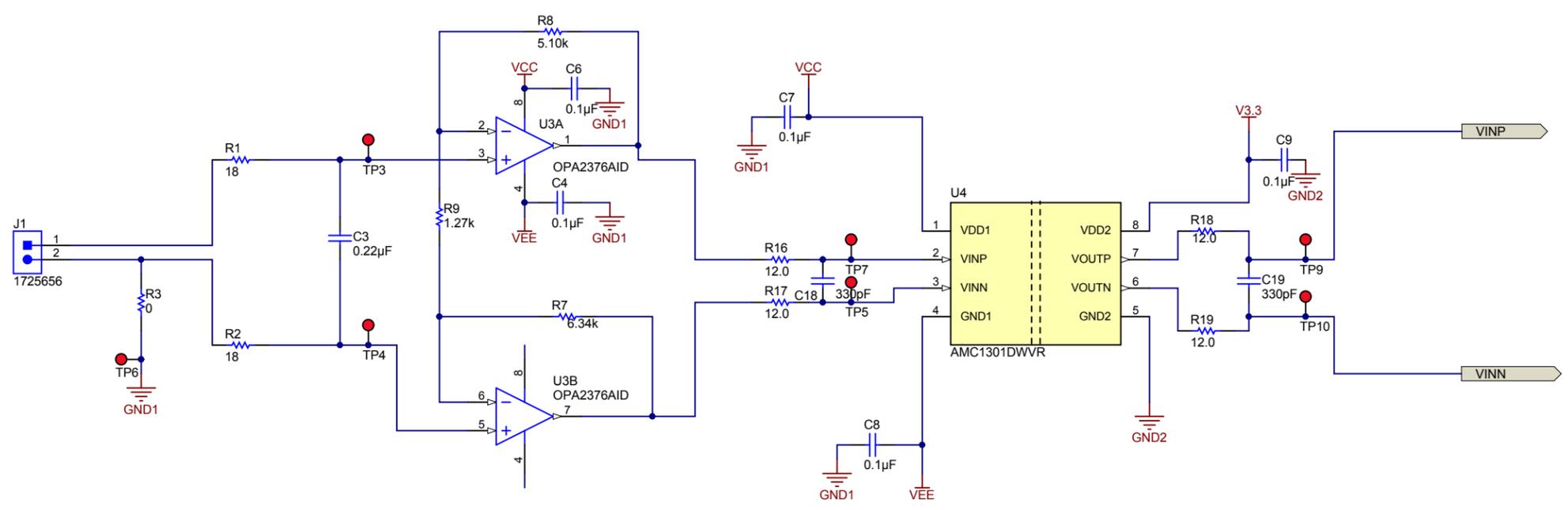
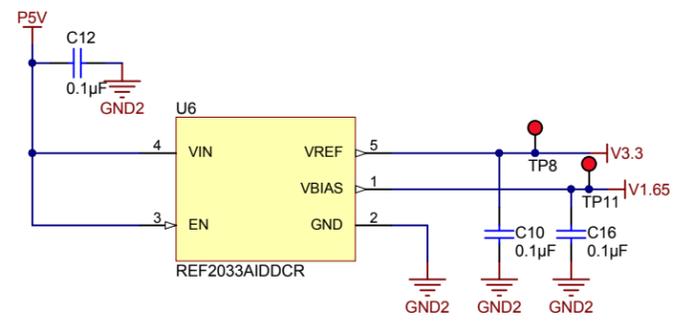
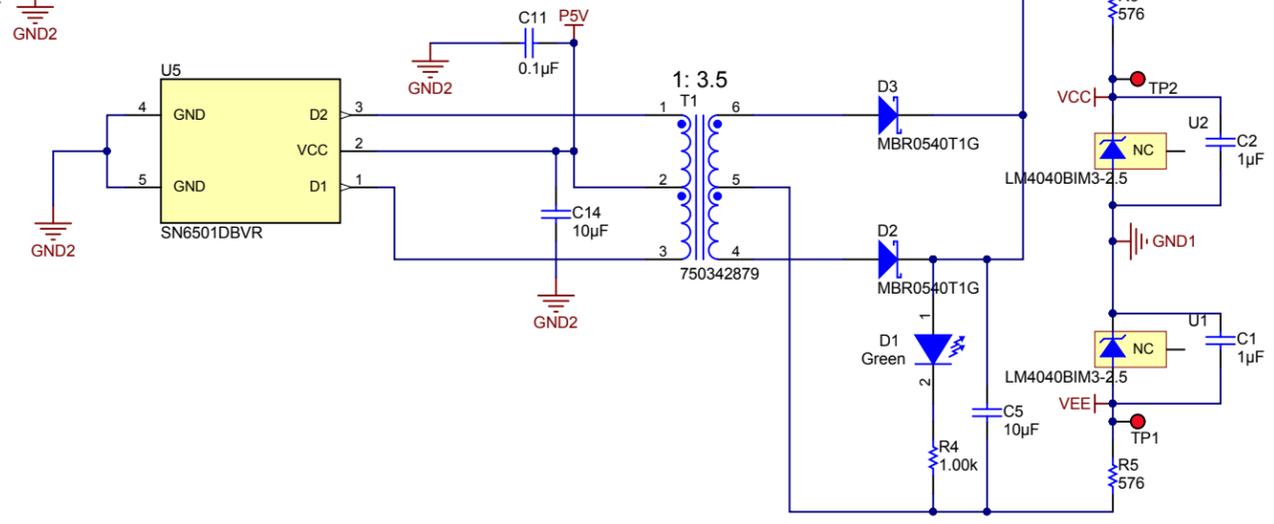
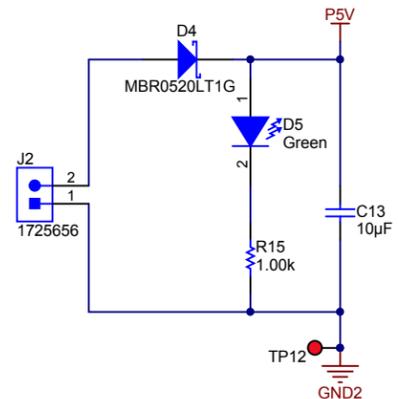
Revision History

Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A



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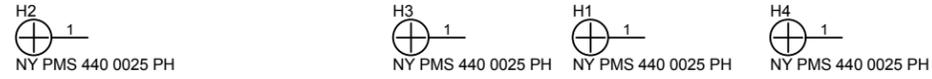
Orderable: N/A	Designed for: Public Release	Mod. Date: 5/16/2016	 http://www.ti.com © Texas Instruments 2016
TID #: TIDA-00912	Project Title: Shunt Design		
Number: TIDA-00912	Rev: E1	Sheet Title: Block diagram	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 3	
Drawn By: Neha Nain	File: TIDA_00912_Block Diagram.SchDoc	Size: B	
Engineer: Neha Nain	Contact: http://www.ti.com/support		



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TID #: TIDA-00912	Project Title: Shunt Design	
Number: TIDA-00912	Rev: E1	Sheet Title: isolated current sense circuit
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 3
Drawn By: Neha Nain	File: TIDA_00912_Schematic.SchDoc	Size: B
Engineer: Neha Nain	Contact: http://www.ti.com/support	





PCB Number: TIDA-00912
PCB Rev: E1

PCB
LOGO
Texas Instruments

PCB
LOGO
Pb-Free Symbol

Label Table	
Variant	Label Text
001	Prototype

ZZ1
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ2
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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TID #: TIDA-00912	Project Title: Shunt Design		
Number: TIDA-00912	Rev: E1	Sheet Title: Hardware components	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 3 of 3	
Drawn By: Neha Nain	File: TIDA_00912_TID_Hardware.SchDoc	Size: B	
Engineer: Neha Nain	Contact: http://www.ti.com/support		

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