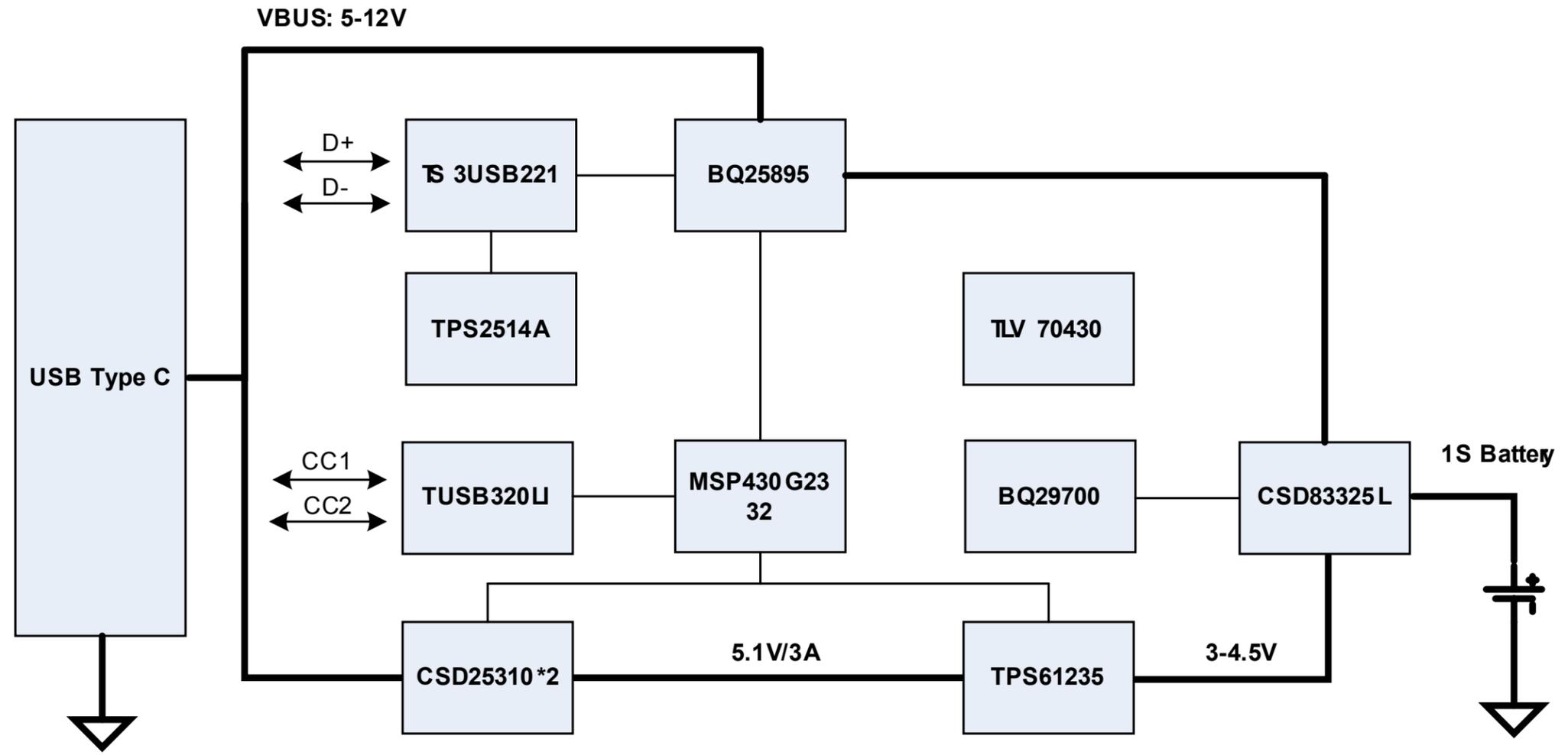
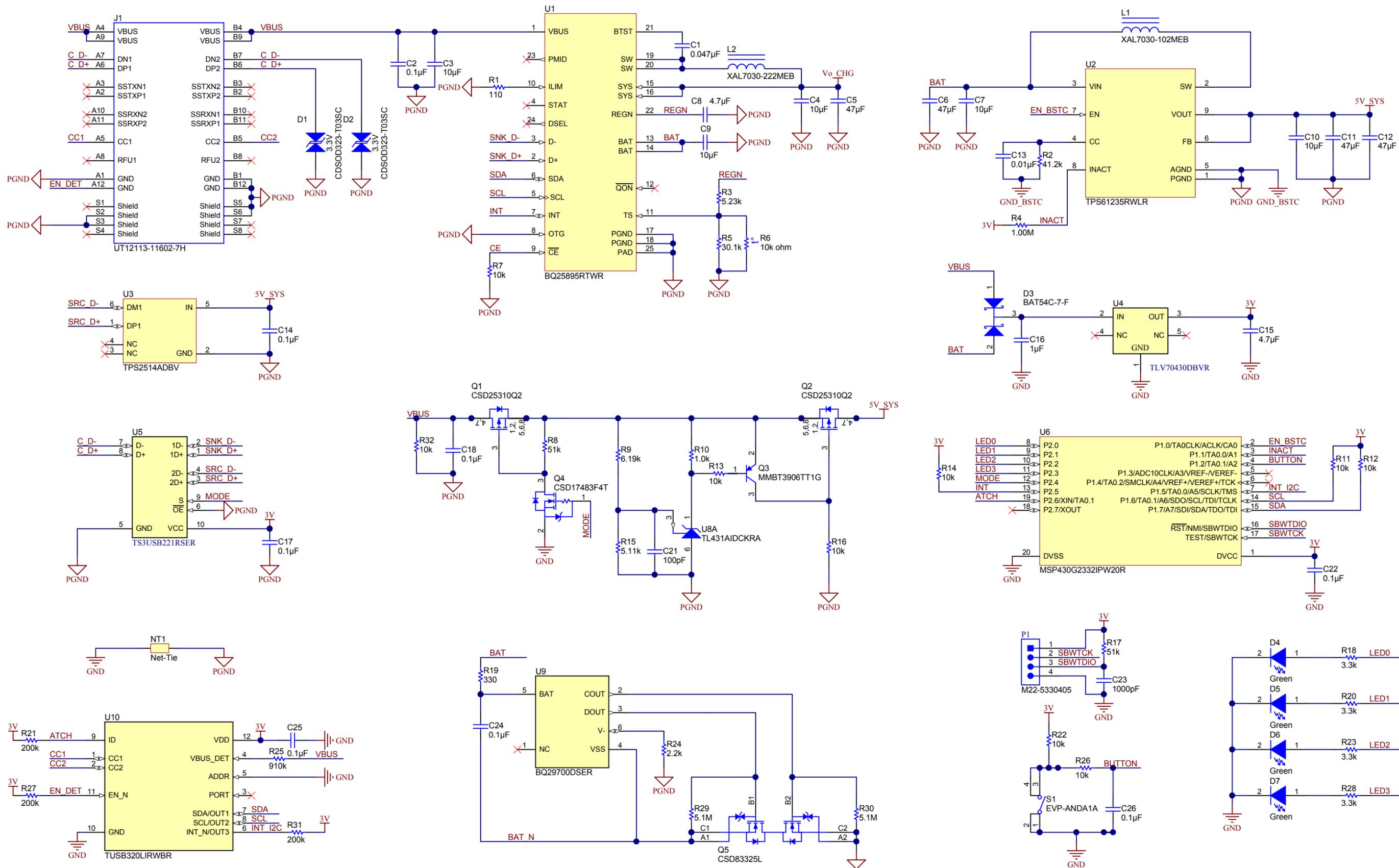


Revision History	
Revision	Notes
1.0	For Public Release



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TID #: PMP4496	Project Title: USB-C DRP Power Bank With Fast Charger Input R	Sheet: 1 of 3	
Number: PMP4496	Rev: 1.0	File: PMP4496_Cover.SchDoc	Size: B
SVN Rev: Version control disabled	Engineer: Yingyang Ou	Contact: http://www.ti.com/support	© Texas Instruments 2015



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TID #: PMP4496	Project Title: USB-C DRP Power Bank With Fast Charger Input R	
Number: PMP4496	Rev: 1.0	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 2 of 3
Drawn By:	File: PMP4496_Main.SchDoc	Size: B
Engineer: Yingyang Ou	Contact: http://www.ti.com/support	

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PCB
LOGO
Pb-Free Symbol

Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

ZZ1
Assembly Note
 These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ2
Assembly Note
 These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ3
Assembly Note
 These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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TID #: PMP4496	Project Title: USB-C DRP Power Bank With Fast Charger Input R		
Number: PMP4496	Rev: 1.0	Sheet: 3 of 3	
SVN Rev: Version control disabled	Assembly Variant: 001	Size: B	
Drawn By:	File: PMP4496_Hardware.SchDoc	Contact: http://www.ti.com/support	
Engineer: Yingyang Ou			

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