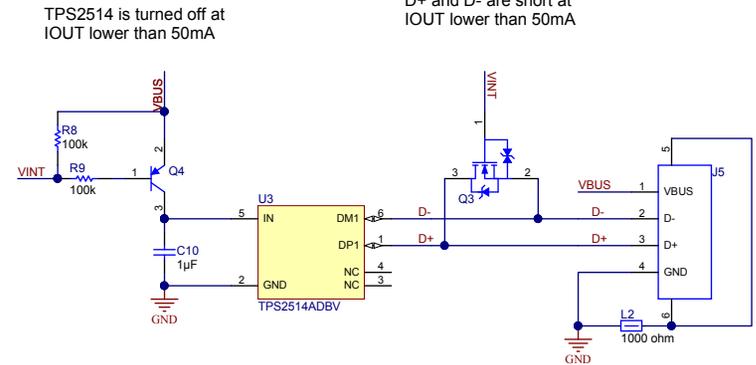
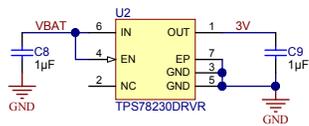


VBUS increase 100mV at IOUT > 1.6A
Current limit values are 1A or 3.2A.



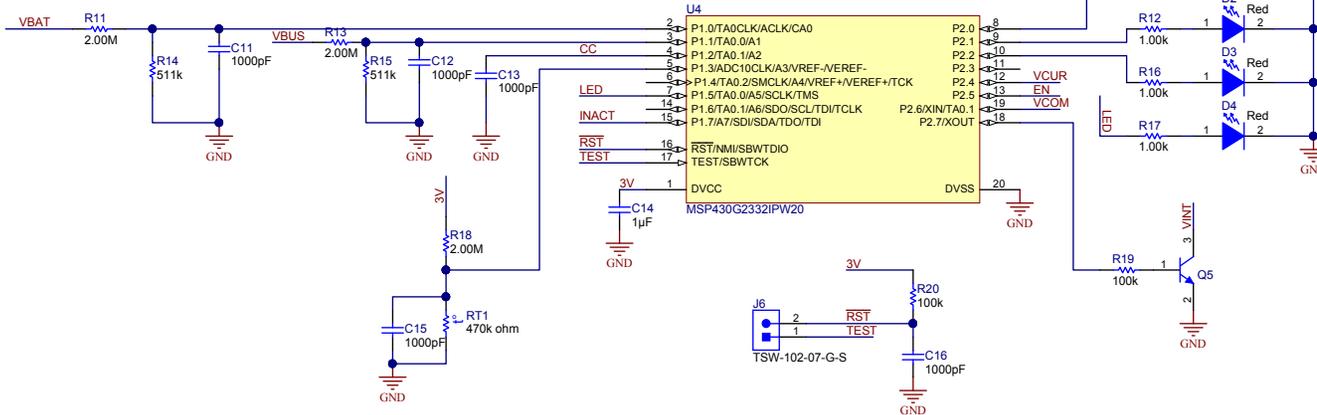
TPS2514 is turned off at IOUT lower than 50mA

D+ and D- are short at IOUT lower than 50mA



VBAT, CC, VBUS are ADC pins
INACT falling edge trigger interrupt and awake MCU
VCOM, EN, VCUR are logic output pins

LEDs indicate the battery state of charge.



⚠ No Installed

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TID #: N/A	Project Title: Power Bank Reference Design Using TPS61236	
Number: PMP9776	Rev: A	Sheet Title:
SVN Rev.: Version control disabled	Assembly Variant: [No Variations]	Sheet 1 of 1
Drawn By:	File: PMP9776_SchDoc	Size: A3
Engineer: Jasper Li	Contact: http://www.ti.com/support	



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