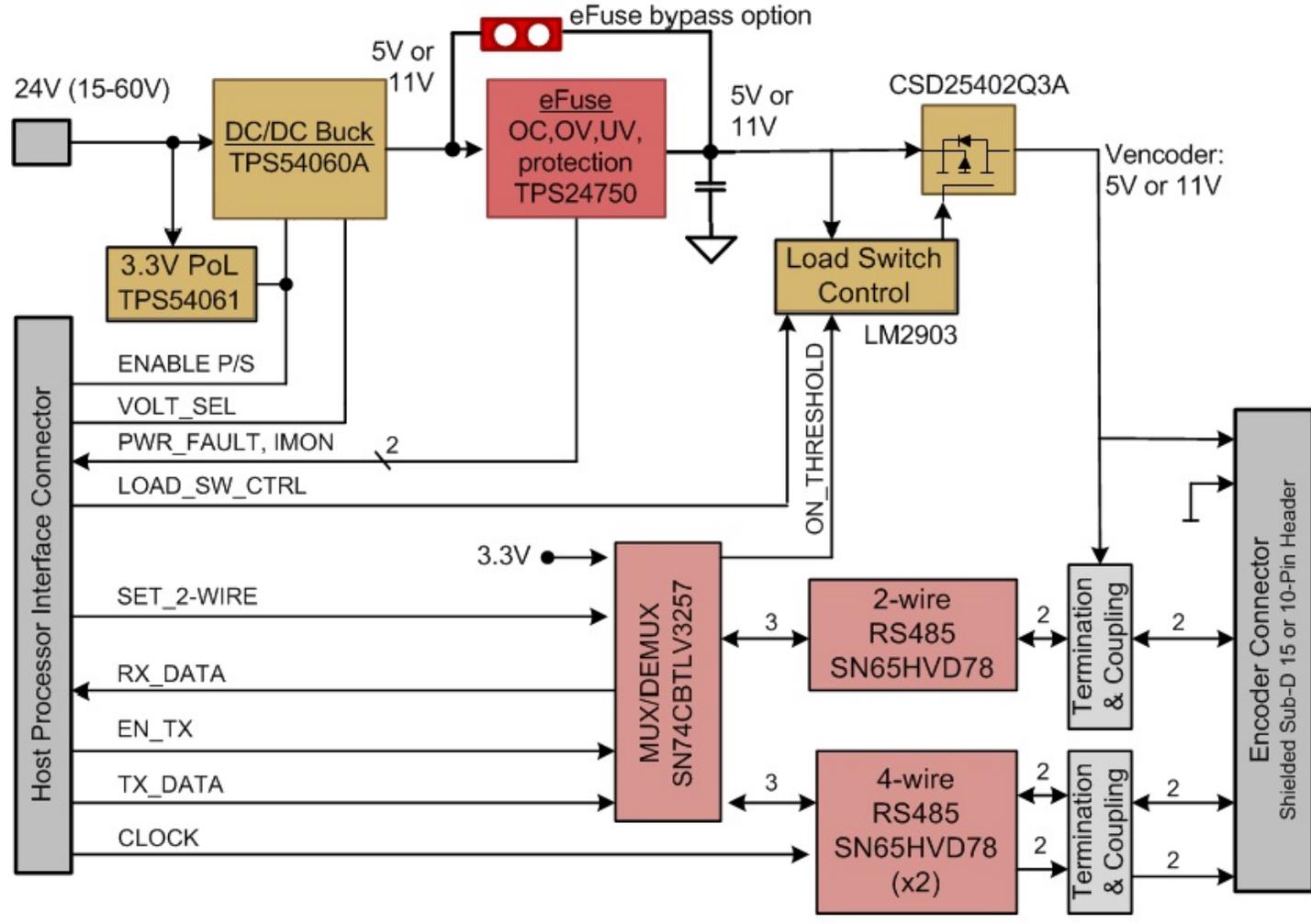
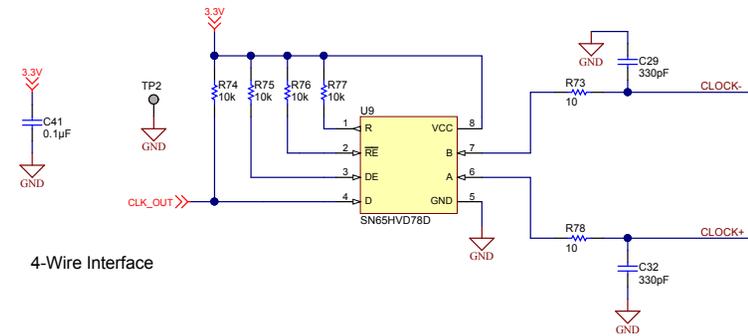
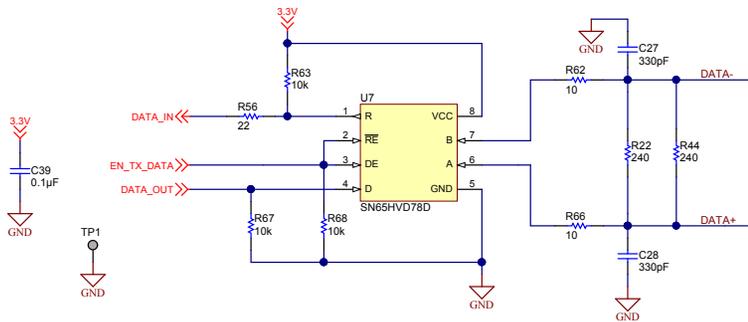


Revision History	
Revision	Notes



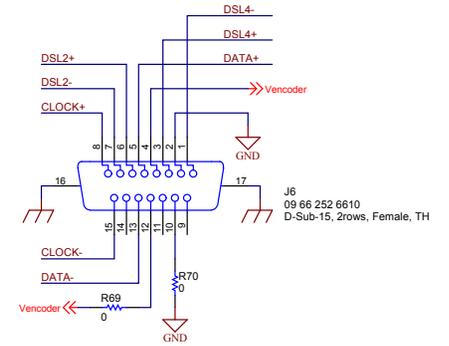
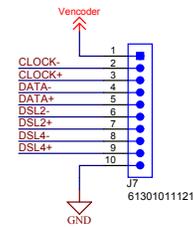
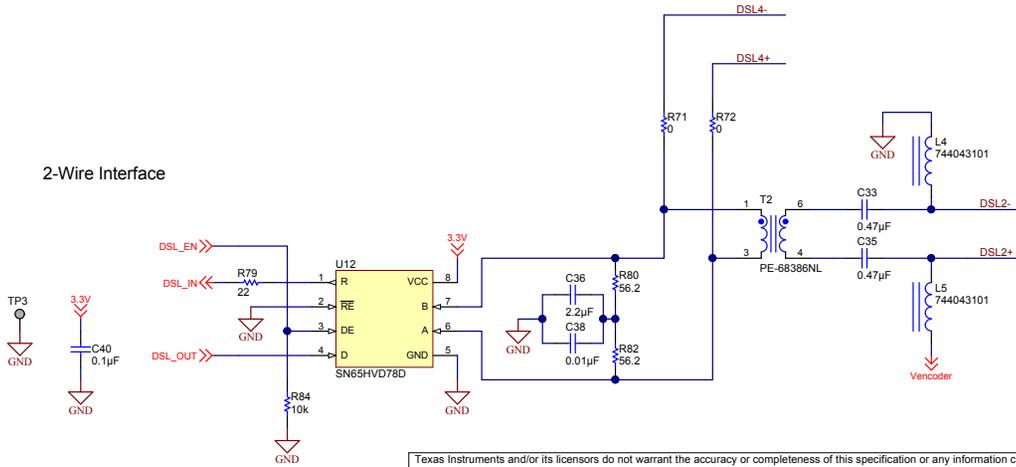
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SVN Rev: Version control disabled	Assembly Variant: Production	Sheet: 1 of 3	
Drawn By:	File: CoverSheet_SchDoc	Size: B	
Engineer: V.Pizzolante	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>		



4-Wire Interface

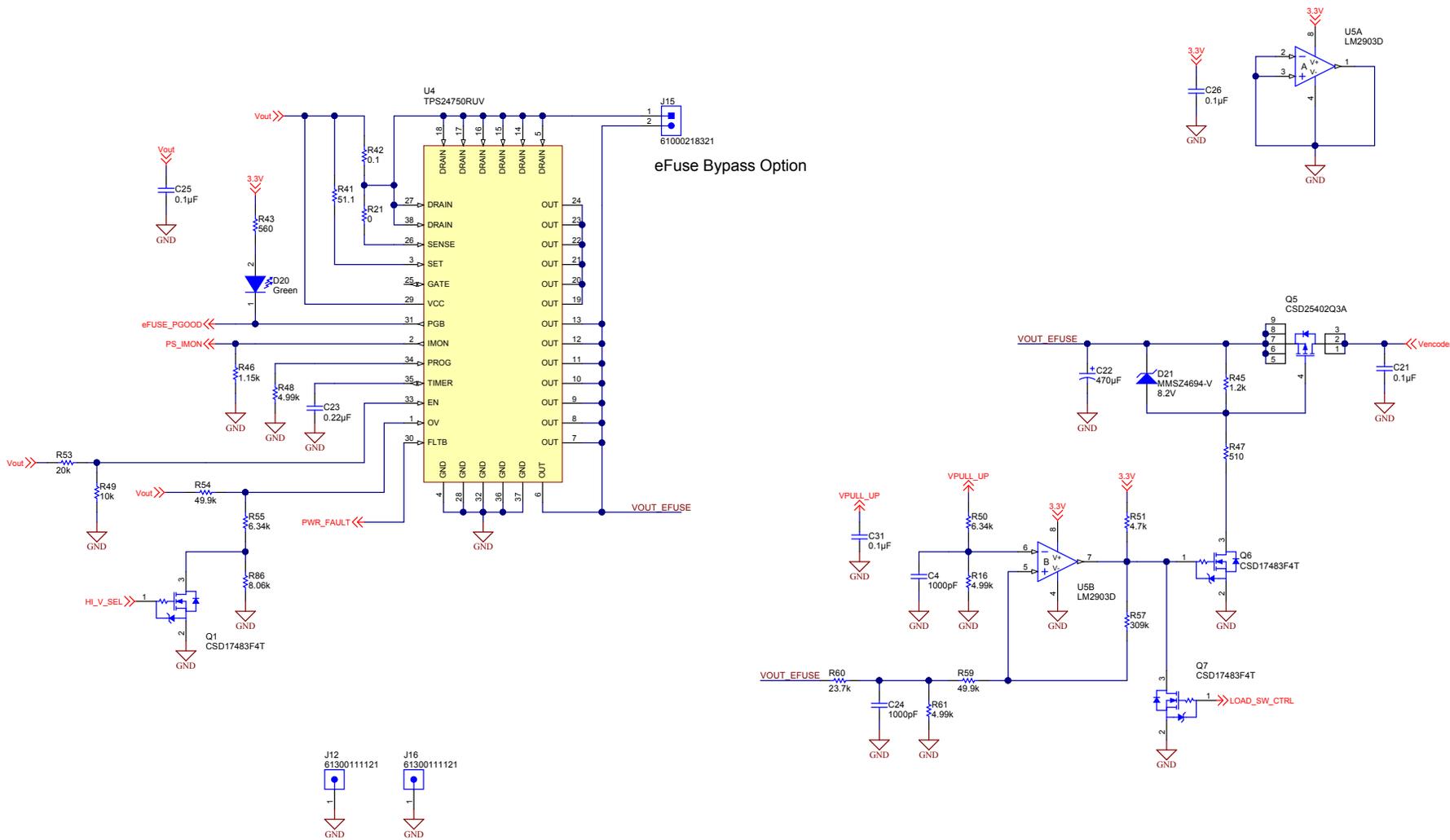
2-Wire Interface



Encoder Connectors

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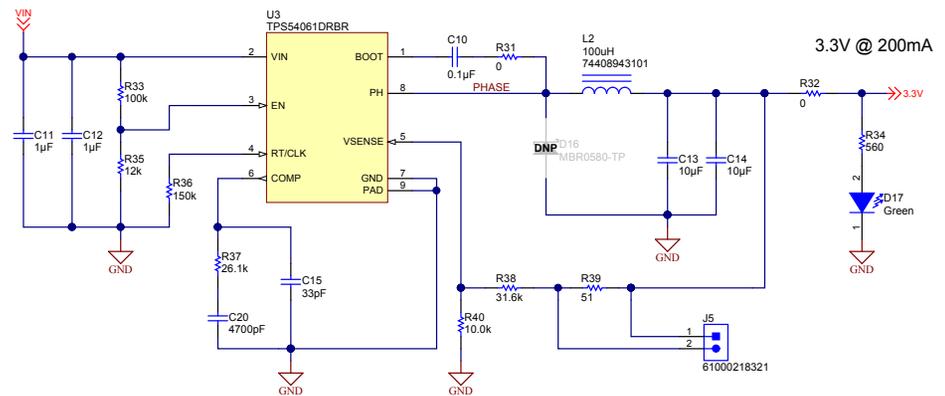
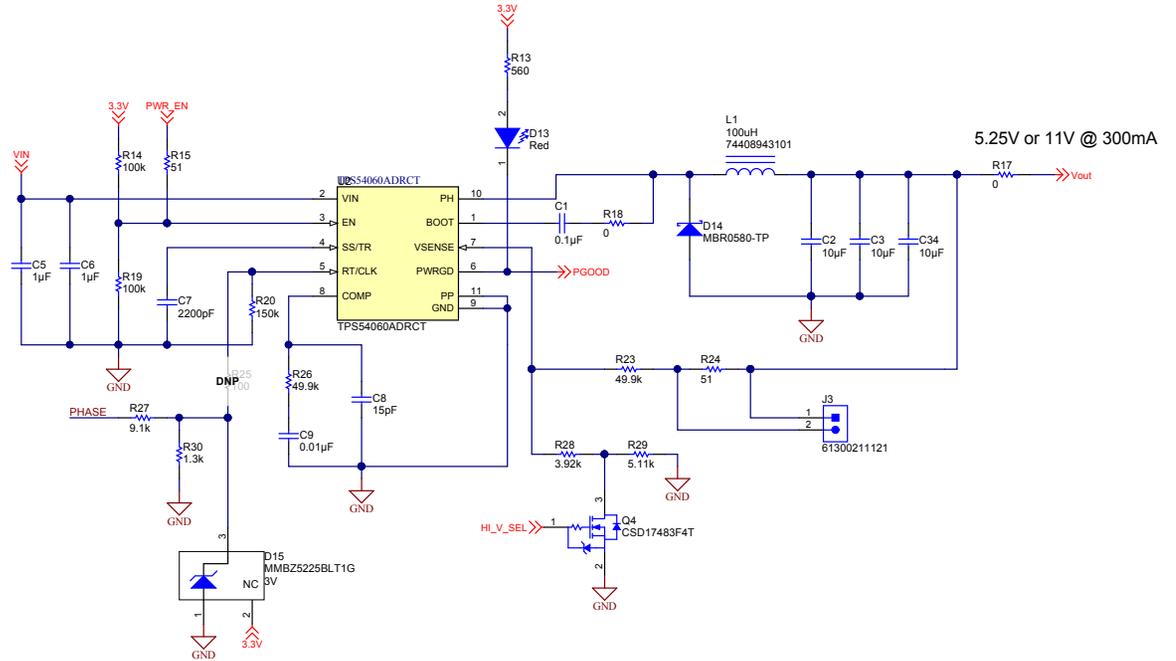
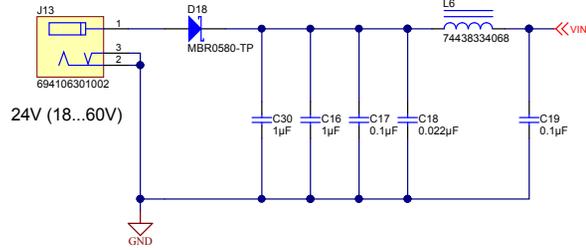


eFuse Bypass Option

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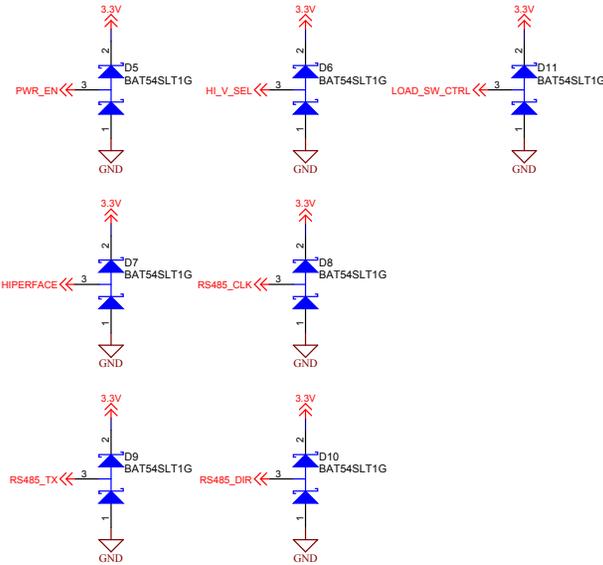
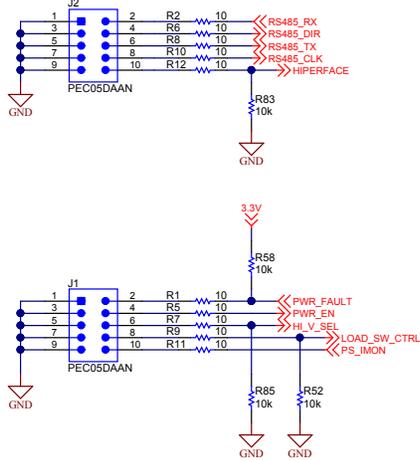


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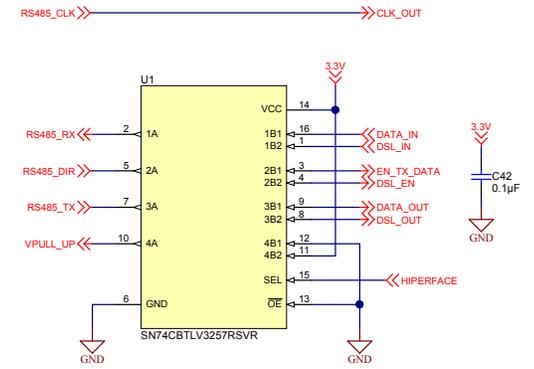
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SVN Rev: Version control disabled	Assembly Variant: Production	
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### Host Processor Interface Connectors

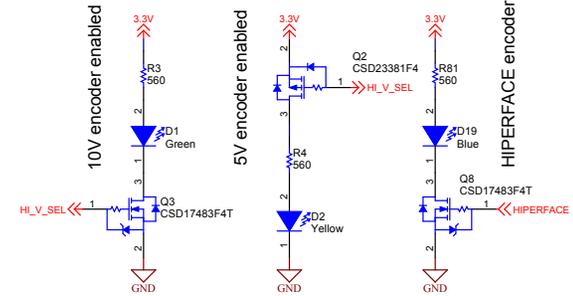
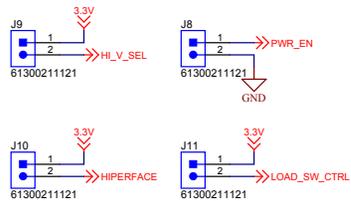


### Input ESD protection



### LED status indicators

### Configuration Settings



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H1  
NY PMS 440 0025 PH  
H2  
NY PMS 440 0025 PH  
H3  
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H5  
1902C  
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1902C

FID1  
FID2  
FID3

PCB Number: TIDA-00179  
PCB Rev: E1

PCB  
LOGO  
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PCB  
LOGO  
Pb-Free Symbol

A

A

B

B

C

C

D

D

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SVN Rev: Version control disabled	Assembly Variant: Production	Sheet: 3 of 3
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