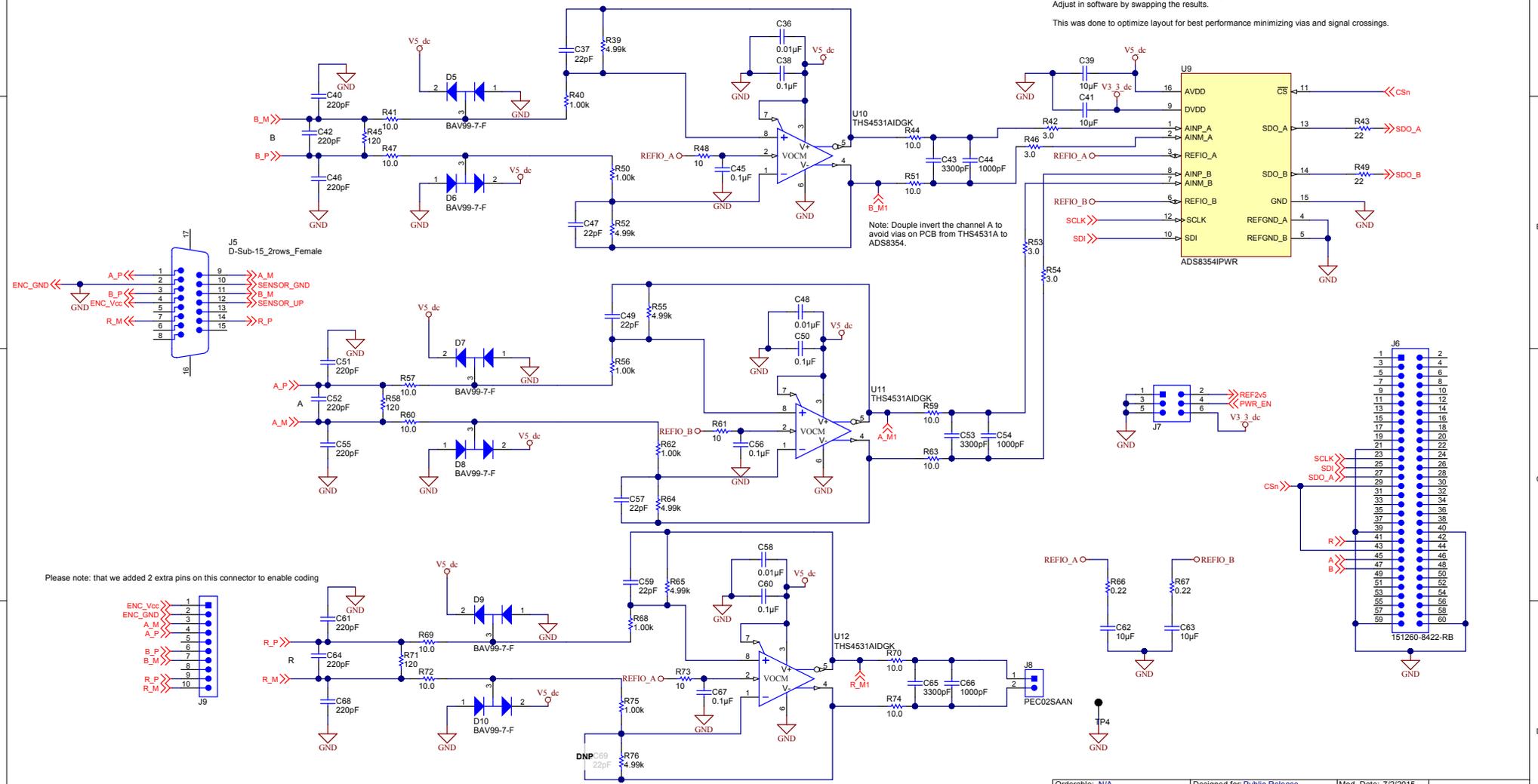
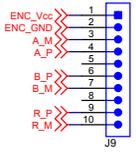


NOTE
 The differential inputs A+ A- are connected to ADS8354 AINM_B and AINP_B
 The differential inputs B+ B- are connected to ADS8354 AINM_A and AINP_A
 Since both signals are sampled simultaneously there's no impact.
 Adjust in software by swapping the results.
 This was done to optimize layout for best performance minimizing vias and signal crossings.



Please note: that we added 2 extra pins on this connector to enable coding

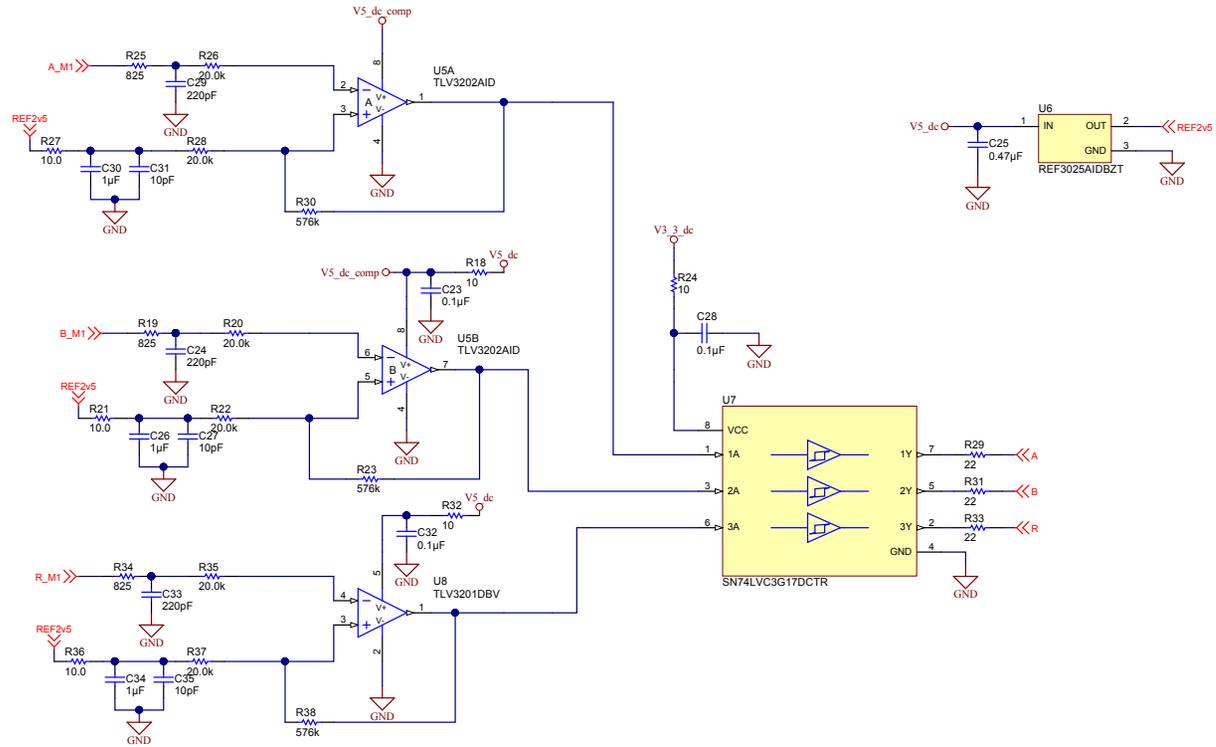


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TID #: 001178	Project Title: Interface to a SinCos Encoder	
Number: TIDA-00178 Rev: E1	Sheet Title:	
SVN Rev. Version control disabled	Assembly Variant: TIDA-00178_REVE1_product	Sheet: 2 of 3
Drawn By:	File: TIDA-00178_SCLS.SchDoc	Size: B
Engineer: Staebler/Mogensen	Contact: http://www.ti.com/support	



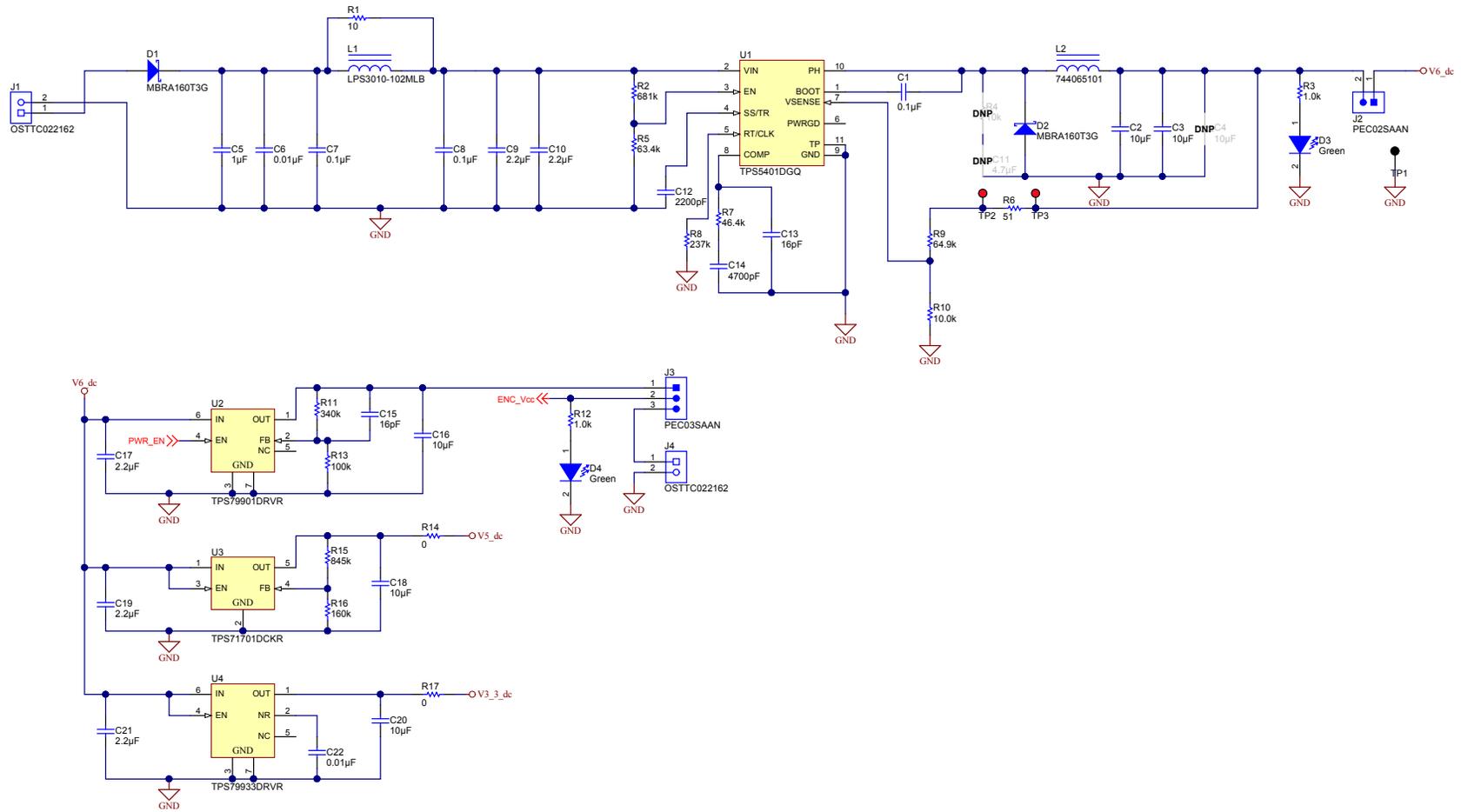
NOTE: Use negative differential signal to double invert the signal at the comparator



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Number: TIDA-00178 Rev: E1	Sheet Title:	
SVN Rev. Version control disabled	Assembly Variant: TIDA-00178_REVE1_product	Sheet 2 of 3
Drawn By:	File: TIDA-00178_SCHS.SchDoc	Size: B
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Number: TIDA-00178 Rev: E1	Sheet Title:		
SVN Rev: Version control disabled	Assembly Variant: TIDA-00178_REVE1_prod	Sheet: 2 of 3	
Drawn By: Engineer: Staebler/Mogensen	File: TIDA-00178_PS_SchDoc	Size: B	
Contact: http://www.ti.com/support			

1 2 3 4 5 6



H1
NY PMS 440 0025 PH



H2
NY PMS 440 0025 PH



H3
1902C



H4
1902C



DNP
FID1



DNP
FID2



DNP
FID3



FID4



FID5



FID6

PCB Number: TIDA-00178
PCB Rev: Rev E1

PCB
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1 2 3 4 5 6

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TID #: 00178	Project Title: Interface to a SinCos Encoder	
Number: TIDA-00178 Rev: E1	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: TIDA-00178_REVE1_product	Sheet 3 of 3
Drawn By:	File: TIDA-00178_BD_SchDoc	Size: B
Engineer: Staebler/Mogensen	Contact: http://www.ti.com/support	



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A

A

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D

D

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