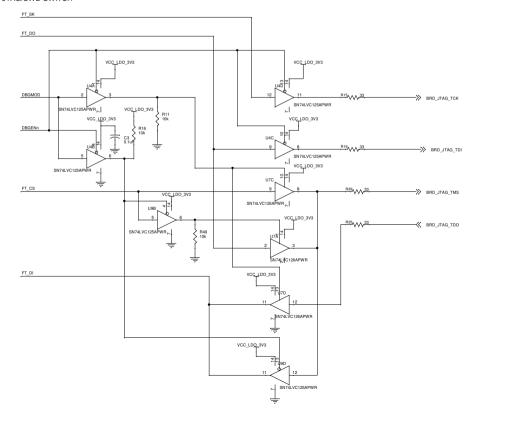
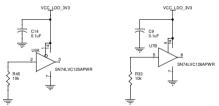
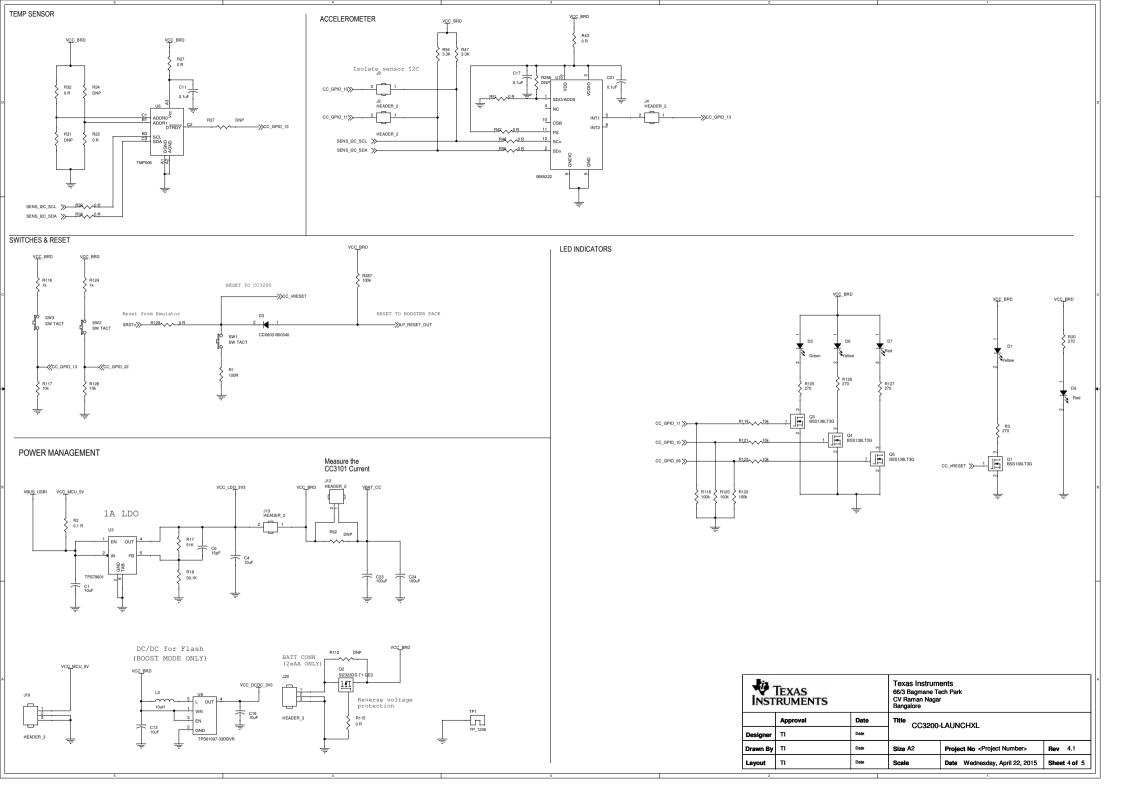


## JTAG/SWD SWITCH





TEXAS INSTRUMENTS			Texas Instruments 66/3 Bagmane Tech Park CY Raman Nagar Bangalore			
	Approval	Date	Title CC3200-LAUNCHXL			
Designer	П	Date				
Drawn By	П	Date	Size A2	Project No <project number=""></project>	Rev	4.1
Layout	π	Date	Scale	Date Wednesday, April 22, 2015	Sheet	3 <b>of</b> 5



Board Revision control naming convention X.Y-Z (X= schemtic ver, Y = layout version (include silk changes), Z = BOM variant)

## Revision history

Changes in Rev 4.0-A

- Changes in Rev 4.0-A

  1. Add pull-ups and pull-down resistors for the serial flash

  2. Move the nRESET pull from VCC\_BRD to VBAT\_CC

  3. Add pulll-up on UART\_TX going to the FTDI to prevent false start bits.

  4. Add pull-up resistor for Accelerometer address to avoid conflict with Audio Booster pack

  5. Add 100K pull-up on RESET\_OUT net for any BP without RESET pulls.

  6. Change R61 to 2.7K, R57-> 270 Ohms

Rev 4.1-B

1. Changed the s-flash from Micron to Winbond

TEXAS INSTRUMENTS			Texas Instruments 66/3 Bagmane Tech Park CV Raman Nagar Bangalore				
	Approval	Date	Title CC3200LAUNCHXL				
Designer	TI	Date					
Drawn By	TI	Date	Size A3	Project No	Rev 4.1C		
Layout	TI	Date	Scale	Date Wednesday, April 22, 2015	Sheet 5 of 5		

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