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REVISIONS				
REV	ECO	DESCRIPTION	DATE	APPROVED
A	2144961	Initial release	10/8/2014	NG
B	2147248	Refer to page 12	12/16/2014	NG

ESD 2514273
PCB 2514274
CCA 2514275

DWN	Kevin Lowderman	DATE	04/30/2015
ENGR	Kevin Lowderman		04/30/2015
SYS	Dan Morgan		????
APVD	Jeff Dennis		????
QA	????		????

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TITLE TIDA_00576 DLPDLCR4710EVM

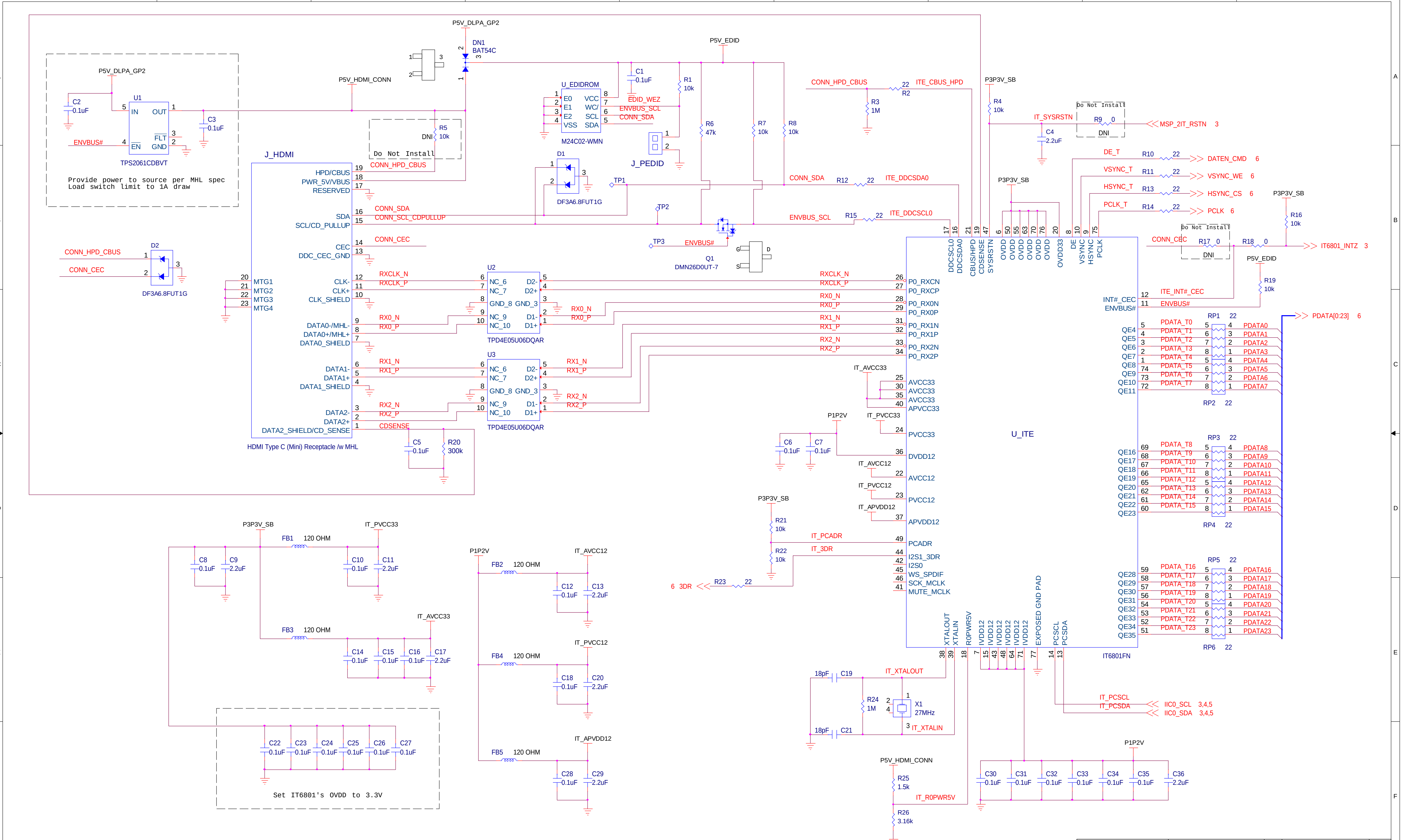
DRAWING NO 2514273

REV B

11 x 17

Orcad Capture 16.6

SHEET 1 of 13



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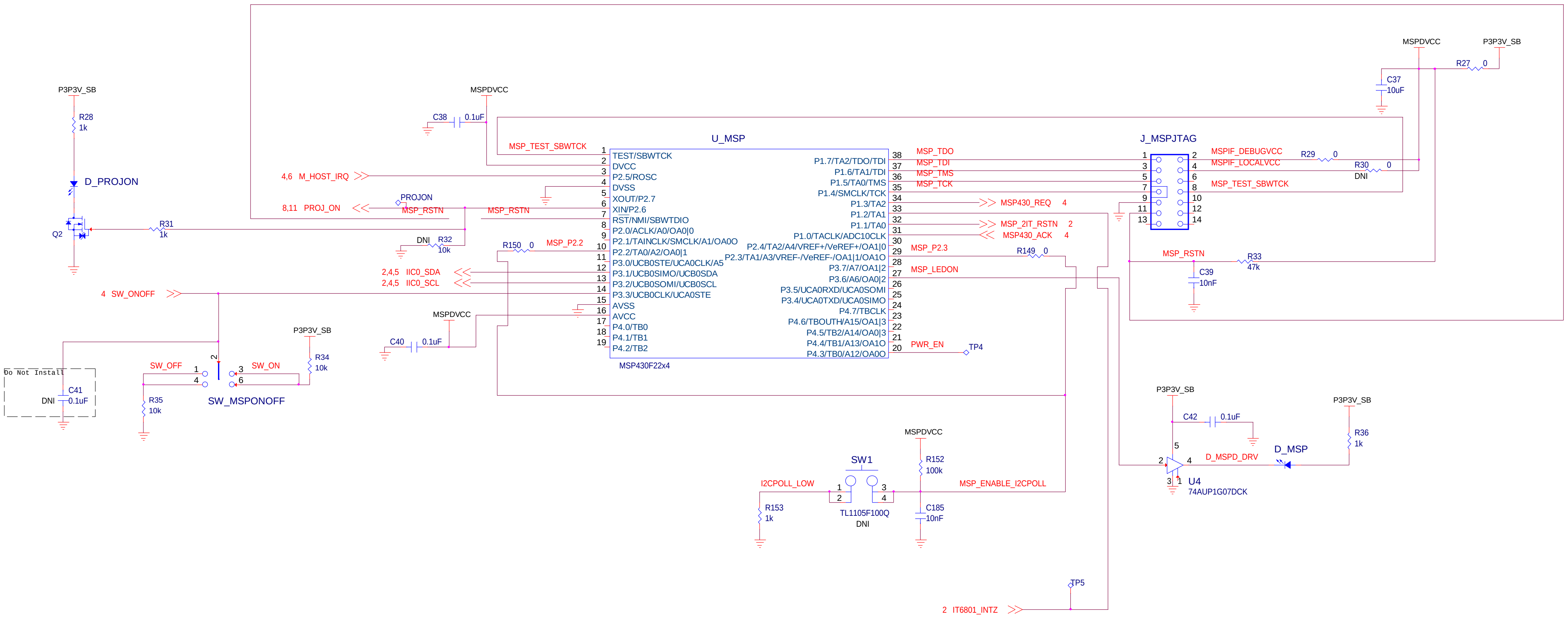

MSP430

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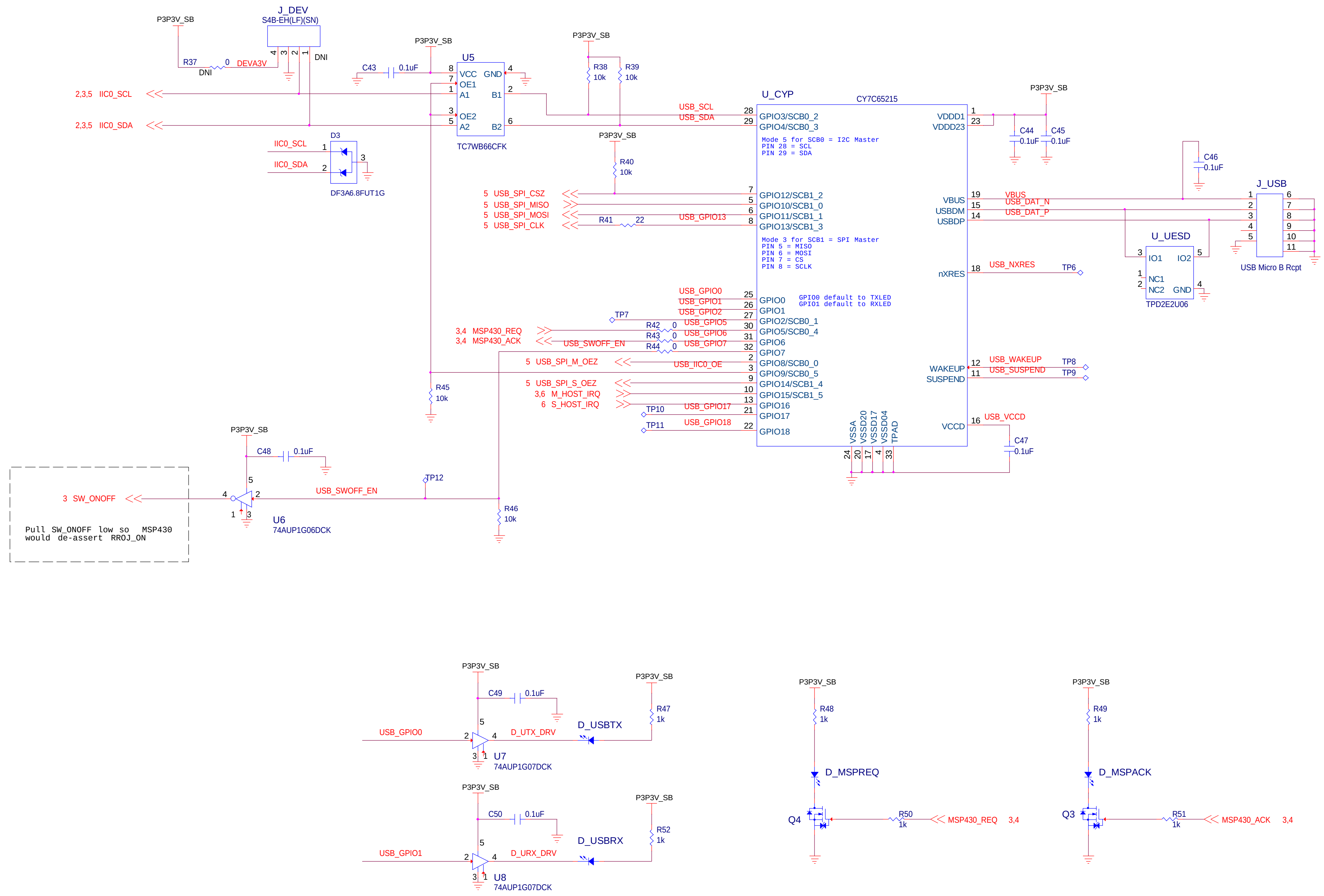

USB Interface

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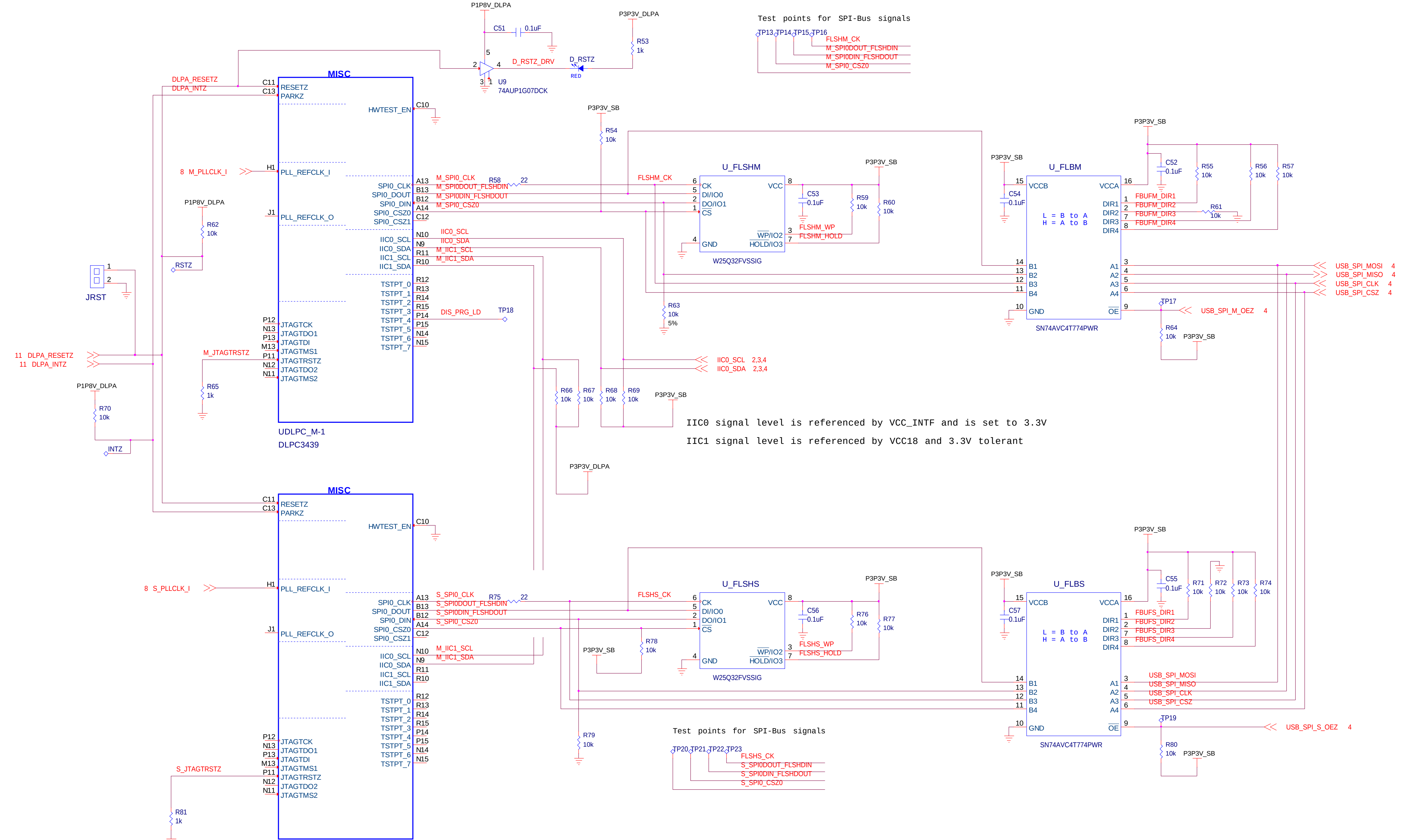

IIC0 signal level is referenced by VCC_INTF and is set to 3.3V

IIC1 signal level is referenced by VCC18 and 3.3V tolerant

DLPC3439 Clock, Reset, SPI Flash

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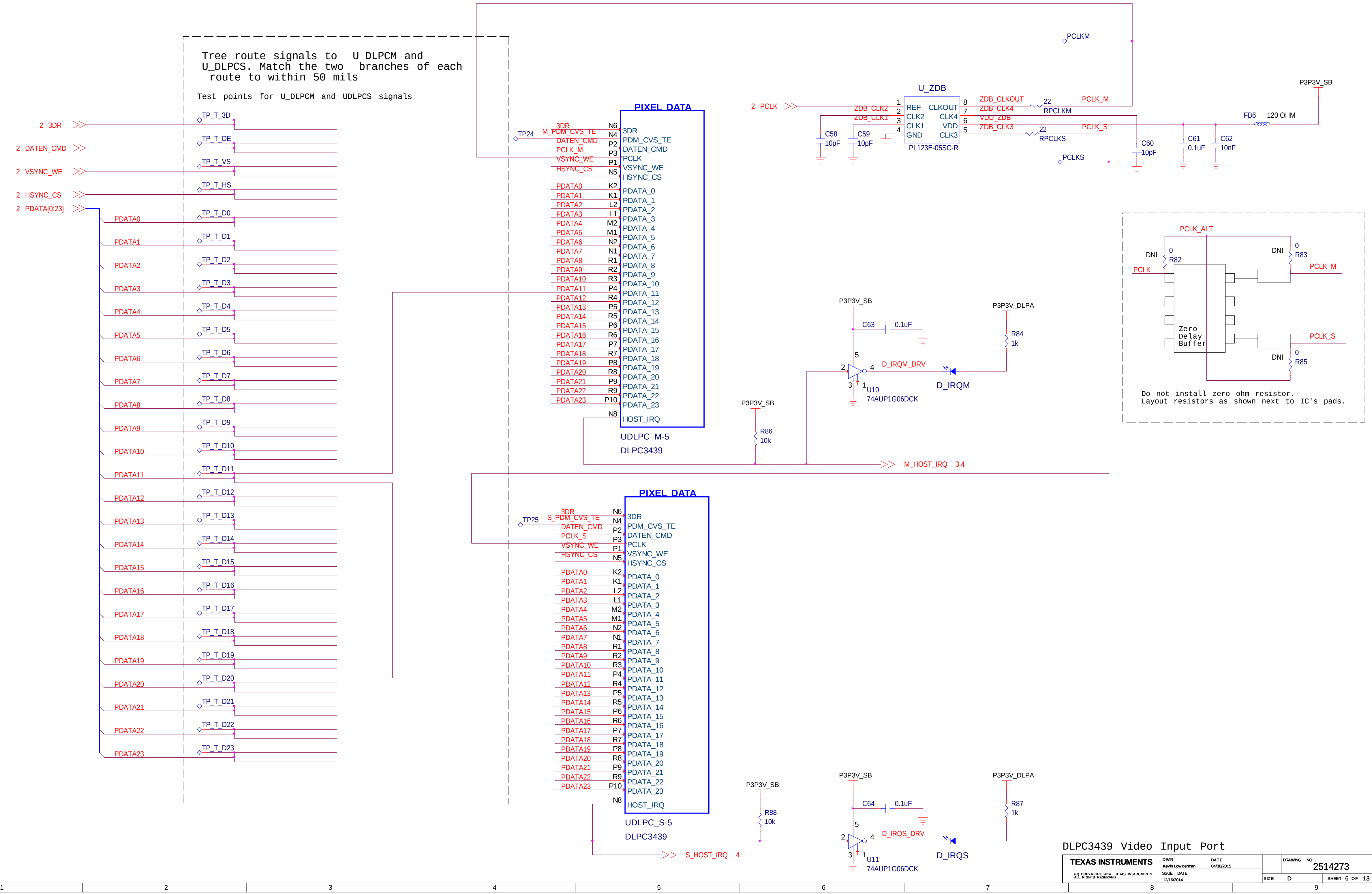

Tree route signals to U_DLPCM and U_DLPCS. Match the two branches of each route to within 50 mils

Test points for U_DLPCM and UDLPCS signals

Do not install zero ohm resistor. Layout resistors as shown next to IC's pads.

DLPC3439 Video Input Port

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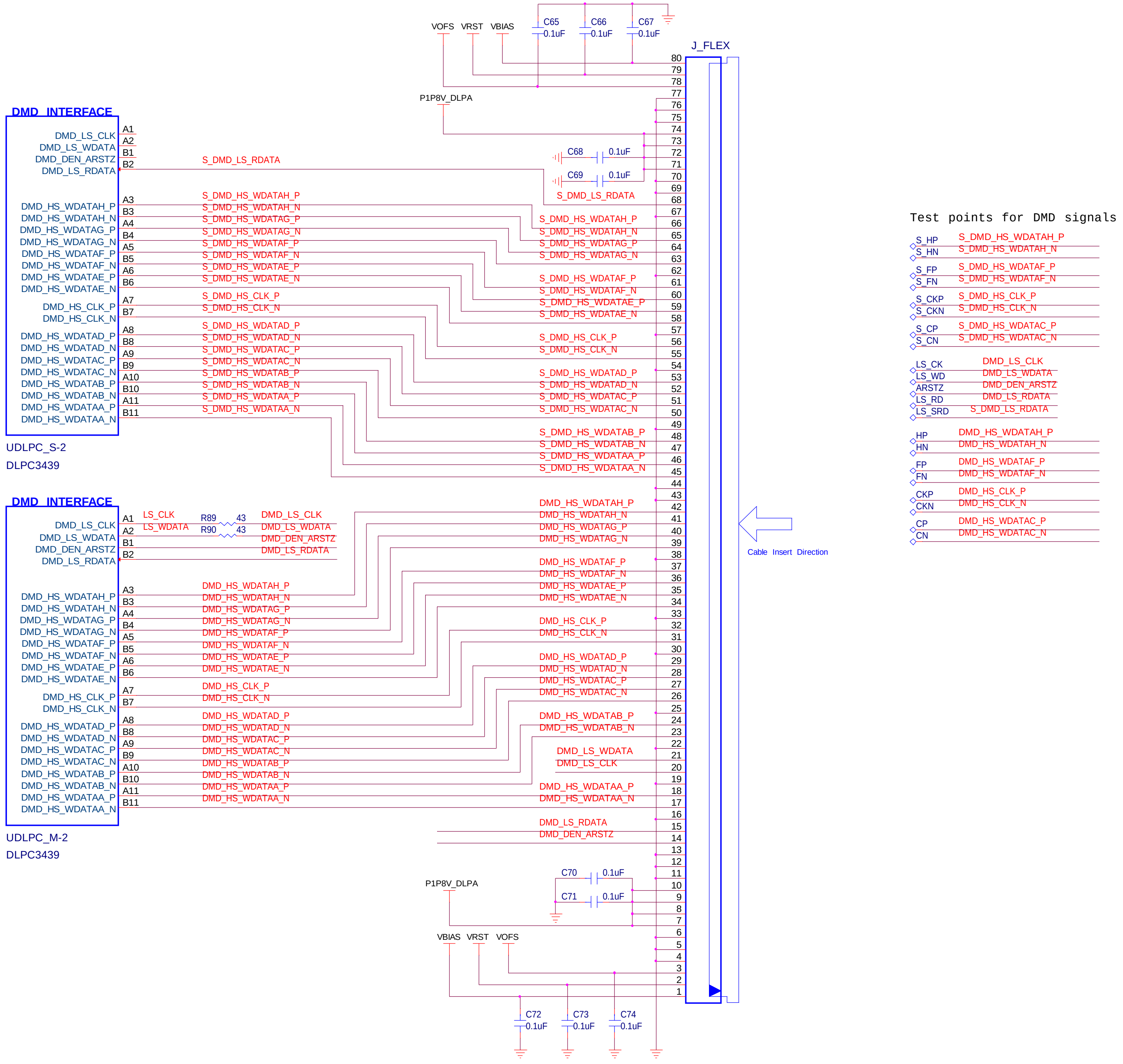

DLPC3439 DMD Interface

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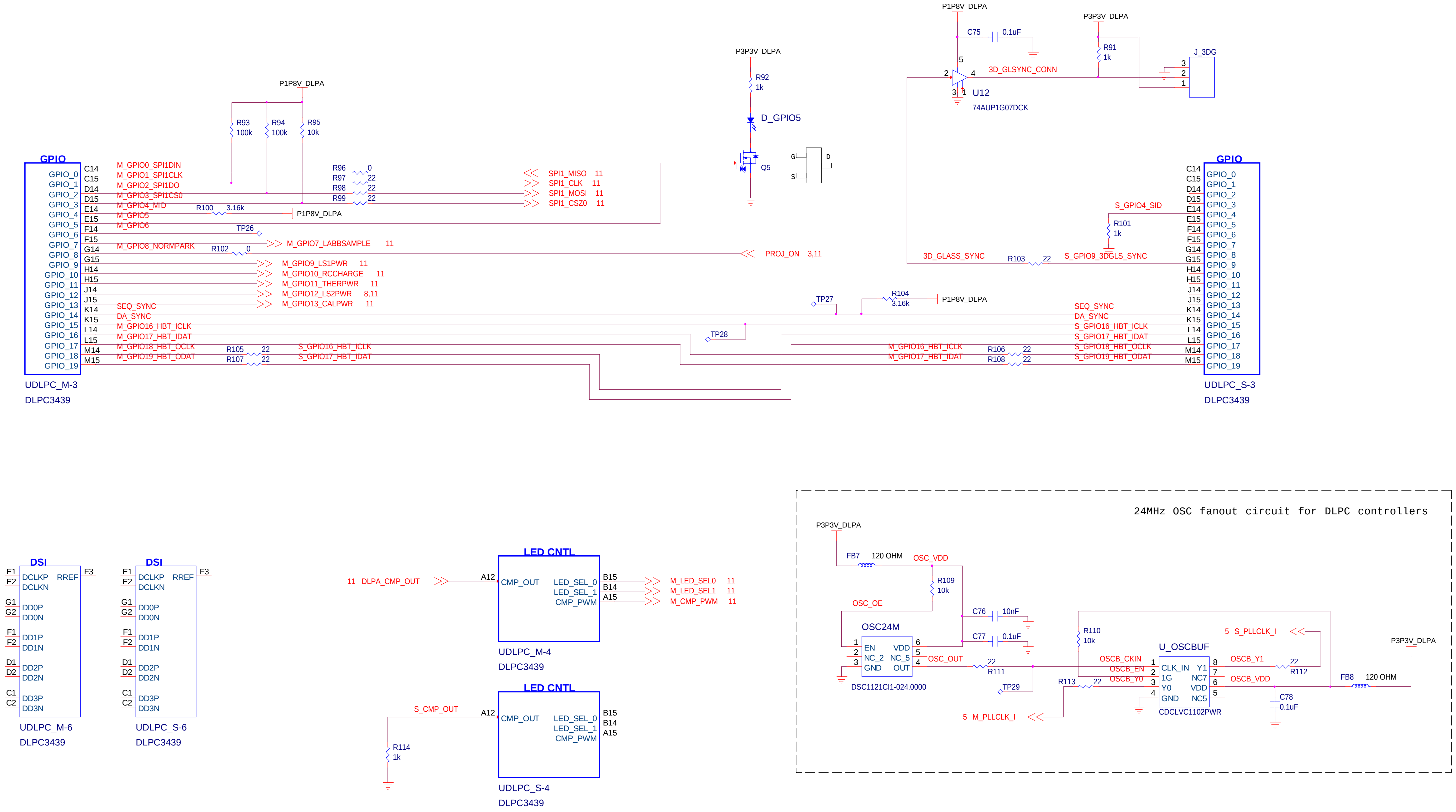

DLPC3439 Dual ASIC Intf & GPIOs

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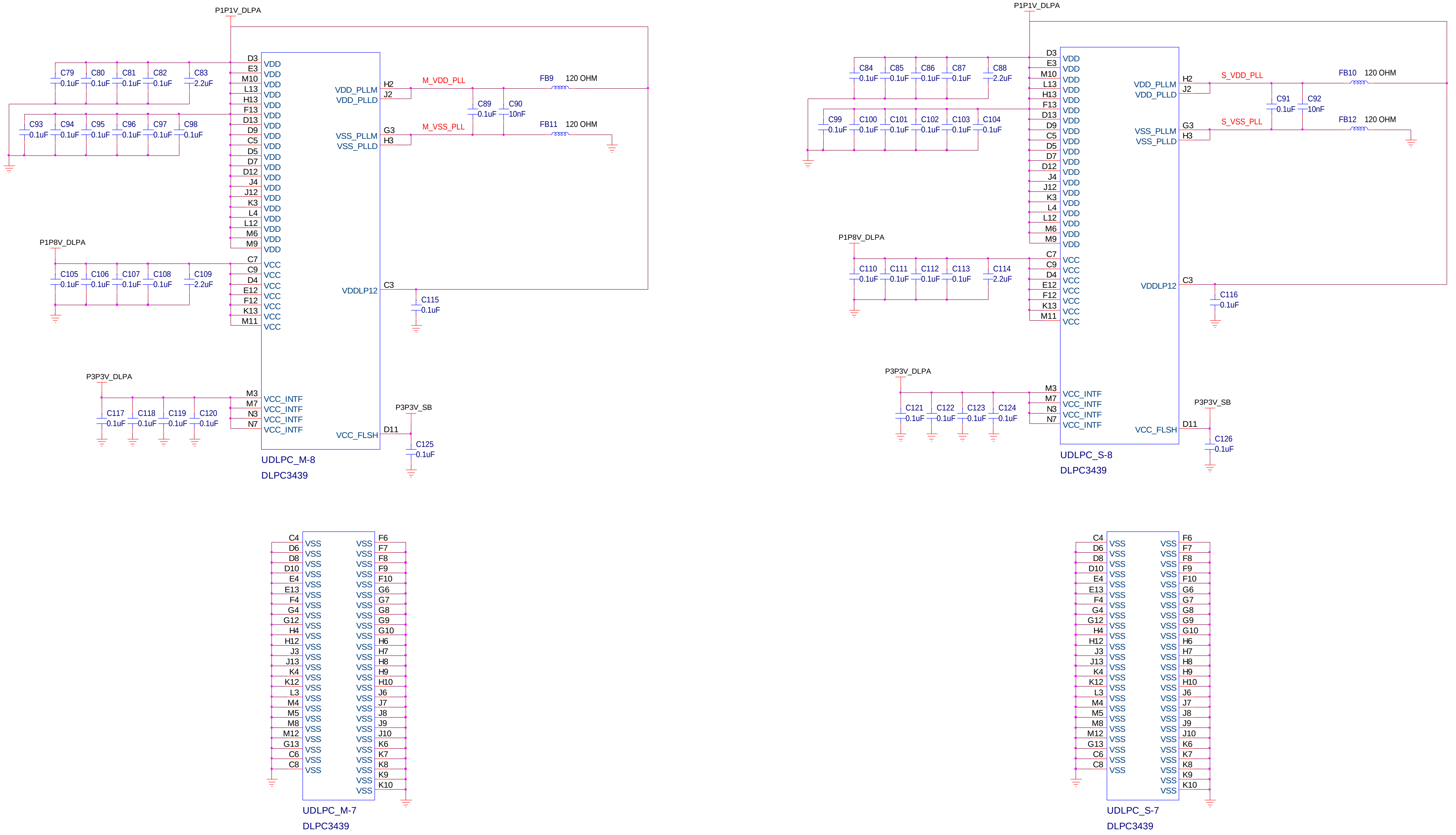
REV B		



DLC3439 Power/Ground

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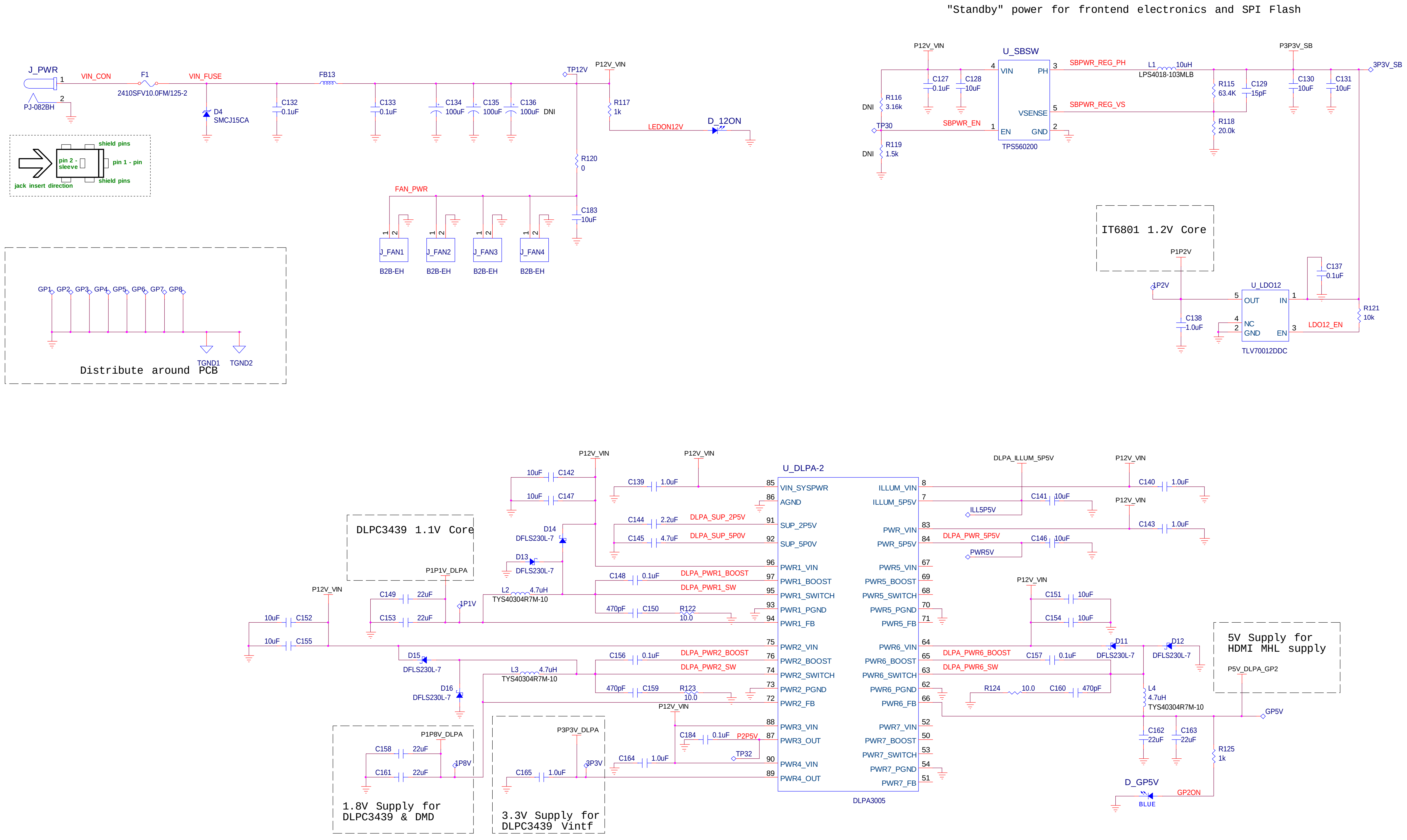

12V Supply In, 3.3V Standby, DLPA 5V 3.3V 1.8V 1.1V, LDO 1.2V

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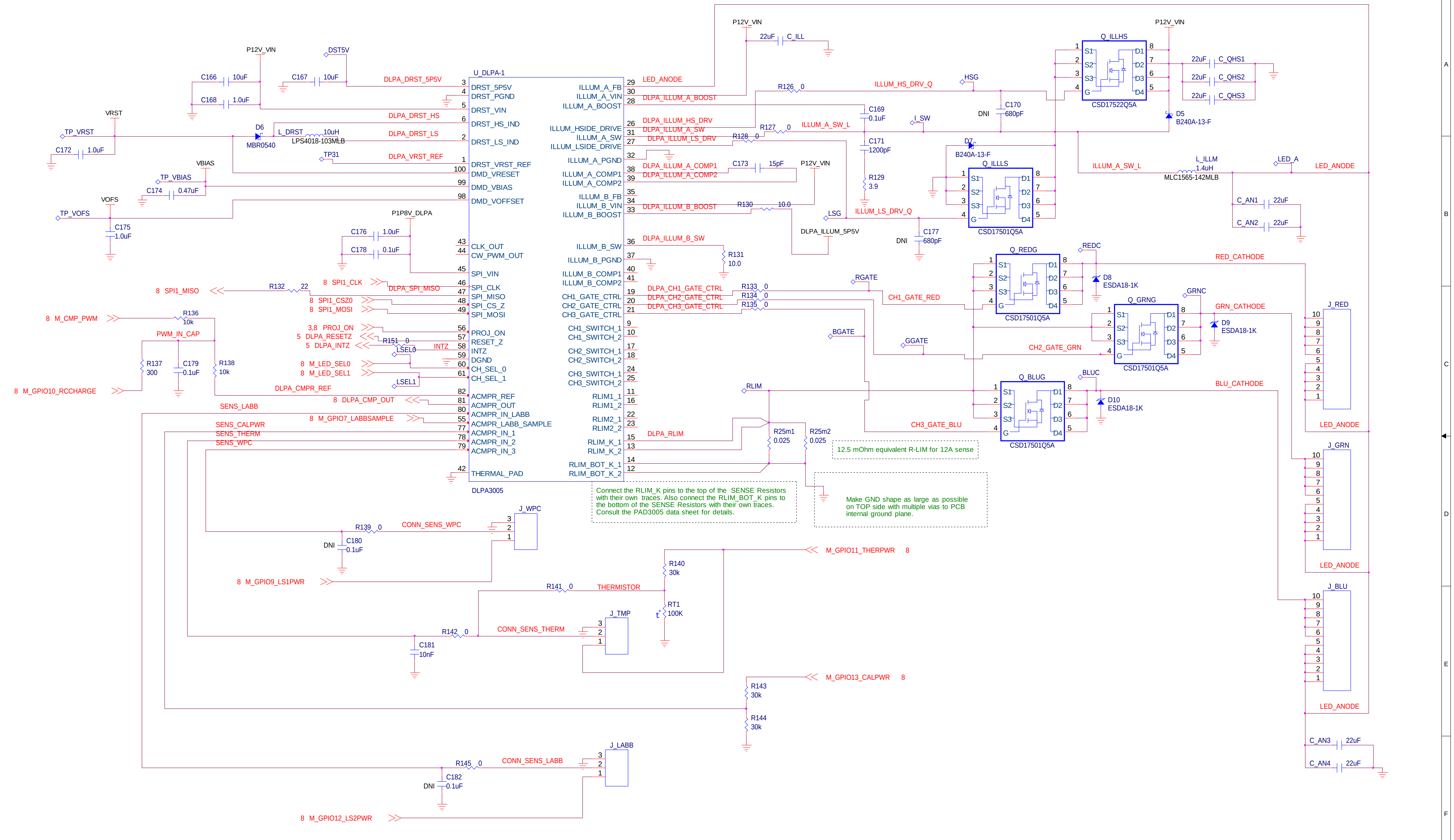

12.5 mOhm equivalent R-LIM for 12A sense

Connect the RLIM_K pins to the top of the SENSE Resistors with their own traces. Also connect the RLIM_BOT_K pins to the bottom of the SENSE Resistors with their own traces. Consult the PAD3005 data sheet for details.

Make GND shape as large as possible on TOP side with multiple vias to PCB internal ground plane.

DLPA 12A LED Driver Circuit

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Revisions

Rev. A : Initial Release

Rev. B : Initial Release
- Page 3 : R32 changed to DNI. Added "MSP_ENABLE_I2CPOLL" for test & debug purpose
- Page 5 : install JRST for test & debug purpose
- Page 10 : add connection for DLPA3005 pin 87,88
- Page 11 : add zero ohm resistor for DLPA_INTZ for test & debug purpose

REVISION NOTES

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