

Revision History	
Revision	Notes

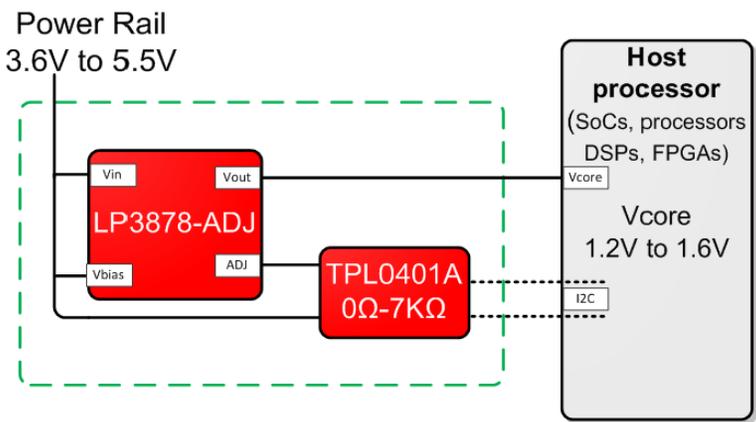
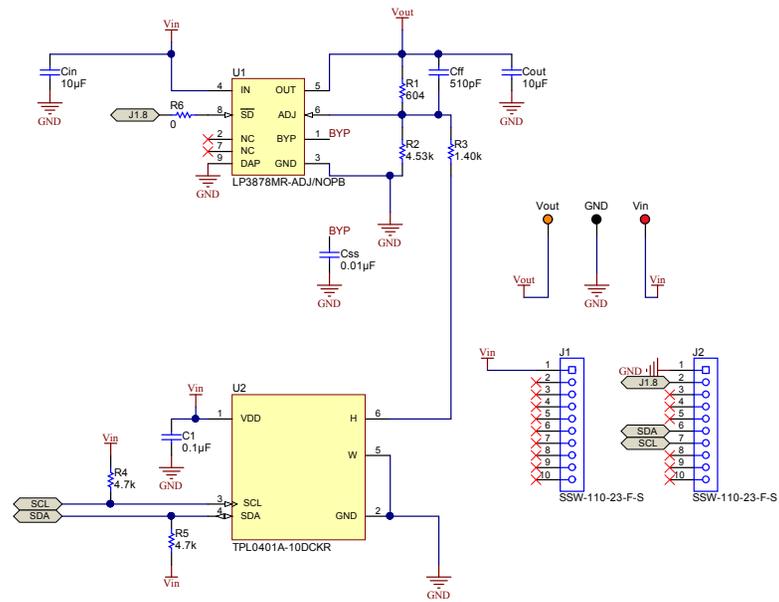


Figure 1 Comprehensive block diagram



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Orderable: N/A	Designed for: Public Release	Mod. Date: 4/14/2015
TID #: TIDA00531	Project Title:	
Number: TIDA-00531 Rev: E1	Sheet Title:	
SW Rev: Version control disabled	Assembly Variant: 001	Sheet: 2 of 3
Drawn By:	File: TIDA_DVS_SchDoc	Size: B
Engineer: Antony Carvajales	Contact: http://www.ti.com/support	



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~~DNE~~
FID1

~~DNE~~
FID2

~~DNE~~
FID3

PCB Number: TIDA-00531
PCB Rev: E1

PCB
LOGO
Texas Instruments

PCB
LOGO
Pb-Free Symbol

Label Table	
Variant	Label Text
001	TIDA-00531

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: N/A	Designed for: Public Release	Mod. Date: 4/14/2015
TID #: TIDA00531	Project Title:	
Number: TIDA-00531 Rev: E1	Sheet Title:	
SW Rev: Version control disabled	Assembly Variant: 001	Sheet: 3 of 3
Drawn By:	File: PCB Project_TID_Hardware_SchDoc	Size: B
Engineer: Antony Carvajales	Contact: http://www.ti.com/support	

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