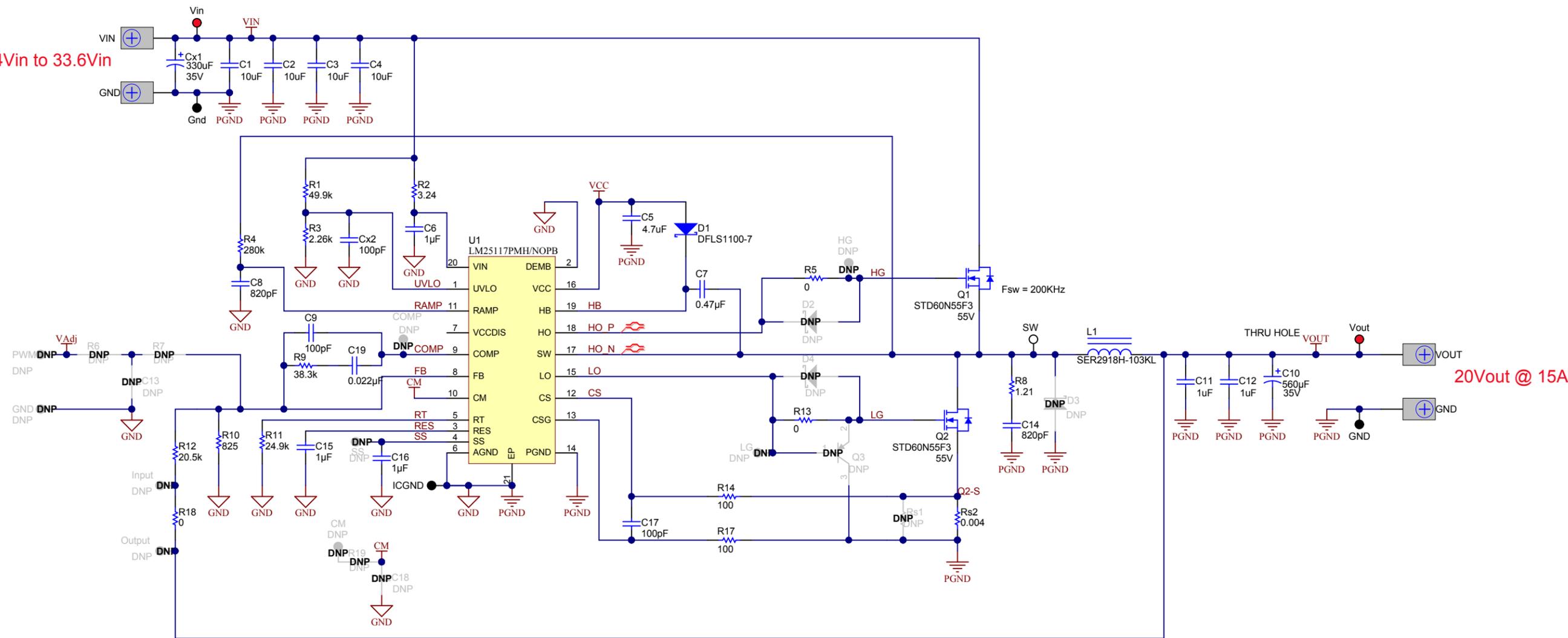


Input Voltage = 30.4Vin to 33.6Vin



**\*NOTES:**

1. Cx1 is a bulk capacitor that was used to dampen the input supply. When building this design please ensure to use input bulk capacitor(s) that can handle a total of 7.5Arms.
2. All components with an "x" suffix are components that were not in the original PCB design (i.e. do not have pads on the PCB) and were kluged into the circuit.
3. C6 can be replaced by a 50V rated capacitor (still needs to be a ceramic, 1uF, X7R rated device).
4. Built on the PMP9280 Rev1 PCB.

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Number: PMP10514	Rev: A	Designed for: Public Release	Mod. Date: 4/2/2015
SVN Rev: Version control disabled	Sheet: 1 of 2	Project Title: LM25117 Single-Phase Synchronous Buck	Sheet Title: Schematic
Drawn By:	Engineer: Hrag Kasparian	Assembly Variant: 001	File: PMP10514 Schematics.SchDoc
Size: B	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	Size: B	© Texas Instruments 2014

H1 NY PMS 440 0025 PH H2 NY PMS 440 0025 PH H3 NY PMS 440 0025 PH H4 NY PMS 440 0025 PH

H5 1902C H6 1902C H7 1902C H8 1902C

DNP FID1 DNP FID2 DNP FID3

PCB Assembly  
PMP9280 Rev1 PCB

PCB LOGO  
Texas Instruments

Label Table	
Variant	Label Text
001	

LBL1  
PCB Label  
Size: 0.65" x 0.20 "

ZZ1  
Label Assembly Note  
This Assembly Note is for PCB labels only

ZZ2  
Assembly Note  
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3  
Assembly Note  
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4  
Assembly Note  
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Number: PMP10514		Rev: A	Designed for: Public Release	Mod. Date: 3/31/2015
SVN Rev: Version control disabled		Project Title: LM25117 Single-Phase Synchronous Buck		
Drawn By:		Assembly Variant: 001	Sheet: 2 of 2	
Engineer: Hrag Kasparian		File: PMP10514_Hardware_ANSI-B.SchDoc	Size: B	http://www.ti.com
		Contact: http://www.ti.com/support	© Texas Instruments 2014	



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