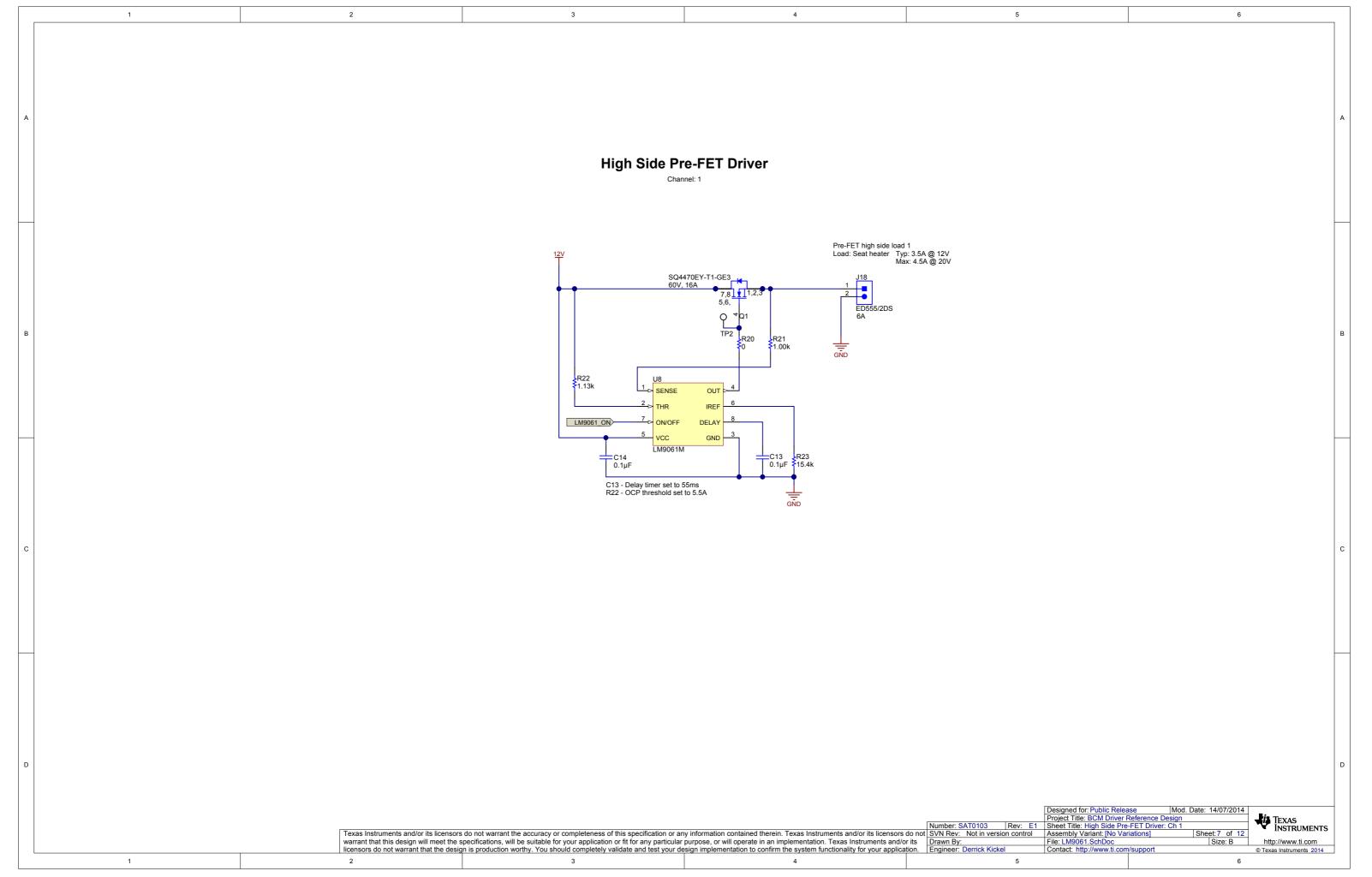
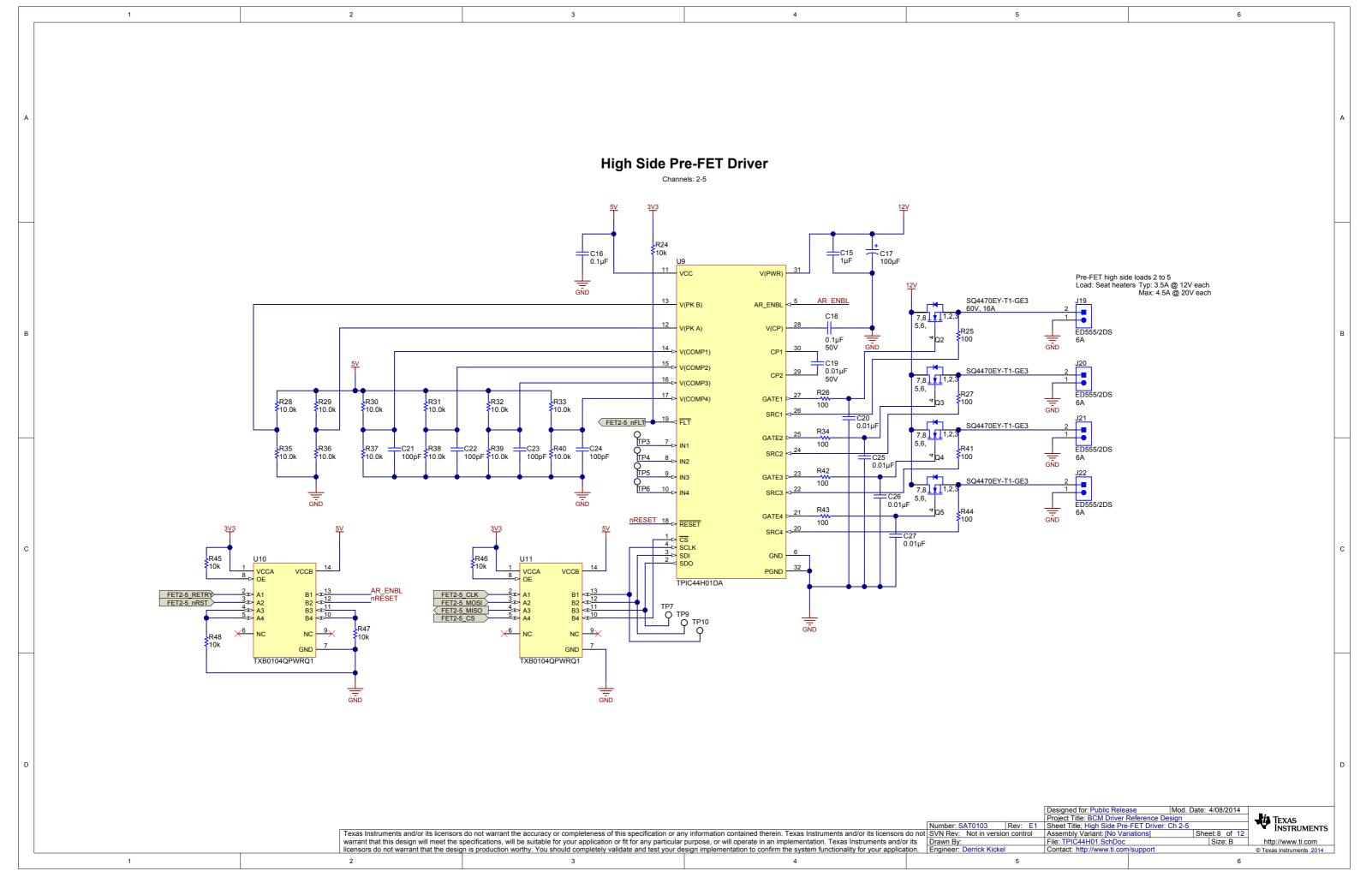


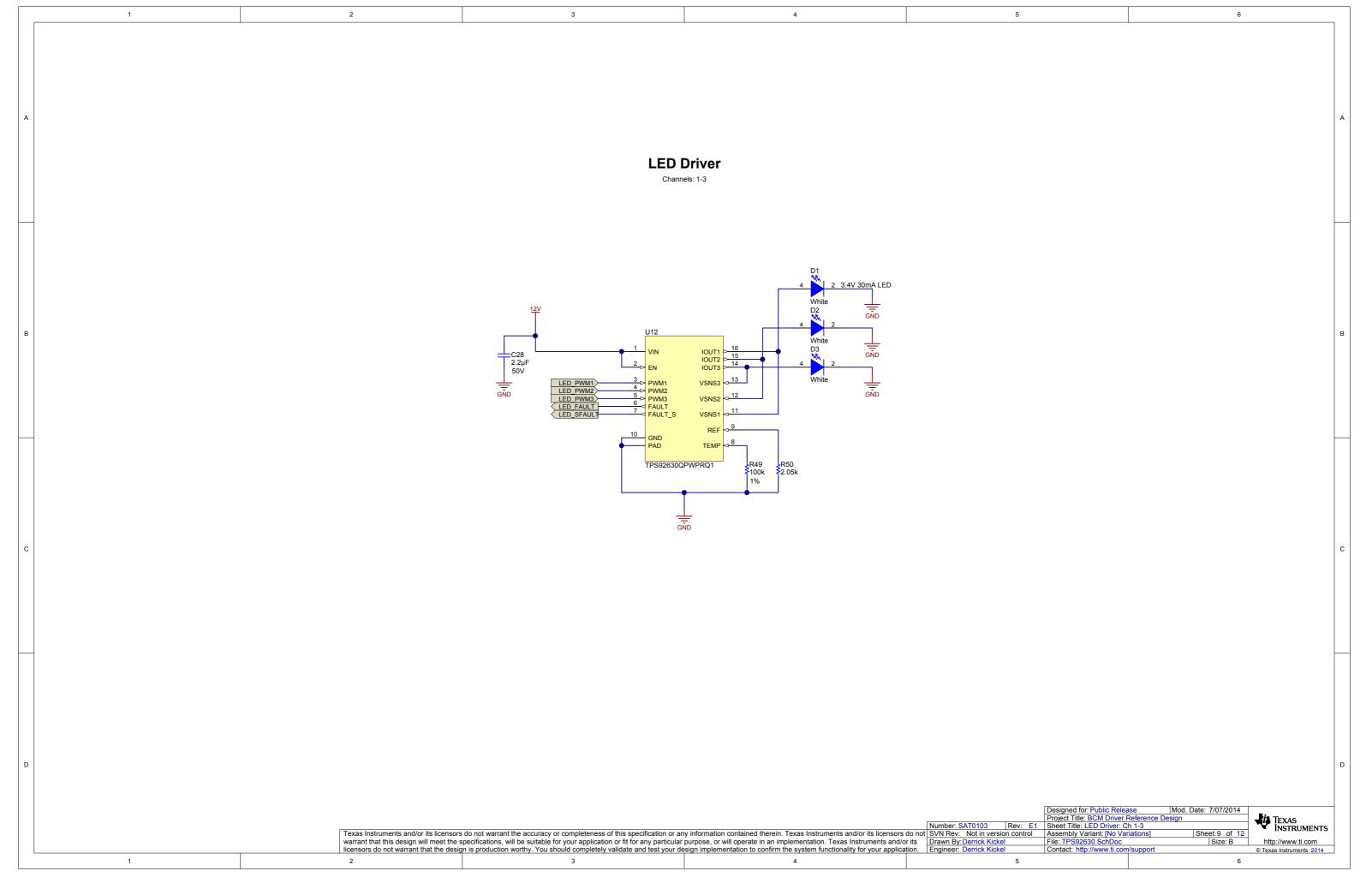
High Side/Low Side Driver HS Channels: 6-7 LS Channels: 6-7 HS Load 6, 7 Load: Door lock actuator (2x 250mA relay) <u>3V3</u> Device max. output: 2x 650mA 4x 540mA 2x 1200mA (parallel mode) C10 0.1µF 6.3V Jump J12 & J17 for parallel mode, leave open for normal operation 0.1µF 50V PEC02SAAN VM_FET HS1 7 SLEEP
MODE_SEL
INFO_SEL1 HSLS MODE
HSLS INFO1
HSLS INFO2 ED555/2DS 9 INFO_SEL2
12 FAULT_RST •• J17 HSLS_INFO INFO_OUT PEC02SAAN GND HSLS_nFAULT PAD LS Load 6, 7 DRV8865QPWPRQ1 Mode Sel = 0; Device in RETRY mode (OCP flag clears after 200ms) 100k resistor in series with MODE_SEL, selects parallel mode Project Title: BCM Driver Reference Design

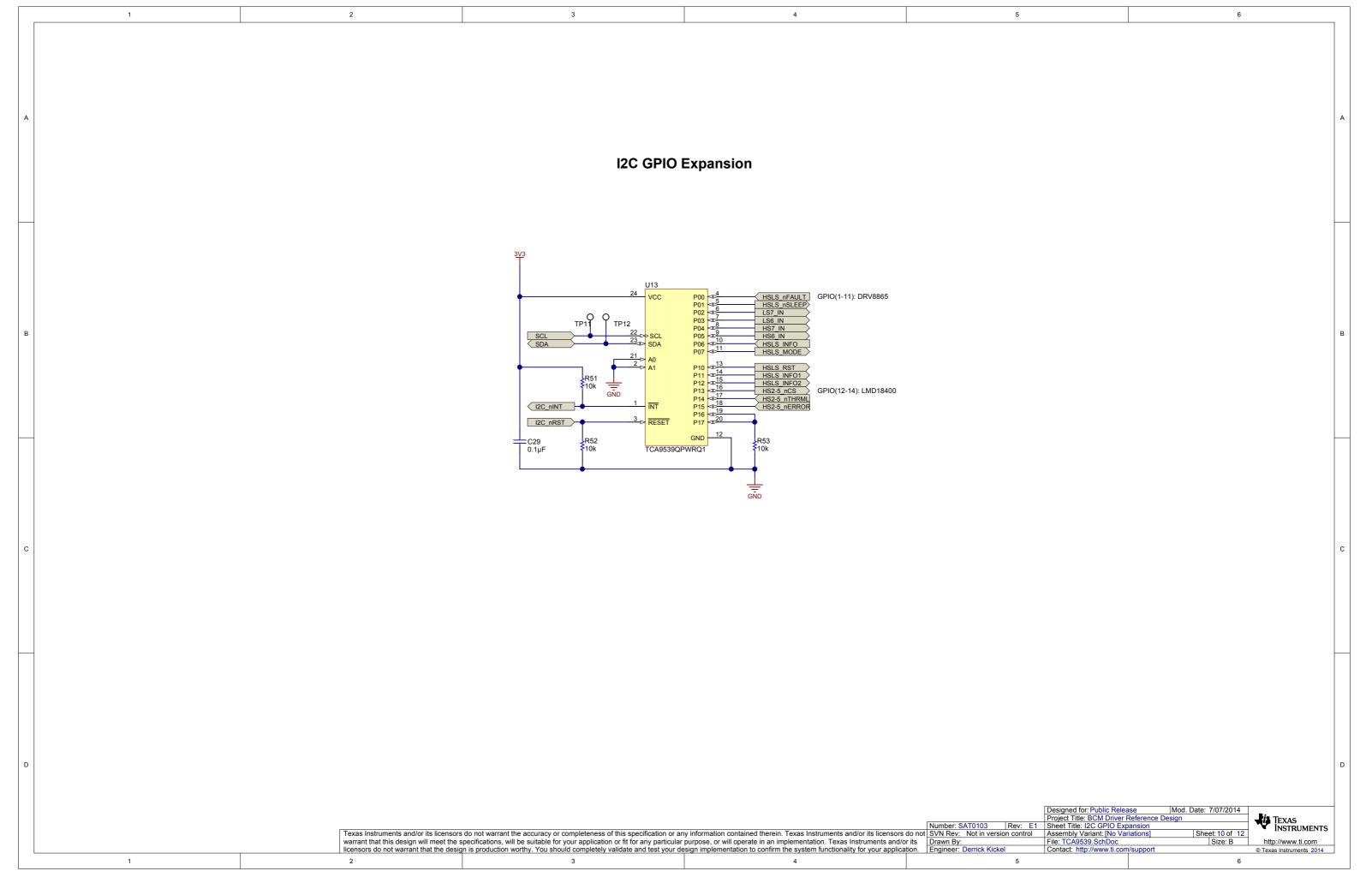
Number: SAT0103 Rev: E1 Sheet Title: High Side/Low Side Driver: HS Ch 6-7, LS Ch 6-7

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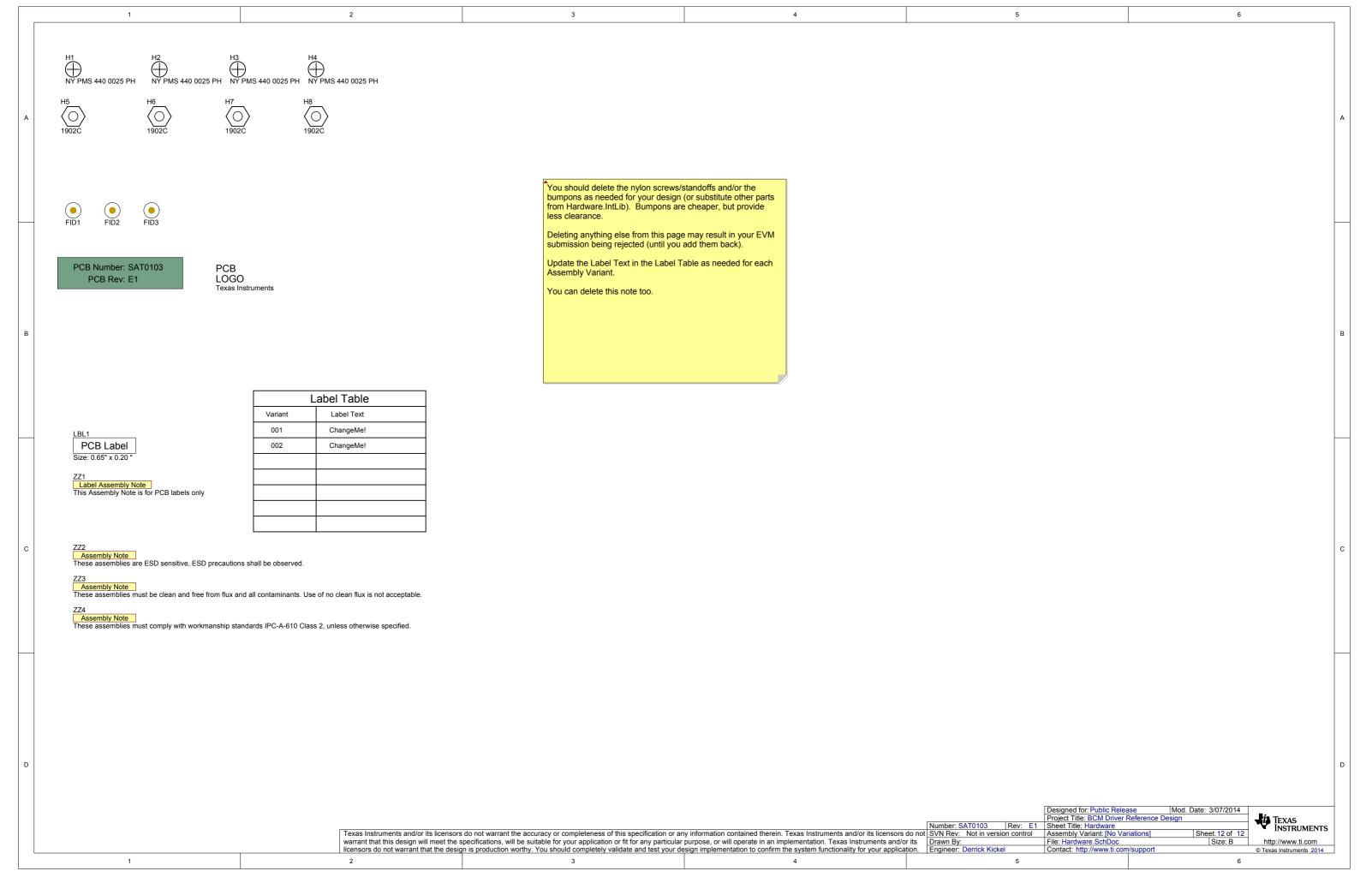








2 3 Power Car Battery 12V typical 9-20V typical continuous range 40V load dump transients 12V_Batt U14 TPS7A6650QDGNRQ1 0 2 TLV70033QDDCRQ1 VOUT ED120/2DS R54 R55 15A 10k GND MBRB2545CTT4G C31 1µF 6.3V GND C30 1µF 50V C33 1µF 10V C32 10µF 10V 30A, 45V GND -C34 1000pF R57 665 C35 0.1µF 10V SN74LVC1G07QDBVRQ1 Mod. Date: 14/07/2014 TEXAS INSTRUMENTS | Number: SAT0103 | Rev: E1 | Shed briver reference | Number: SAT0103 | Rev: E1 | Shed briver reference | Number: SAT0103 | Rev: E1 | Shed briver reference | Section Sheet: 11 of 12 warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application. File: Power.SchDoc Contact: http://www.ti.com/support Size: B http://www.ti.com © Texas Instruments 2014 2 5 3



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