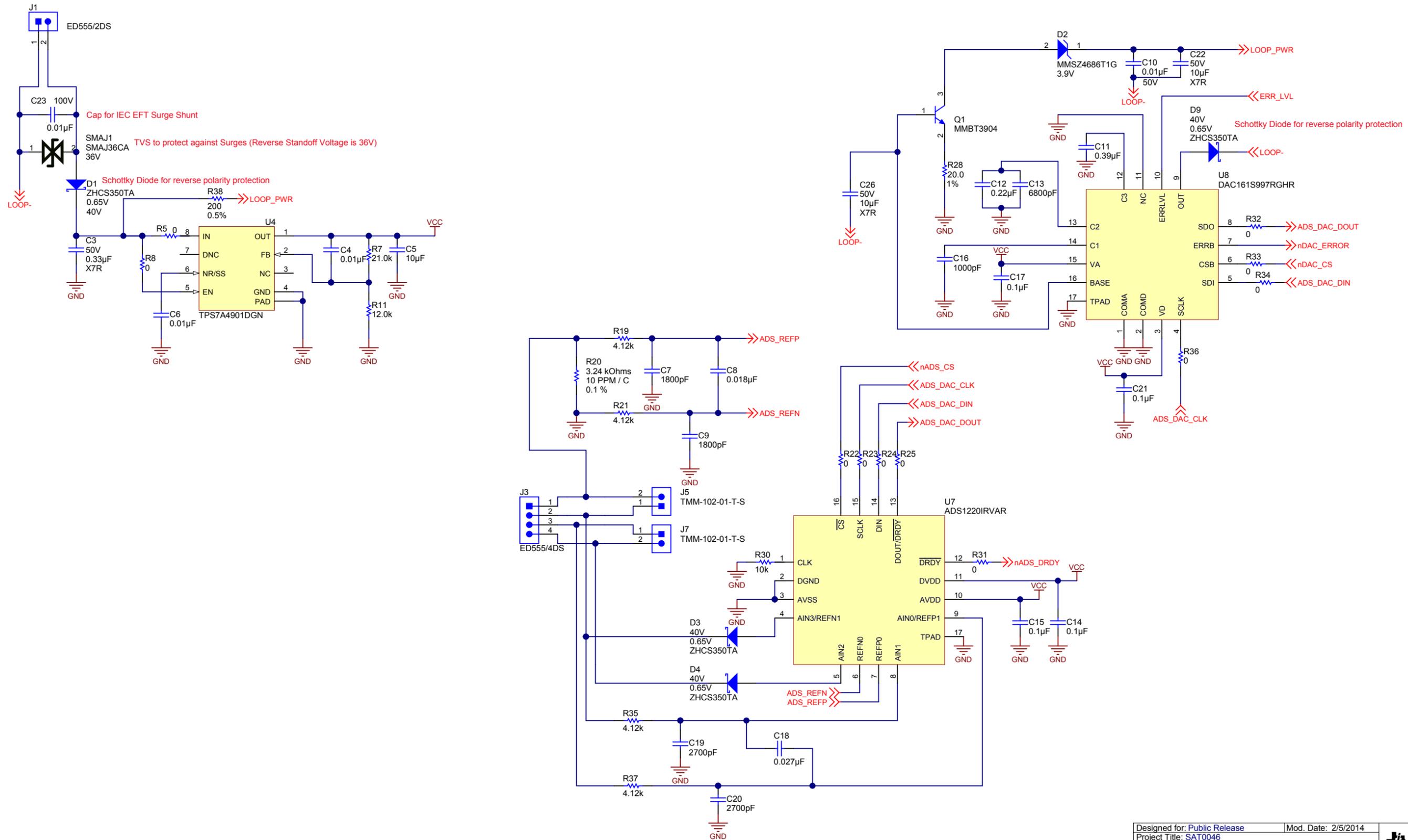


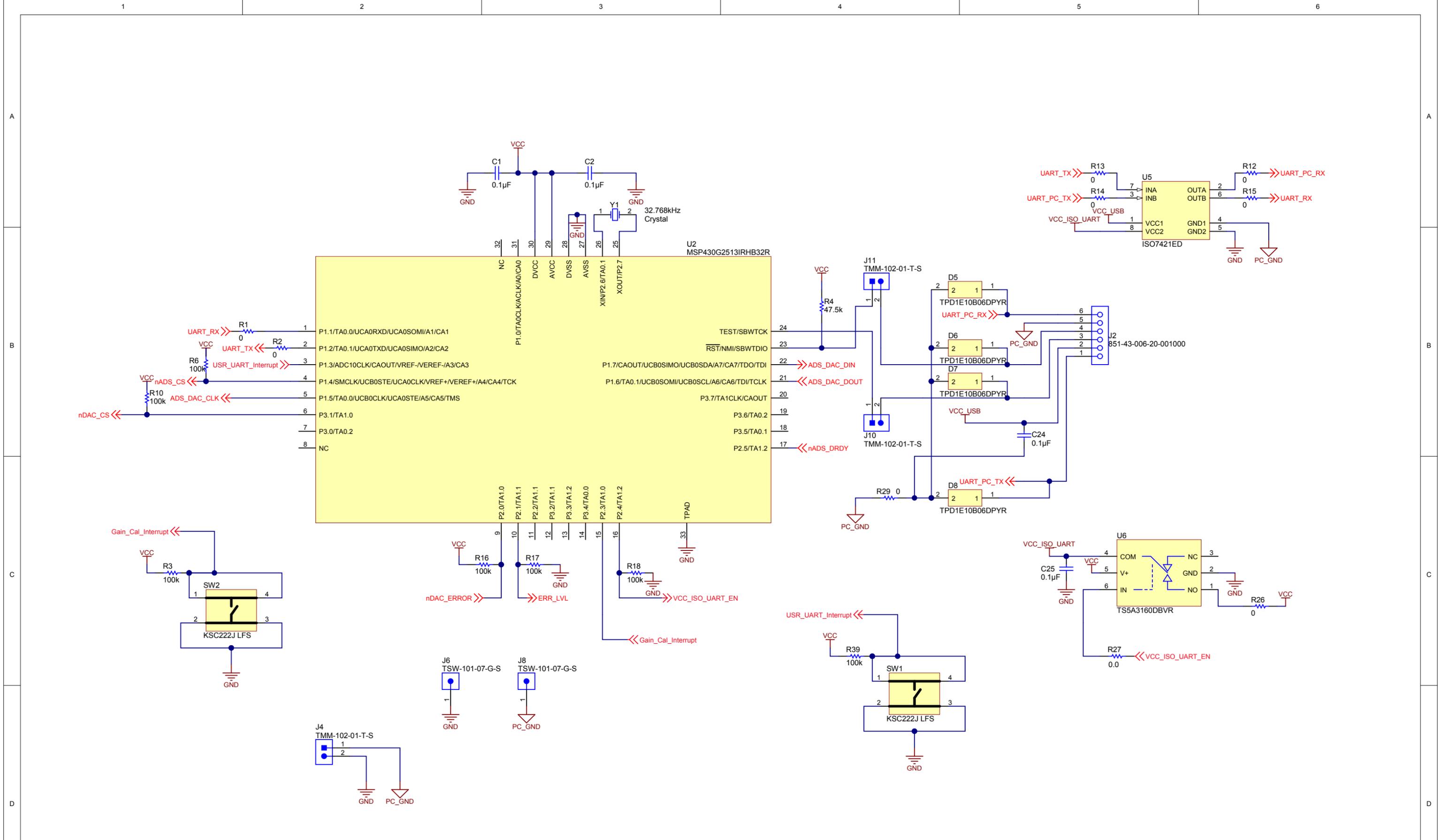
All CAPS ARE X7R..VOLTAGE SAFETY FACTOR IS AT LEAST:2



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|                                 |                                              |                                                                            |
|---------------------------------|----------------------------------------------|----------------------------------------------------------------------------|
| Number: SAT0046                 | Rev: E4                                      | Mod. Date: 2/5/2014                                                        |
| SVN Rev: Not in version control | Project Title: SAT0046                       | Sheet Title: Temp Transmitter System                                       |
| Drawn By: Ajinder Singh         | Assembly Variant: 001                        | Sheet: 1 of 1                                                              |
| Engineer: Ajinder Singh         | File: SAT0046_E4_Temp_Transmitter_System.Sch | Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a> |





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|---------------------------------|---------|----------------------------------------------------------------------------|---------------------|
| Number: SAT0046                 | Rev: E4 | Designed for: Public Release                                               | Mod. Date: 2/5/2014 |
| SVN Rev: Not in version control |         | Project Title: SAT0046                                                     |                     |
| Drawn By: Ajinder Singh         |         | Sheet Title: MSP430                                                        |                     |
| Engineer: Ajinder Singh         |         | Assembly Variant: 001                                                      | Sheet: 1 of 1       |
|                                 |         | File: SAT0046_E4_MSP430_SchDoc                                             | Size: B             |
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FID1



FID4

PCB Number: SAT0046  
PCB Rev: E4

PCB  
LOGO  
Texas Instruments



H1

SJ61A1



H2

SJ61A1



H3

SJ61A1



FID2



FID5



FID3



FID6

ZZ1  
Assembly Note  
Mouse bites are to be located at the 12, 3, 6, and 9 o'clock orientations.

ZZ2  
Assembly Note  
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3  
Assembly Note  
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4  
Assembly Note  
These assemblies must comply with workmanship standards IPC-A-610 Class 2., unless otherwise specified.

ZZ5  
Assembly Note  
Assembly: Three bumpons are to be placed where possible to provide a balanced standoff

ZZ6  
Assembly Note  
Polygon pour along the board edge is serving as an ESD ring. Soldermask is not to be placed over the ring on either side.

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|                                 |  |         |  |                                                                            |  |                       |  |
|---------------------------------|--|---------|--|----------------------------------------------------------------------------|--|-----------------------|--|
| Number: SAT0046                 |  | Rev: E4 |  | Designed for: Public Release                                               |  | Mod. Date: 11/25/2013 |  |
| SVN Rev: Not in version control |  |         |  | Project Title: SAT0046                                                     |  |                       |  |
| Drawn By: Ajinder Singh         |  |         |  | Sheet Title: Hardware                                                      |  |                       |  |
| Engineer: Ajinder Singh         |  |         |  | Assembly Variant: 001                                                      |  | Sheet: 1 of 1         |  |
|                                 |  |         |  | File: Hardware.SchDoc                                                      |  | Size: B               |  |
|                                 |  |         |  | Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a> |  |                       |  |



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