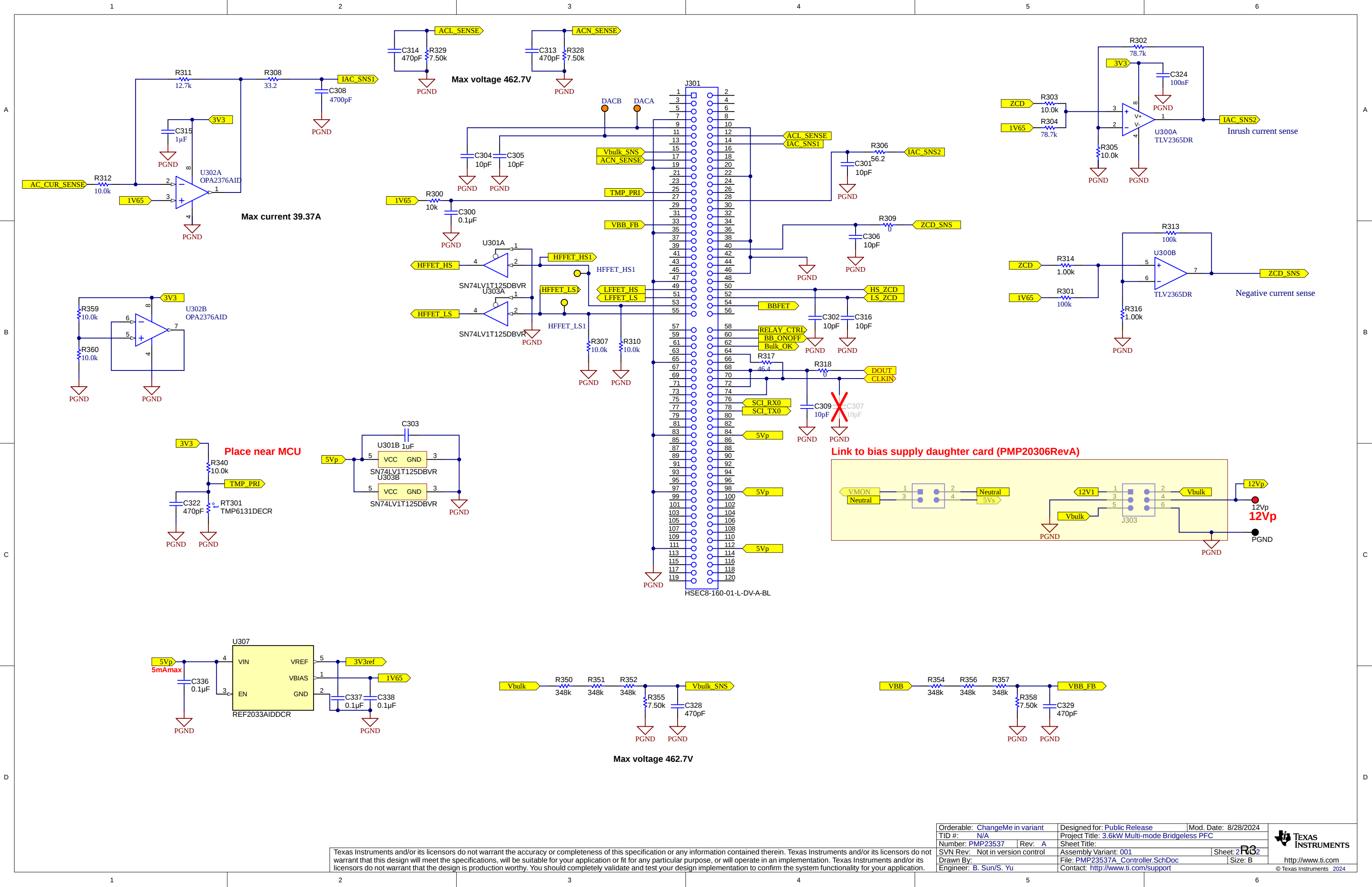


1. Need to check part availability when request to build.
2. This board uses boot strap gate driver for LF FETs, does not support negative DC input on Neutral.
3. 4x 180uF bulk cap is applied: C123 and C130 to be placed on top of C114 and C120, respectively.
4. C114, C120, C123, C130 in laid down position.



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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 3/3/2025	
TID #: N/A	Project Title: 3.6KW Multi-mode Bridgeless PFC		
Number: PMP23537	Rev: A	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 2	
Drawn By:	File: PMP23537A_PFC.SchDoc	Size: B	
Engineer: B. Sun/S. Yu	Contact: http://www.ti.com/support		http://www.ti.com © Texas Instruments 2024



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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 8/28/2024
TID #: N/A	Project Title: 3.6kW Multi-mode Bridgeless PFC	
Number: PMP23537	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 32
Drawn By:	File: PMP23537A_Controller.SchDoc	Size: B
Engineer: B. Sun/S. Yu	Contact: http://www.ti.com/support	

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