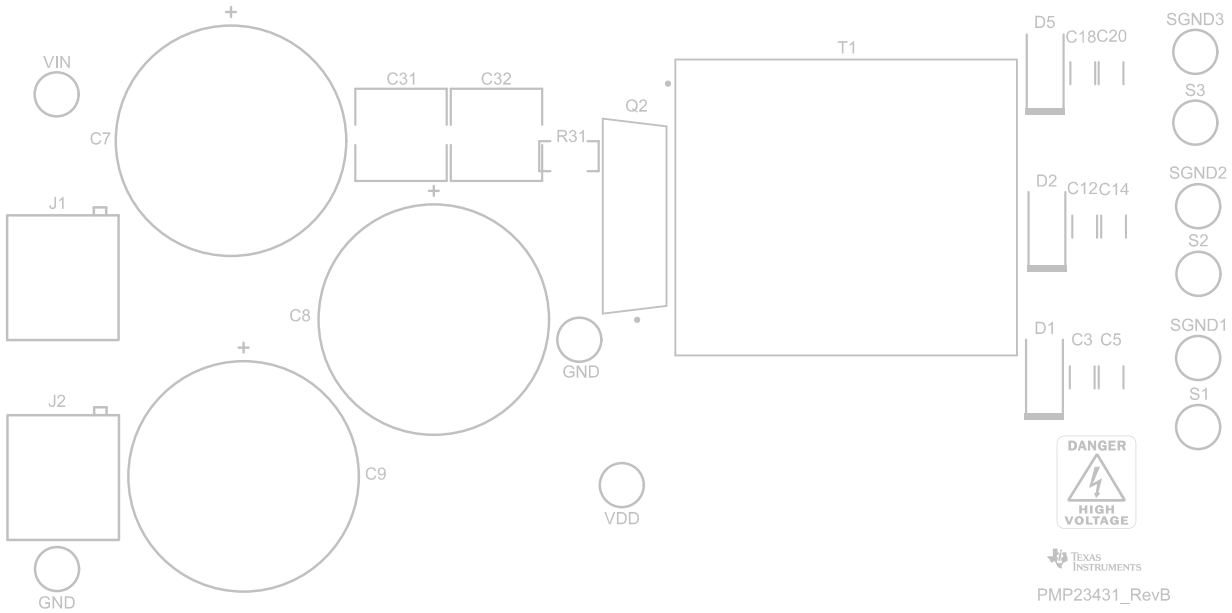
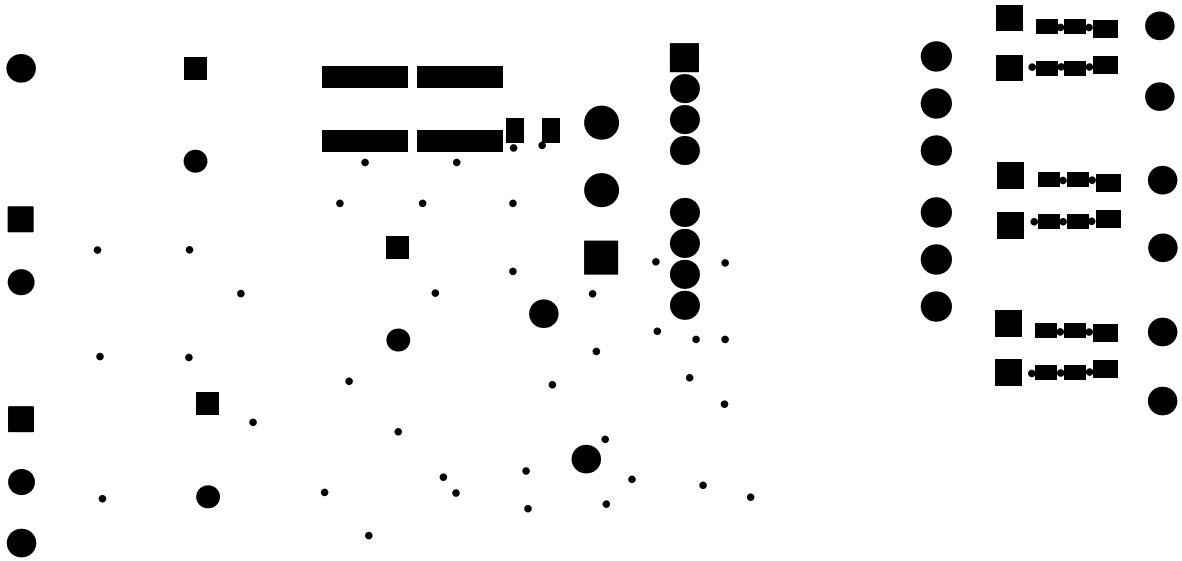
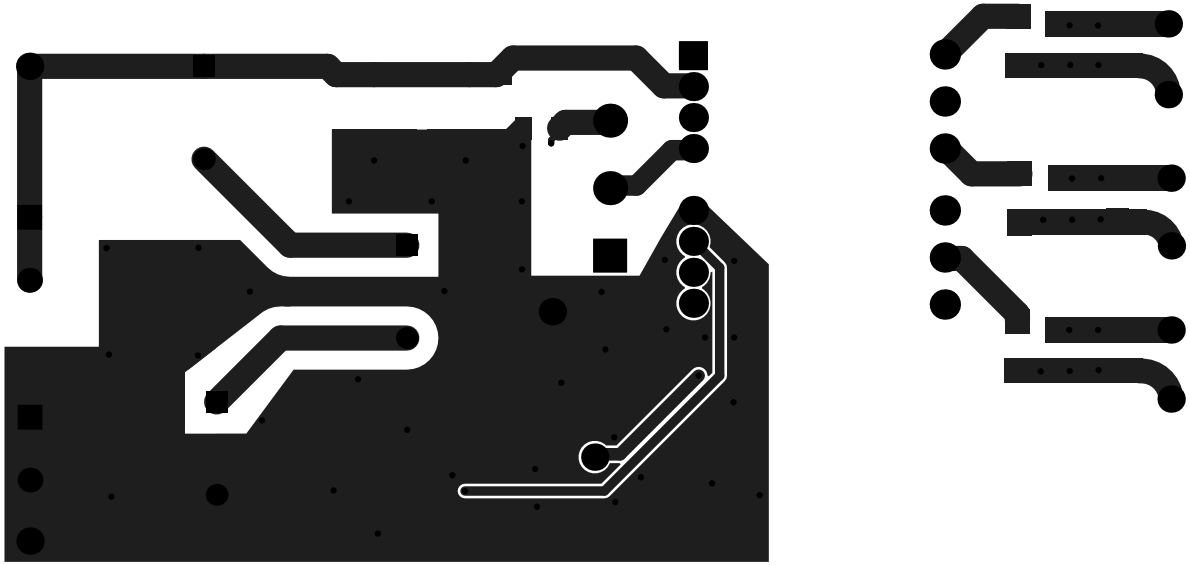

VIN
C7
C31
C32
R31
Q2
T1
D5
C18C20
SGND3
S3
J1
C8
GND
D2
C12C14
SGND2
S2
D1
C3 C5
SGND1
S1
J2
C9
VDD
GND
DANGER
HIGH VOLTAGE
Texas Instruments
PMP23431_RevB

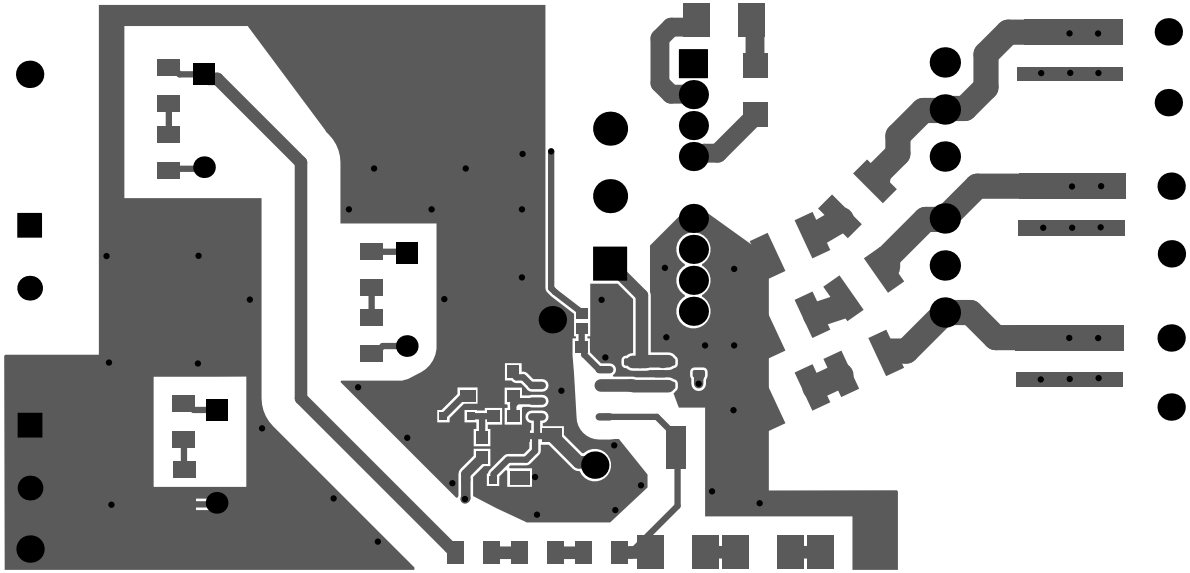
Top Overlay



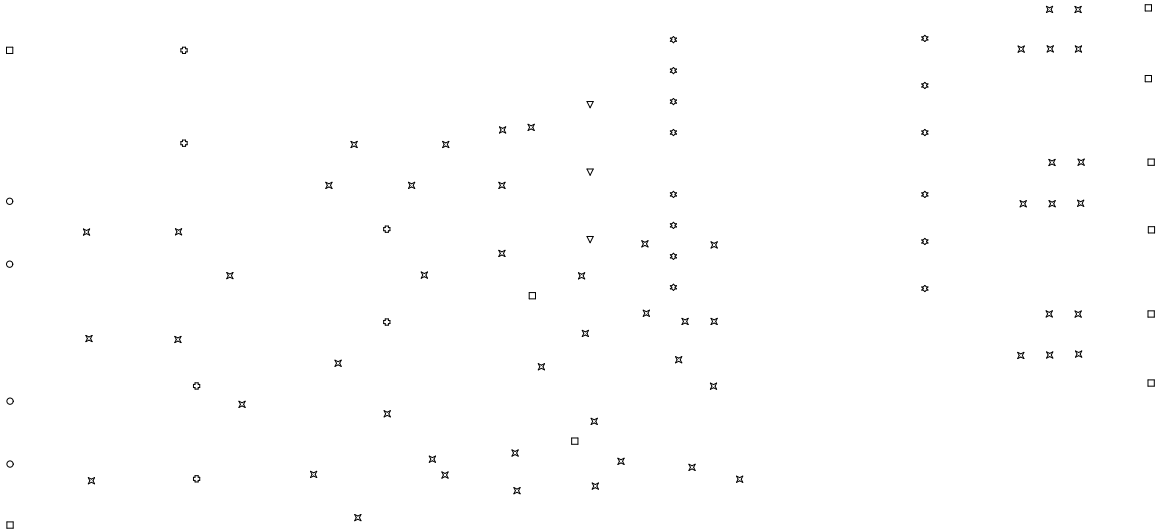
Top Solder



Top Layer

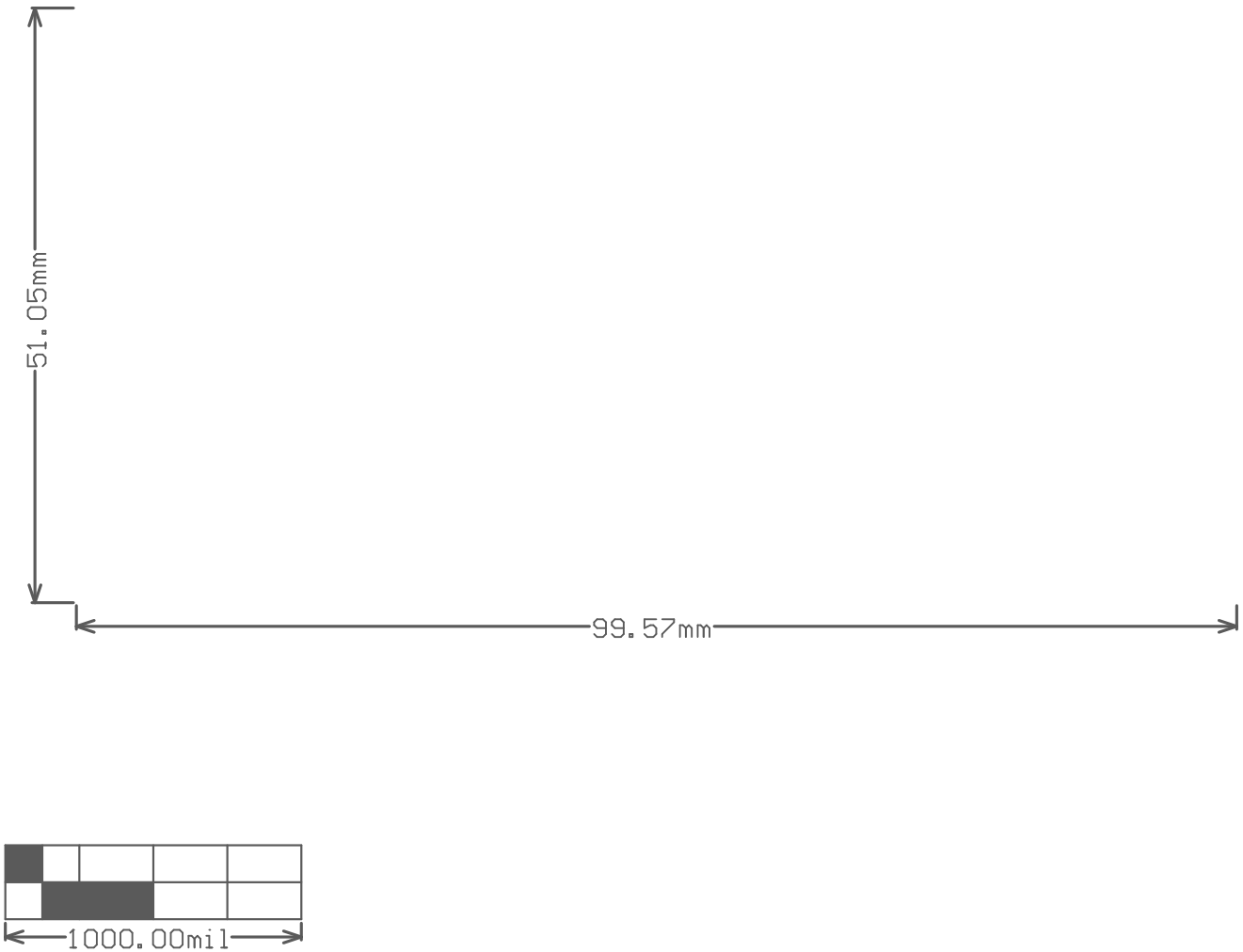


Bottom Layer



Drill Drawing

Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
⌘	54	10.00mil (0.254mm)	PTH	Round	Top Layer - Bottom Layer	
✚	6	43.31mil (1.100mm)	PTH	Round	Top Layer - Bottom Layer	
○	4	49.21mil (1.250mm)	PTH	Round	Top Layer - Bottom Layer	
✡	14	62.99mil (1.600mm)	PTH	Round	Top Layer - Bottom Layer	
□	10	63.00mil (1.600mm)	PTH	Round	Top Layer - Bottom Layer	
▽	3	70.87mil (1.800mm)	PTH	Round	Top Layer - Bottom Layer	
	91 Total					



M2 Board Dimensions

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