

LP-EM-CC2340R53 LaunchPad Development Kit for SimpleLink Bluetooth 5.3 Low Energy MCU



Description

This LaunchPad™ development kit speeds up development with the SimpleLink™ Bluetooth® Low Energy MCU with support for Bluetooth 5 Low Energy (LE), IEEE 802.15.4 PHY and MAC, and 2.4GHz proprietary protocols. [SimpleLink Low Power F3 software development kit \(SDK\)](#) provides software support.

Get Started

1. Order the [LP-EM-CC2340R53](#) device.
2. Get the latest [software development kit \(SDK\)](#).
3. Download the comprehensive [reference design files](#) in the TI reference design page.
4. See the latest [CC2340R53](#) product page.

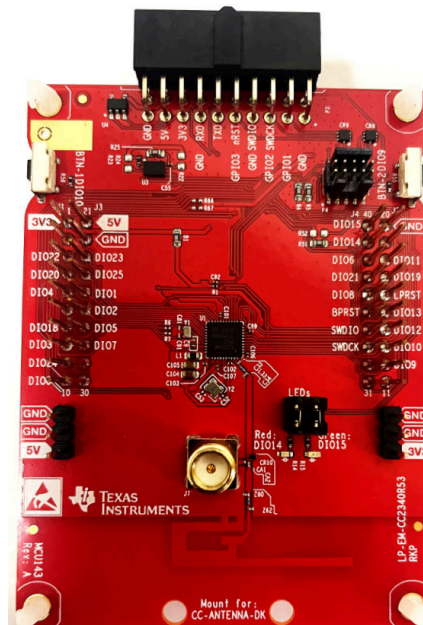
Features

- CC2340R53 wireless MCU
- 32-bit Arm® Cortex®-M0+ processor
- Up to +8dBm output power
- 2.4GHz PCB antenna with SMA connector for external antennas

- 20-pin LP-EM Debug connector for a LaunchPad XDS110 Debugger ([LP-XDS110](#) or [LP-XDS110ET](#), sold separately and required for software development and RF evaluation)
- 40-pin dual-gender BoosterPack™ connectors
- Two LEDs
- Two user buttons
- Access to all I/O signals with the BoosterPack plug-in module connectors
- Connect your LaunchPad development kit to your smartphone using TI SimpleLink Connect

Applications

- [Medical](#)
- [Building automation](#)
- [Lighting](#)
- [Factory automation and control](#)
- [Retail automation and payment—electronic point of sale](#)
- [Grid infrastructure](#)
- [Communication equipment](#)
- [Personal electronics](#)
- [Industrial](#)



1 Evaluation Module Overview

1.1 Introduction

The CC2340R53 LaunchPad kit ([LP-EM-CC2340R53](#)) brings easy Bluetooth Low Energy connectivity to the LaunchPad ecosystem with the SimpleLink ultra-low power [CC2340R53 wireless MCU](#).

The CC2340R53 is a wireless MCU targeting Bluetooth 5.3 Low Energy, ZigBee®, Thread, and Proprietary 2.4GHz applications. The CC2340R53 has a 48MHz, 32-bit Arm Cortex-M0+ as the main processor and a rich peripheral set that includes 12-bit ADC, UART, SPI, I²C, and Timers.

The CC2340R53 LaunchPad kit is supported by the SimpleLink Starter app for iOS™ and Android™. This app connects your LaunchPad to a smartphone using Bluetooth. The Starter app supports reading the LaunchPad buttons, controlling LEDs, and all I/O signals on the BoosterPack connectors. It also supports setting up cloud connectivity to the IBM Quickstart server or to any cloud service via MQTT. This enables a cloud view where you can control your LaunchPad from any web browser in minutes after setting it up.

The CC2340R53 LaunchPad kit can also be upgraded to the latest firmware version with the over-the-air (OTA) upgrade from the SimpleLink Starter app.

1.2 Kit Contents

- [CC2340R53 LaunchPad development kit](#)
- 2-wire female to female cable
- 10-pin flat ribbon cable
- [Standard Terms and Conditions for EVMs](#)
- Abbreviated Terms and Conditions for EVMs
- [Quick Start Guide](#) for LP-EM-CC2340R53

1.3 Specification

The [LP-EM-CC2340R53](#) is designed using the CC2340R53 wireless MCU that has a 48MHz Arm Cortex-M0+ processor, 512kB of in-system programmable flash, 12kB of ROM for bootloader and drivers, and 64kB of ultra-low leakage SRAM with full RAM retention in standby mode. In addition, the [LP-EM-CC2340R53](#) is a 2.4GHz RF transceiver compatible with Bluetooth 5.3 Low Energy and IEEE 802.15.4 PHY and MAC, contains an integrated balun, supports over-the-air upgrade (OTA), and has a Serial Wire Debug (SWD) interface.

The MCU consumption has a 2.6mA active mode running CoreMark®, at 53µA/MHz, consumes less than 710nA in standby mode, and 165nA shutdown mode with wake-up on a pin.

The Radio consumption is 5.3mA in RX mode, 5.1mA in TX mode at 0dBm, and less than 11.0mA in TX at +8dBm. In addition, the radio is capable of –102dBm sensitivity for Bluetooth Low Energy 125kbps, –96.5dBm sensitivity for Bluetooth Low Energy 1Mbps, and –98dBm sensitivity for IEEE 802.15.4 (2.4GHz)

The [LP-EM-CC2340R53](#) has support for 2-pin SWD debugging and a 32kHz external XTAL for the lowest power consumption and accurate RTC timekeeping. Each feature uses 2 pins that can be re-used as GPIOs if desired, bringing the number of GPIOs to a maximum of 26. In addition, this LaunchPad can support:

- 3 × 16-bit and 1 × 24-bit general-purpose timers, quadrature decode mode support
- 12-bit ADC, 1.2Msps with external reference, 267ksps with internal reference, up to 12 external ADC inputs
- 1 × low power comparator
- 1 × UART
- 1 × SPI
- 1 × I²C
- Real-time clock (RTC)
- Integrated temperature and battery monitor
- Watchdog timer

1.4 Device Information

The CC2340R SimpleLink family of devices are 2.4GHz wireless microcontrollers (MCUs), targeting Bluetooth 5.3 Low Energy, ZigBee, Thread, and Proprietary 2.4GHz applications. These devices are optimized for low-power wireless communication with Over the Air Download (OAD) support in Building automation (wireless

sensors, lighting control, beacons), asset tracking, medical, retail EPOS (electronic point of sale), ESL (electronic shelf), and Personal electronics (toys, HID, stylus pens) markets. Highlighted features of CC2340R53 on this development kit include:

- Support for Bluetooth 5 features: high-speed mode (2Mbps PHY), long-range (LE Coded 125kbps and 500kbps PHYs), privacy 1.2.1 and channel selection algorithm #2, as well as backward compatibility and support for key features from the Bluetooth 4.2 and earlier Low Energy specifications.
- Fully qualified Bluetooth 5.3 software protocol stack included with the [SimpleLink Low Power F3 software development kit \(SDK\)](#)
- ZigBee protocol stack support in the [SimpleLink Low Power F3 software development kit \(SDK\)](#)
- Thread protocol stack support in [SIMPLELINK TI OPENTHREAD SDK](#) ¹
- Ultra-low standby current less than 0.71µA with RTC operational and full RAM retention that enables significant battery life extension, especially for applications with longer sleep intervals.
- Integrated balun for reduced bill-of-material (BOM) board layout

The CC2340R family is part of the SimpleLink MCU platform, which consists of Wi-Fi®, Bluetooth Low Energy, Thread, ZigBee, Sub-1GHz MCUs, and host MCUs that all share a common, easy-to-use development environment with a single-core software development kit (SDK) and rich tool set. A one-time integration of the SimpleLink platform enables you to add any combination of the portfolio's devices into your design, allowing 100 percent code reuse when your design requirements change. For more information, visit the [SimpleLink MCU platform](#).

¹ Available in a future release

2 Hardware

Figure 2-1 shows the location of the LP-EM-CC2340R53 connectors, buttons/switches, and LEDs. The CC2340R53 wireless microcontroller and antenna locations are also shown.

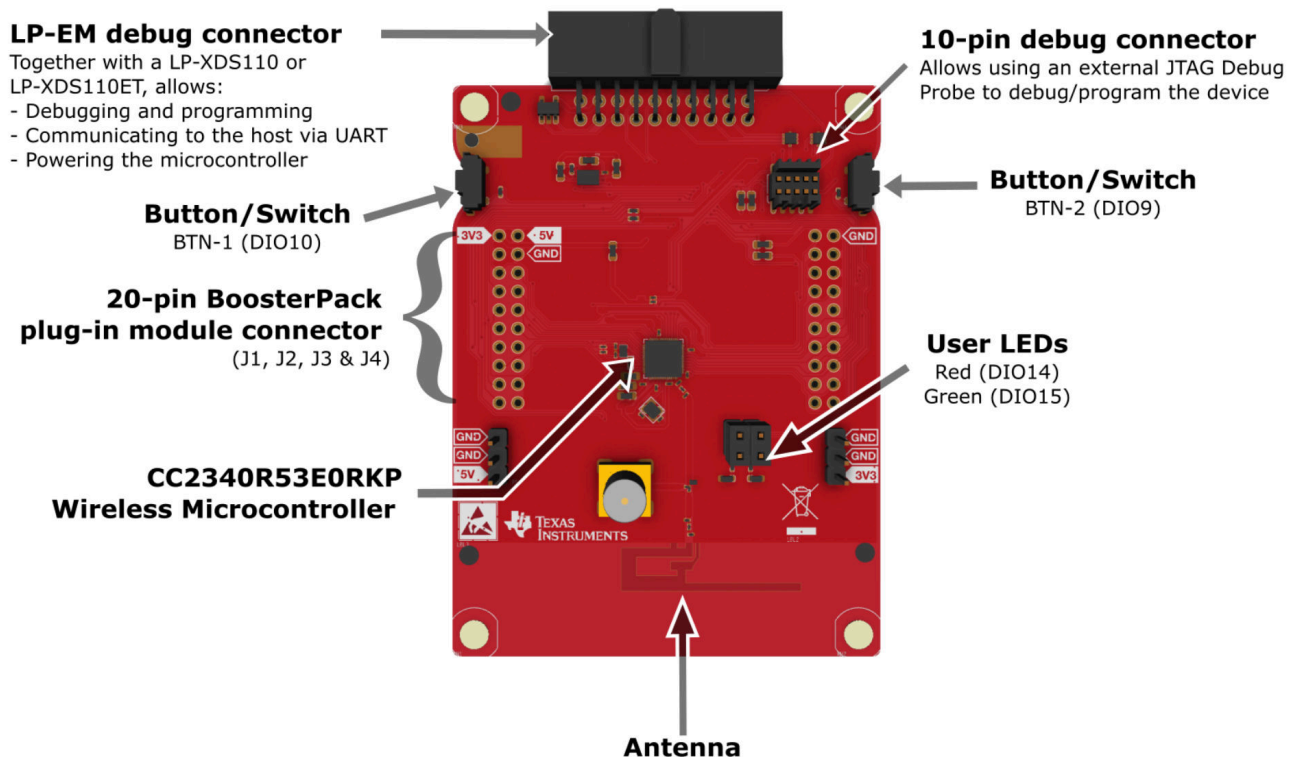


Figure 2-1. LP-EM-CC2340R53 Connector and User Interface Layout

2.1 Power Requirements

The LaunchPad is designed to be powered from the LaunchPad EM Debug Probe via the LP-EM Debug connector, or from an external power supply connected to the GND and the 3V3 pin header. There is also support for 5V on the pin header, but this is not required or used for LaunchPad operation. When powering from the LaunchPad EM Debug Probe, the device supply voltage is fixed at 3.3V. When powering externally, care must be taken to keep the board voltage within its operating range (1.8V to 3.6V).

Note: even though the CC2340R53 supply voltage ranges from 1.8V to 3.8V, the maximum voltage of the LP-EM-CC2340R53 is limited by the XDS110 level shifters at 3.6V.

Additional details about how to power the LaunchPad are shown in the [Hardware and Debug Setup](#) section below.

2.2 Temperature Range

The LaunchPad is designed for operation from -25°C to $+70^{\circ}\text{C}$. Note that other BoosterPack accessories and LaunchPads may have different temperature ranges and, when combined, these ranges will be limited by the most restrictive values. Also, when powering the LaunchPad from an external battery, keep the system within its specified temperature operating range.

2.3 Energy Trace

EnergyTrace™ is available on any of the above compatible Debug Probes except the LP-XDS110. The tool can be used standalone as a power profiling tool, which allows ultra-low power measurements, or in EnergyTrace++

mode within a debug session, which allows for complete state monitoring and helps optimize the application for ultra-low-power consumption.

To use EnergyTrace, the XDS110 Debug Probe must provide power to the [LP-EM-CC2340R53](#) to perform current measurements. EnergyTrace embedded in the [LP-XDS110ET](#) or in a separate LaunchPad only supports 3.3V supply voltage, but using a [TMDSEMU110-U](#) with the optional EnergyTrace HDR adapter [TMDSEMU110-ETH](#) allows powering through the full range of the board voltages. The [Measuring Current Consumption Application Report](#) describes how to run EnergyTrace from CCS.

2.4 Hardware and Debug Setup

The [LP-EM-CC2340R53](#) LaunchPad does not include an onboard debug probe. Compatible debug probes are the LaunchPad XDS110 debug probe ([LP-XDS110](#) or [LP-XDS110ET](#)), standalone XDS110 ([TMDSEMU110-U](#) with the optional EnergyTrace HDR adapter [TMDSEMU110-ETH](#)), or a LaunchPad with an onboard debug probe.

2.4.1 Using a LaunchPad XDS110 Debug Probe

Before connecting the [LP-XDS110](#) or [LP-XDS110ET](#) Debug Probe to the [LP-EM-CC2340R53](#), enable power by setting the TGT VDD jumper on the XDS110 Debug Probe to **XDS**. In this setting, the device voltage is fixed at 3.3V. If external power will be provided to the LaunchPad, then set this jumper on the XDS110 Debug Probe to **EXT**. Afterward, connect the edge connector of the debugger to the edge connector of the LaunchPad and connect the USB port of the debugger to a computer.

The final configuration for this setup is shown in [Figure 2-2](#) along with the correct jumper configuration for TGT VDD connected to **XDS** (as shown in the bottom right corner of the XDS 110 Debug Probe).

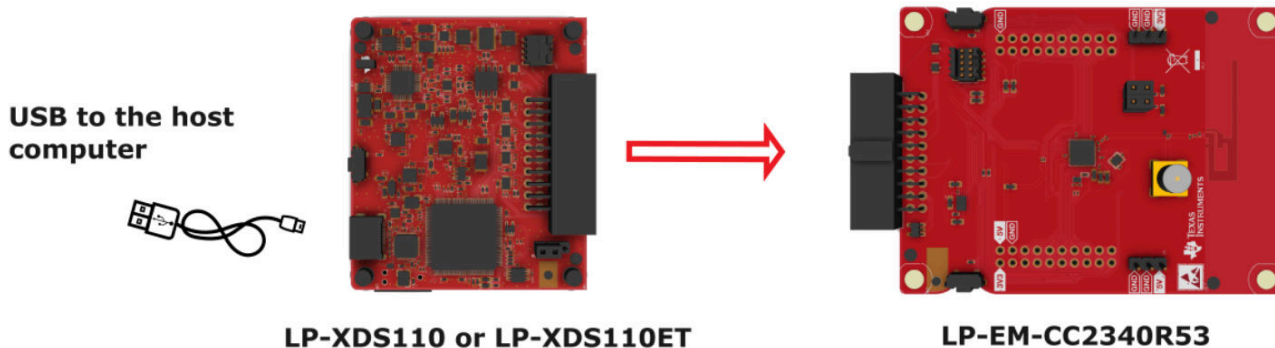


Figure 2-2. Connection of LP-EM-CC2340R53 with XDS110 Debug Probe

2.4.2 Using a Generic XDS110 Debug Probe, Including a Separate LaunchPad

On the separate LaunchPad:

- Remove all of the header jumpers except **GND** and **3V3**.
- Set the power jumper to reflect the scenario. Set the jumper to **XDS110 power** if the [LP-EM-CC2340R53](#) is to be powered by the separate LaunchPad. Set this to **Extern Pwr** if external power is to be provided instead. Care must be taken to keep the board voltage within its operating range (1.8V to 3.6V).
- Connect one end of the 10-pin Debug Cable to the **XDS110 Out** connector on the LaunchPad.
- Connect one end of the 2-wire Power Cable to the **3V3** and **GND** header of the LaunchPad.

On the ([LP-EM-CC2340R53](#)):

- Connect the other end of the 10-pin Debug Cable to the **Target In** connector.
- Connect the other end of the 2-wire Power Cable to the **GND** and **3V3** header. Verify that the polarity is correct.

The final configuration is shown in [Figure 2-3](#).

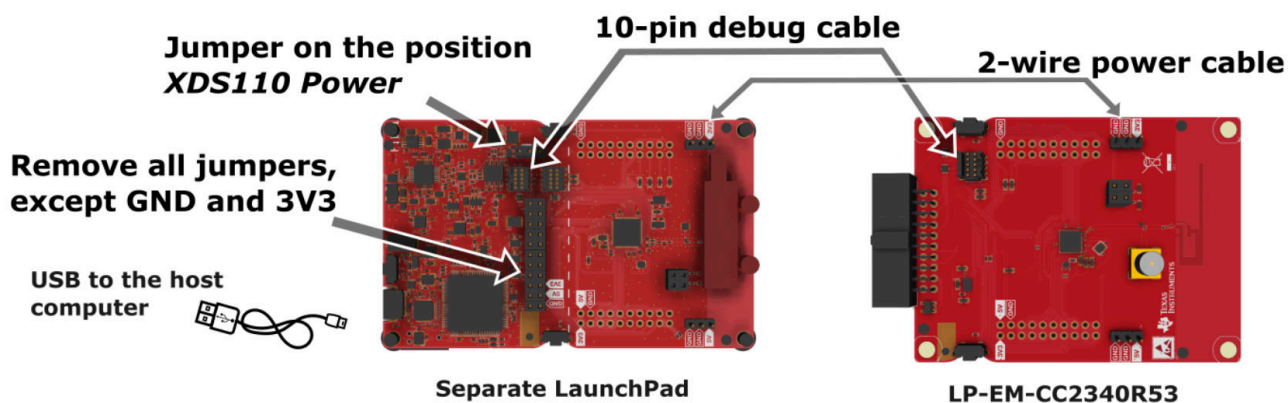
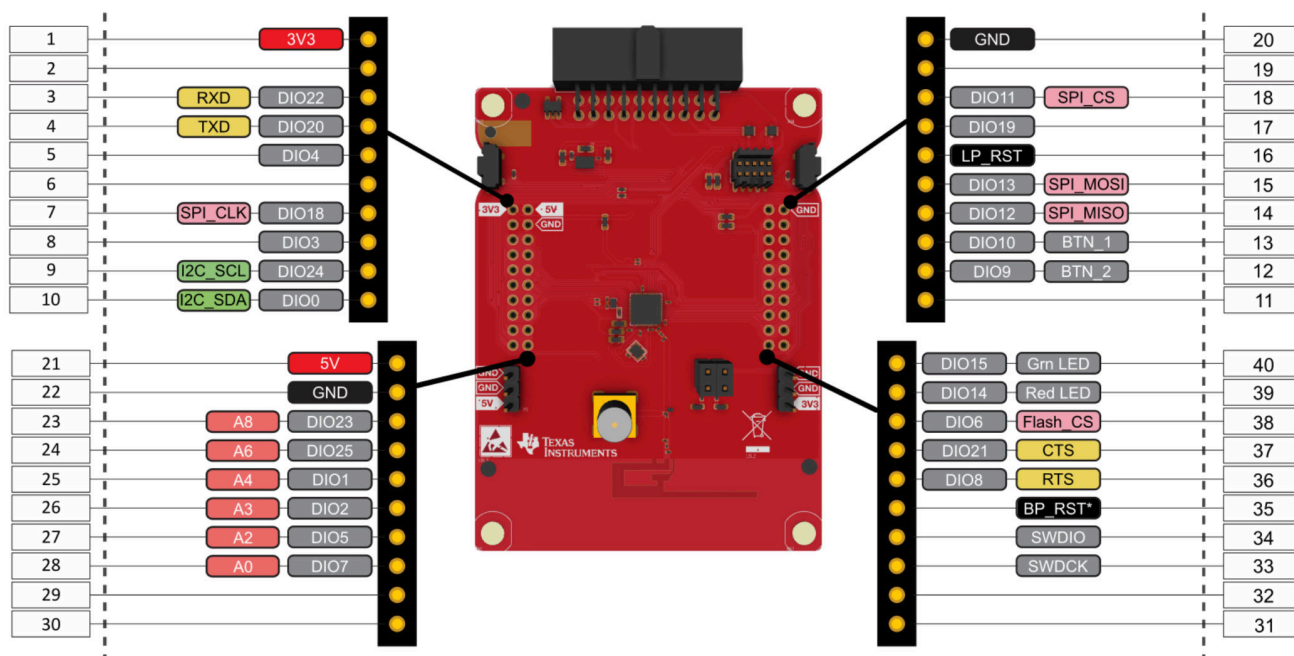


Figure 2-3. Connection of LP-EM-CC2340R53 with XDS110 Debug Probe

2.5 BoosterPack Connector Pinout

The BoosterPack header connection diagram can be seen in [Figure 2-4](#).



UART (DIO20, DIO22), **Reset** (LP_RST) and **JTAG** (SWCK and SWD) are also present in the LP-EM Debug Connector. Power (**GND**, **3V3** and **5V**) is also provided.

*This function is not connected to the LaunchPad connector by default.

Figure 2-4. LP-EM-CC2340R53 BoosterPack Connector Pinout

2.6 XDS110 Interface Connector

The **LP-EM-CC2340R53 P3** connector pinout is show in [Table 2-1](#). For the pin 1 location please refer to [Section 5](#) or the full set of [reference design files](#).

Table 2-1. XDS110 Interface Connector (P3) Pinout Description

Pin Number	Pin Name	Pin Description
1	GND	Ground connection
2	NC	No connect
3	XDS_GPIO1	Connection to GPIO1 of XDS board
4	NC	No connect
5	XDS_GPIO2	Connection to GPIO2 of XDS board
6	WMCU_SWDCCK	SWDCCK connection to WMCU device
7	GND	Ground connection
8	WMCU_SWDIO	SWDIO connection to WMCU device
9	XDS_GPIO3	Connection to GPIO3 of XDS board
10	WMCU_RESET	RESET connection to WMCU device
11	NC	No connect
12	WMCU_TXD	TXD connection to WMCU device
13	GND	Ground connection
14	WMCU_RXD	RXD connection to WMCU device
15	XDS_BoardID_SCL	XDS SCL connection to LaunchPad board ID
16	WMCU_VDD	VDD connection to WMCU device
17	XDS_BoardID_SDA	XDS SDA connection to LaunchPad board ID
18	5V0_BP	5V connection
19	GND	Ground connection
20	GND	Ground connection

2.7 Debug Interface Connector

The [LP-EM-CC2340R53](#) contains a 10-pin debug connect (**P4**) that allows using an external JTAG Debug Probe in order to debug/program the device. The pinout information can be found in [Table 2-2](#). For the pin 1 location please refer to [Section 5](#) or the full set of [reference design files](#).

Table 2-2. 10-Pin Debug Interface Connector Pinout

Pin Number	Pin Name	Pin Description
1	WMCU_VDD	VDD connection to WMCU device
2	WMCU_SWDIO	SWDIO connection to WMCU device
3	GND	Ground connection
4	WMCU_SWDCCK	SWDCCK connection to WMCU device
5	GND	Ground connection
6	NC	No connect
7	NC	No connect
8	NC	No connect
9	GND	Ground connection
10	WMCU_RESET	RESET connection to WMCU device

2.8 Jumper Information

The [LP-EM-CC2340R53](#) has two user-configurable jumpers as shown in [Figure 2-1](#):

- When the **P2** LaunchPad jumper is connected across **pins 1 and 2**, this will connect **DIO14** to the onboard Red LED. When removed it allows direct connection of **DIO14** to the BoosterPack header **J2:39**.
- When the **P2** LaunchPad jumper is connected across **pins 2 and 4**, this will connect **DIO15** to the onboard Green LED. When removed it allows direct connection of **DIO15** to the BoosterPack header **J2:40**.

In addition the [LP-EM-CC2340R53](#) has 2 additional jumpers that allow access to both 5V and GND via header **P5** and 3.3V and GND through header **P1** as show in [Figure 2-1](#).

2.9 Push Buttons

The [LP-EM-CC2340R53](#) has two user-configurable push buttons as shown in [Figure 2-1](#):

- **BTN-1** is connected to **DIO10** of the CC2340R53. This is also directly connected to the BoosterPack header **J2:13**.
- **BTN-2** is connected to **DIO9** of the CC2340R53. This is also directly connected to the BoosterPack header **J2:12**.

3 Advanced use of the LaunchPad Hardware

Note: the topics in this section involve hardware modifications to your development kit. Your board may be damaged if appropriate soldering equipment is not used and proper ESD mitigation procedures are not followed. Make sure you also have the expertise to perform these modifications.

3.1 External Antenna

The RF path of the [LP-EM-CC2340R53](#) by default uses the Inverted F antenna printed on its PCB. The SMA connector near the PCB antenna can be used instead, which is useful for tests using an external antenna or for RF-conducted measurements.

To do that, the capacitor **CA2** (as shown in [Figure 3-1](#)) has to reroute the RF path from the antenna to the SMA: desolder **CA2** from its original vertical position (as shown in the picture below) and resolder it horizontally to connect to the pad near the SMA.

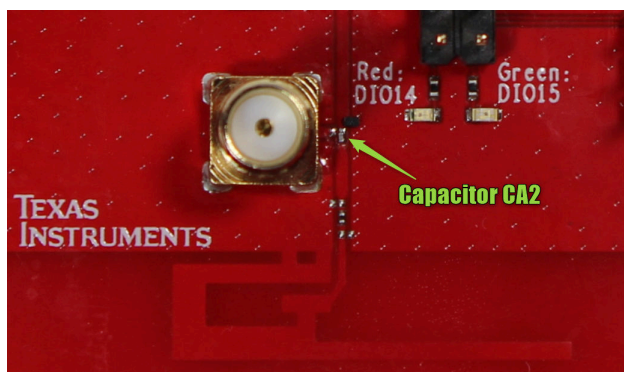


Figure 3-1. Capacitor CA2 Location

3.2 XDS110 GPIOs

The LP-EM Debug Connector has three GPIO pins that are controlled by the LaunchPad XDS110 Debug Probe:

- Pin 3 → XDS_GPIO1
- Pin 5 → XDS_GPIO2
- Pin 9 → XDS_GPIO3

Note: Details on how to use these pins from a host PC are shown in section 3.7.3.3 of the [XDS110 Debug Probe User's Guide](#).

These pins are disconnected by default. In order to enable them, solder three 0 Ω resistors to their corresponding positions at the bottom of the board as shown in [Figure 3-2](#):

- R19 → XDS_GPIO1
- R20 → XDS_GPIO2
- R21 → XDS_GPIO3

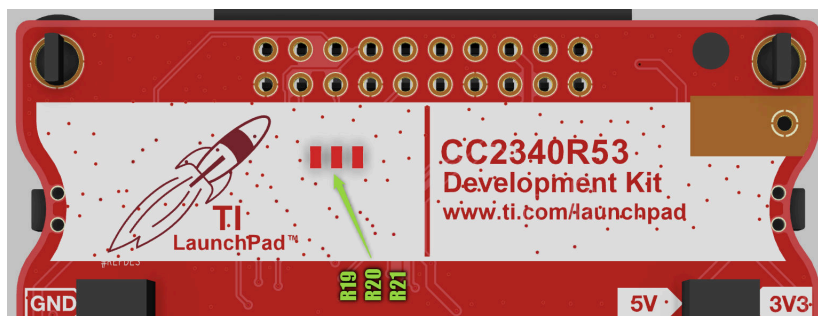


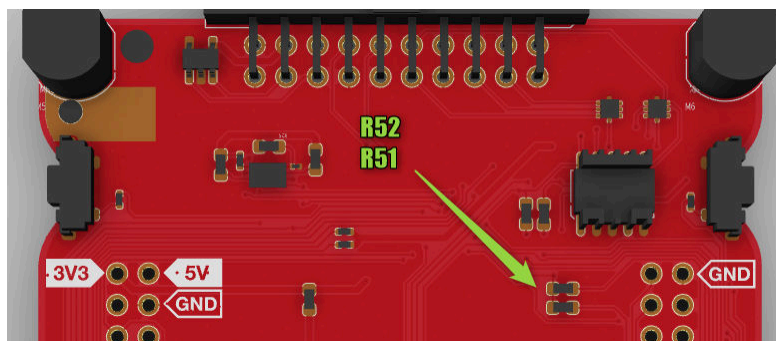
Figure 3-2. Location of R19, R20, and R21

By doing this modification, the CC2340R53 MCU GPIO pins are connected to these signals:

- DIO7_A0 → XDS_GPIO1
- DIO2_A3 → XDS_GPIO2
- DIO1_A4 → XDS_GPIO3

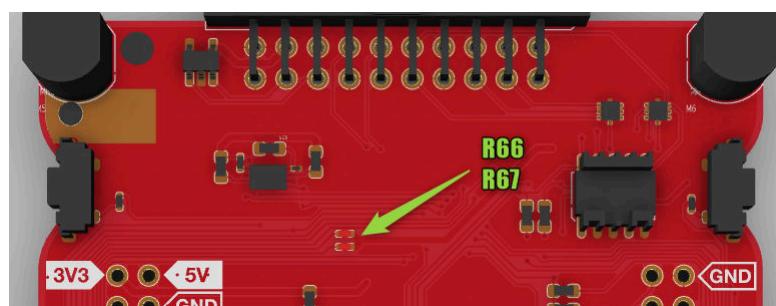
3.3 Reset Selection on BoosterPack Connector

The [LP-EM-CC2340R53](#) board connects the reset signal to pin **J2:16 (LPRST)** of the BoosterPack connector (see [Figure 2-4](#)). Depending on the BoosterPack used you might need to connect this reset signal to **J4:35 (BPRST)**. To do this, solder a 0 resistor in **R52** and remove the 0Ω from **R51**.



3.4 I²C Pullup Resistors

The [LP-EM-CC2340R53](#) supports the I²C port of the CC2340R5 device: pins **DIO0 (SDA)** and **DIO24 (SCL)**. To use I²C, however, pullup resistors are required and can be added to the footprints designated by **R66** and **R67** on the [LP-EM-CC2340R53](#) board. The correct value for these resistors varies according to the bus length and other intrinsic characteristics. To properly do this calculation, consult the application note [Understanding the I²C Bus](#).



4 Software

4.1 Getting Started

The best way to start development with your [LP-EM-CC2340R53](#) is to visit the [SimpleLink Academy](#) which provides a comprehensive set of trainings for the SimpleLink MCU family.

4.2 Out of Box Demo

The [LP-EM-CC2340R53](#) is preprogrammed with the Data Stream Application software that allows wireless communication with smartphones and tablets over Bluetooth Low Energy. Simply connect the [LP-EM-CC2340R53](#) to the XDS110 Debug Probe and then to a computer or power supply.

When power is applied, the board will run a power-on self-test and the green LED will be lit.

To test the functionality of the Data Stream Application, download the Simplelink Connect application from one of the two App Stores below:

Apple App Store	Google Play Store
	

This app lets you control and visualize the Data Stream demo software running on the LaunchPad. An example of the Data stream Application is shown in [Figure 4-1](#).

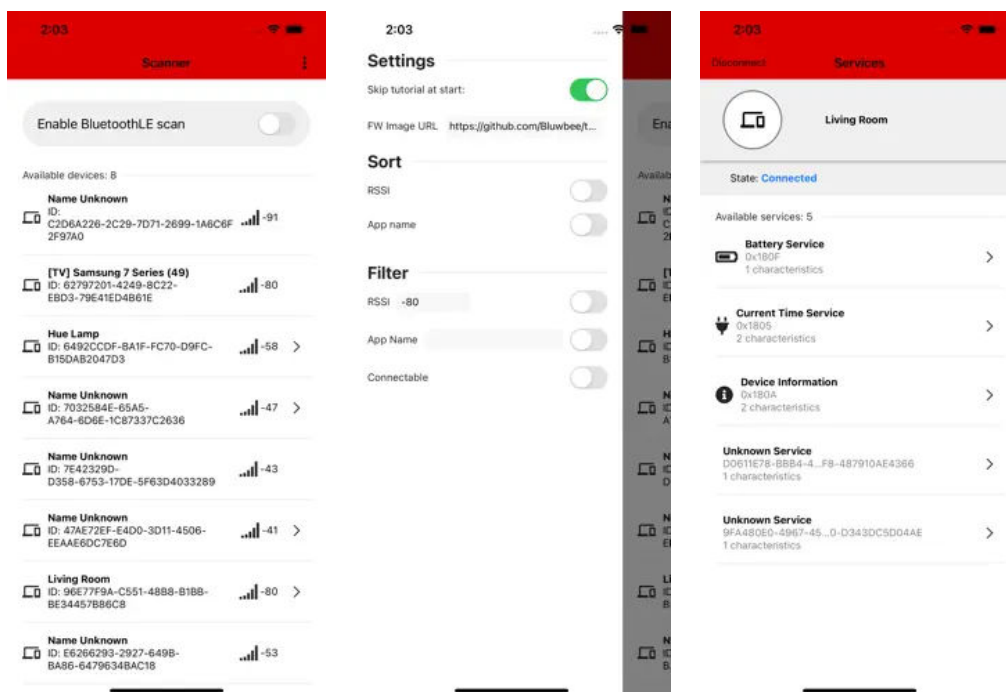


Figure 4-1. Data Stream Application SW example

The Data Stream Application status information can also be seen on the UART port of the host PC as show in [Figure 4-2](#).

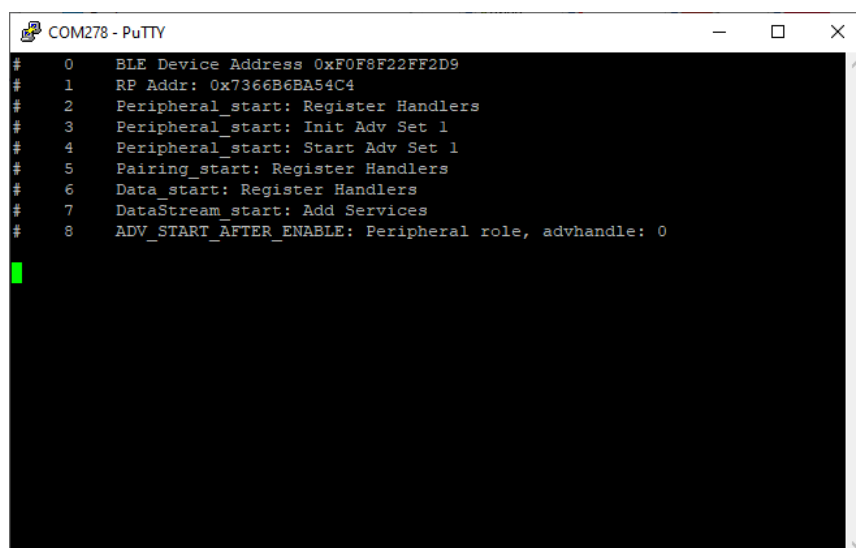


Figure 4-2. Data Stream Application status example

Important: Clear your phone or tablet's Bluetooth cache before running the application or when changing the application on the CC2340R53. If this step is not performed, then you may not see the available characteristics in the smartphone app. Note that closing the smartphone app or rebooting your phone does NOT clear the Bluetooth cache.

iOS: This is accomplished by toggling Bluetooth Off then On via either *Settings* → *Bluetooth* menu or the Control Center (Bluetooth icon)

Android: The procedure can vary by make, model, and software version. On recent versions, navigate to *Settings* → *Apps* Scroll over to *All* → *Choose Bluetooth Share* and tap on *Clear Cache*.

5 Hardware Design Files

5.1 Reference Schematics

Figure 5-1, Figure 5-2, Figure 5-3, show the schematic implementation of the LP-EM-CC2340R53. The full LP-EM-CC2340R53 reference schematic can be downloaded from [LP-EM-CC2340R53 design files](#).

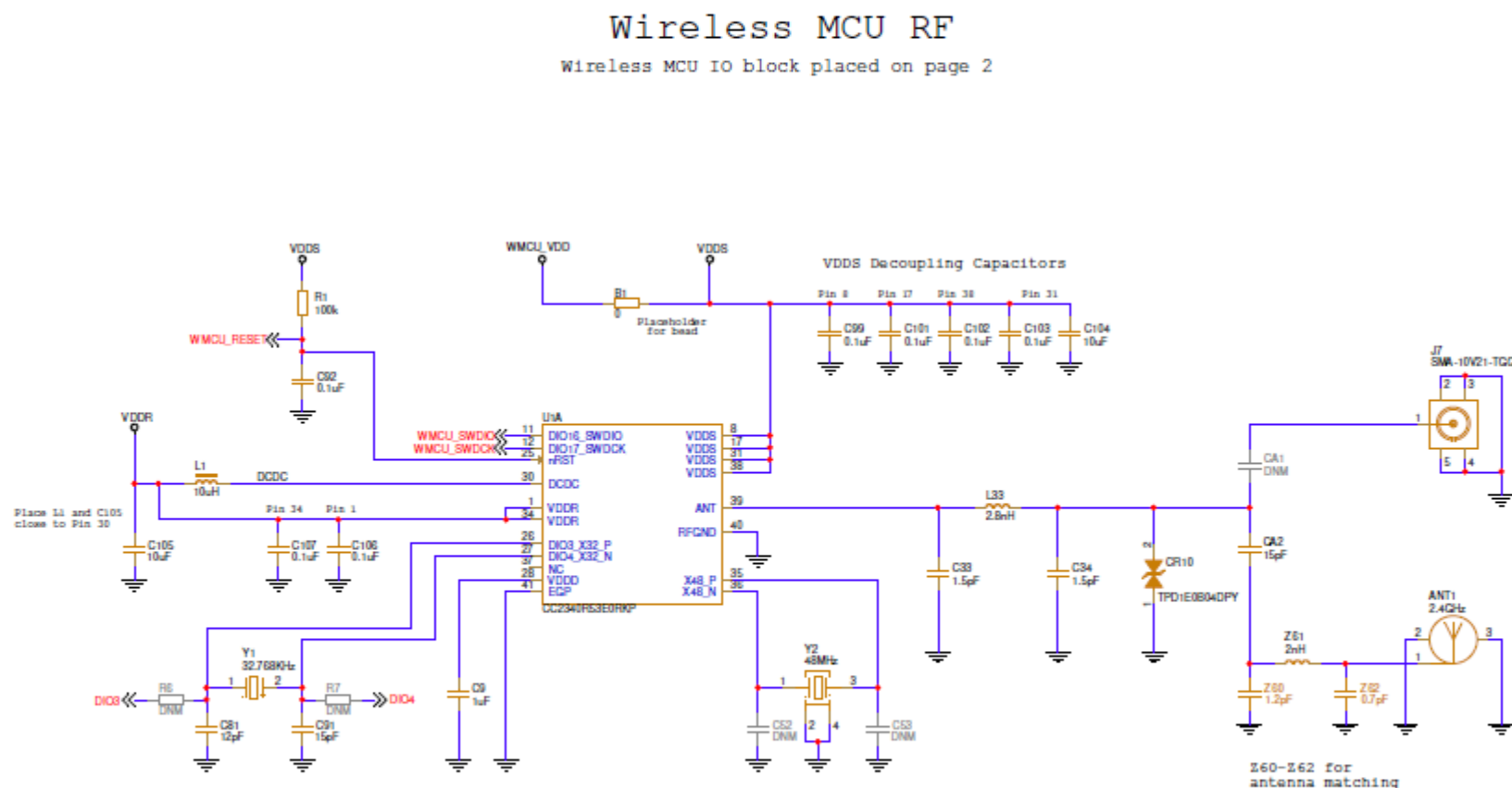


Figure 5-1. LP-EM-CC2340R53 Schematic Page 1

BoosterPack Headers and Peripherals

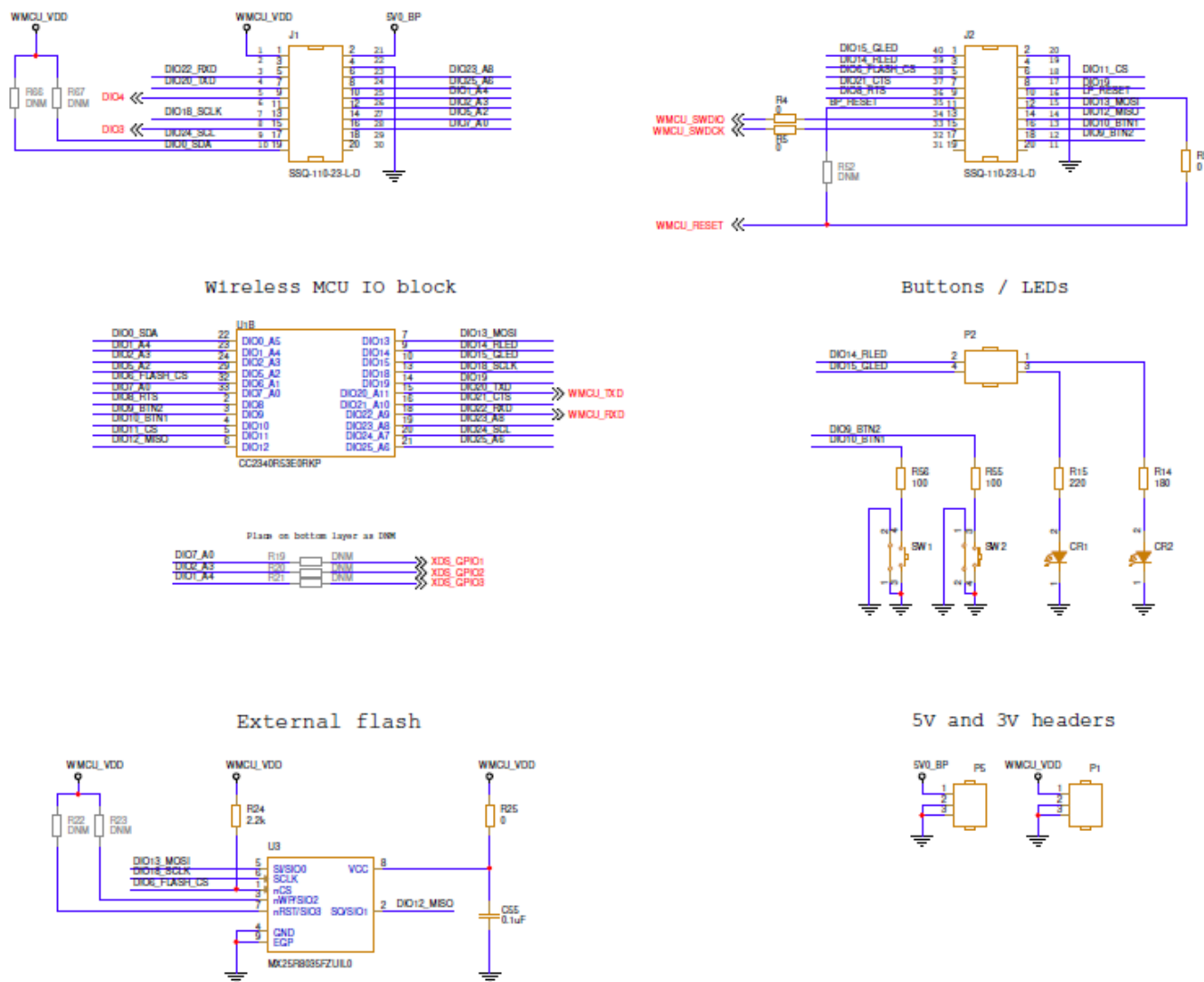


Figure 5-2. LP-EM-CC2340R53 Schematic Page 2

XDS110 Debugger Interface

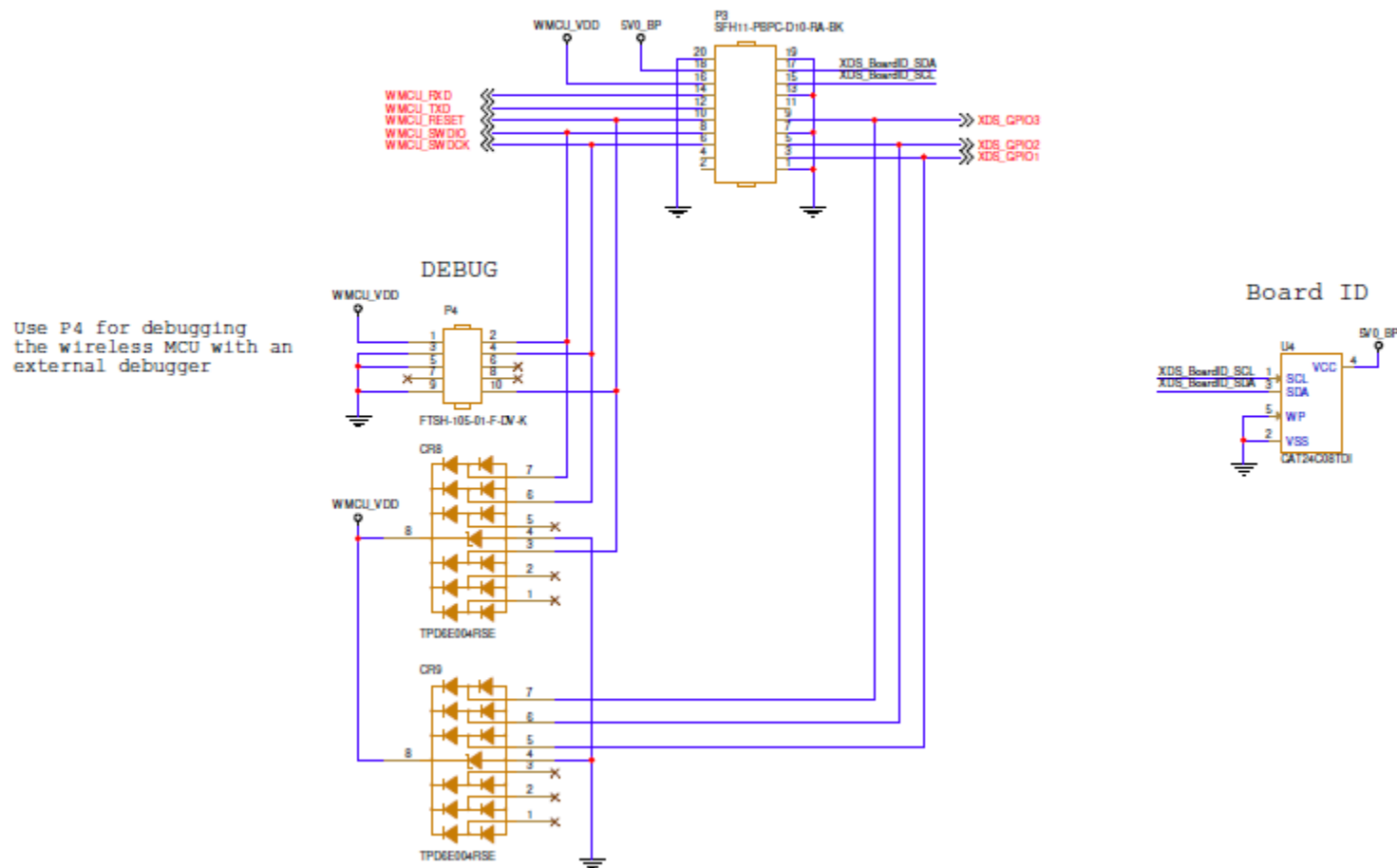


Figure 5-3. LP-EM-CC2340R53 Schematic Page 3

5.2 PCB Layouts

The top and bottom side PCB layout view of the LP-EM-CC2340R53 can be seen in [Figure 5-4](#) and [Figure 5-5](#), respectively. The full LP-EM-CC2340R53 layout files can be downloaded from [LP-EM-CC2340R53 design files](#).

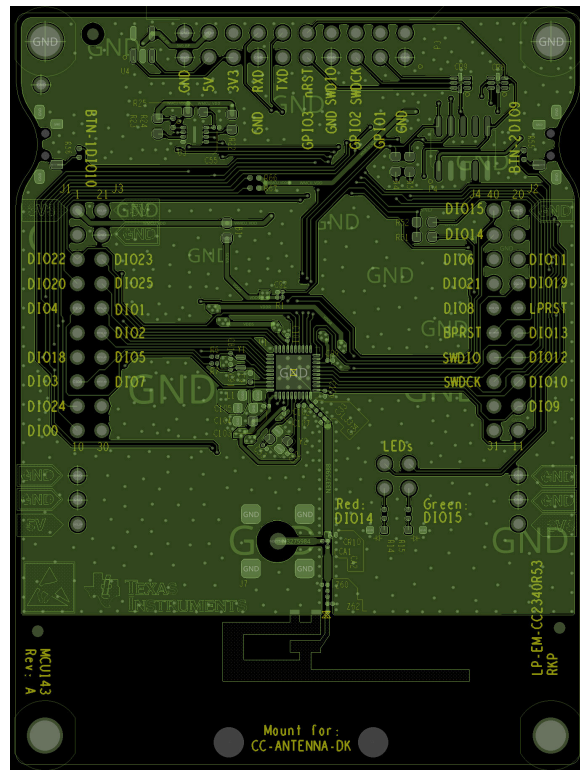


Figure 5-4. Top Side PCB Layout View

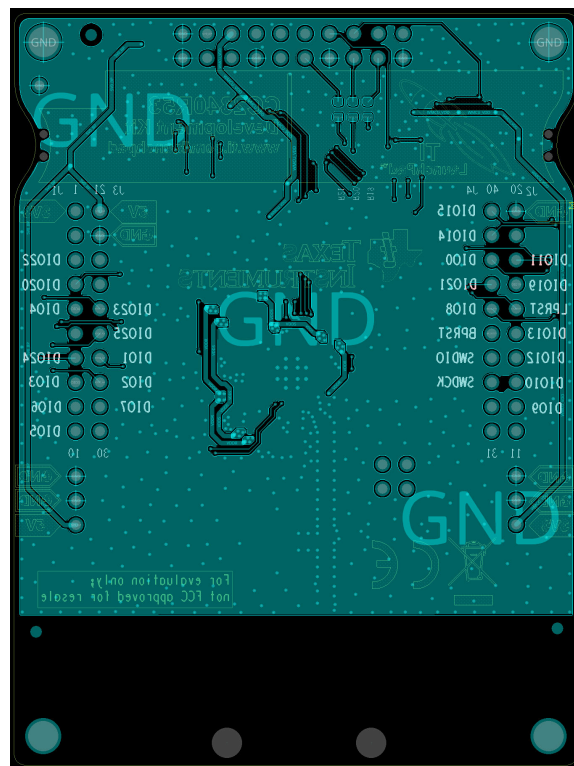


Figure 5-5. Bottom Side PCB Layout View

5.3 Bill of Materials (BOM)

Table 5-1 shows the simplified Bill of Materials (BOM) for the LP-EM-CC2340R53. The full LP-EM-CC2340R53 BOM can be downloaded from [LP-EM-CC2340R53 design files](#).

Table 5-1. LP-EM-CC2340R53 Bill-of-Materials

Quantity	Designator	Value	Manufacturer	Part Number	Description
1	B1	0	KOA SPEER	RK73Z1JTDD	RESISTOR, THICK FILM, 0, 0R/+0.05R, 1A, -55°C/+155°C, 0603
1	C9	1uF	MURATA	GRM155Z71A105KE01D	CAPACITOR, CERAMIC X7R, 1uF, 10V, -10%/+10%, -55°C/+125°C, 0402
2	C33, C34	1.5pF	MURATA	GRM0335C1H1R5BA01J	CAPACITOR, CERAMIC C0G/NP0, 1.5pF, 50V, -0.1pF/+0.1pF, -55°C/+125°C, 0201
8	C55, C92, C99, C101, C102, C103, C106, C107	0.1uF	MURATA	GRM033C71A104KE14D	CAPACITOR, CERAMIC X7S, 0.1uF, 10V, -10%/+10%, -55°C/+125°C, 0201
1	C81	12pF	MURATA	GRM0335C1H120GA01D	CAPACITOR, CERAMIC C0G/NP0, 12pF, 50V, -2%/+2%, -55°C/+125°C, 0201
2	C91, CA2	15pF	MURATA	GRM0335C1H150GA01D	CAPACITOR, CERAMIC C0G/NP0, 15pF, 50V, -2%/+2%, -55°C/+125°C, 0201
2	C104, C105	10uF	MURATA	GRM188Z71A106KA73D	CAPACITOR, CERAMIC X7R, 10uF, 10V, -10%/+10%, -55°C/+125°C, 0603, SMD
1	CR1	LPL296-J2L2-25	OSRAM	LP L296-J2L2-25	OPTO, LED, InGaAlP, GREEN, 560nm, 2V, 0.02A, 0603
1	CR2	LS L296-P2Q2-1	OSRAM	LS L296-P2Q2-1	OPTO, LED, RED, 630nm, 2V, 0.02A, 0603
2	CR8, CR9	TPD6E004RSE	TEXAS INSTRUMENTS	TPD6E004RSER	DIODE, PROTECTION, ESD ARRAY - 6 CHANNELS, 0.9V TO 5.5V, VBR:7V, -40°C/+85°C, QFN8
1	CR10	TPD1E0B04DPY	TEXAS INSTRUMENTS	TPD1E0B04DPYR	DIODE, PROTECTION, TVS, BID, 6.7V, 15W, -40°C/+125°C, X1SON2
2	J1, J2	SSQ-110-23-L-D	SAMTEC	SSQ-110-23-L-D	CONNECTOR, HEADER, FEMALE, STRAIGHT, 2 ROWS, 20 PINS, 2.54mm, PTH
1	J7	SMA-10V21-TGG	HUS-TSAN	SMA-10V21-TGG	CONNECTOR, COAX, RF, FEMALE, STRAIGHT, 1 PIN
1	L1	10uH	MURATA	LQM18DN100M70L	INDUCTOR, STANDARD, CHIP, FERRITE CORE, 10uH, -20%/+20%, 0.1A, -55°C/+125°C, 0603
1	L33	2.8nH	MURATA	LQP03TN2N8B02J	INDUCTOR, RF, CHIP, NON-MAGNETIC CORE, 2.8nH, -0.1nH/+0.1nH, 0.5A, -55°C/+125°C, 0201
2	P1, P5	BB02-HC031-KB1-603000	GRADCONN	BB02-HC031-KB1-603000	CONNECTOR, HEADER, MALE, STRAIGHT, 1 ROW, 3 PINS, PITCH 2.54mm, PTH
1	P2	BB02-HJ041-KB1-603000	GRADCONN	BB02-HJ041-KB1-603000	CONNECTOR, HEADER, MALE, STRAIGHT, 2 ROWS, 4 PINS, PITCH 2.54mm, PTH

Table 5-1. LP-EM-CC2340R53 Bill-of-Materials (continued)

Quantity	Designator	Value	Manufacturer	Part Number	Description
1	P3	SFH11-PBPC-D10-RA-BK	SULLINS	SFH11-PBPC-D10-RA-BK	CONNECTOR, HEADER, FEMALE, RIGHT ANGLE, 2 ROW(S), 20 PINS, PITCH 2.54mm, PTH
1	P4	FTSH-105-01-F-DV-K	SAMTEC	FTSH-105-01-F-DV-K	CONNECTOR, HEADER, MALE, STRAIGHT, 2 ROWS, 10 PINS, PITCH 1.27mm
1	R1	100k	VISHAY	CRCW0201100KJNED	RESISTOR, THICK FILM, 100k, -5%/+5%, 0.05W, 30V, -55°C/+155°C, 0201
4	R4, R5, R25, R51	0	VISHAY	CRCW06030000Z0EC	RESISTOR, THICK FILM, 0, 0R/+0.02R, 0.1W, 2A, -55°C/+155°C, 0603
1	R14	180	VISHAY	CRCW0402180RJNED	RESISTOR, THICK FILM, 180, -5%/+5%, 0.063W, 50V, -55°C/+155°C, 0402
1	R15	220	VISHAY	CRCW0402220RJNED	RESISTOR, THICK FILM, 220, -5%/+5%, 0.063W, 50V, -55°C/+155°C, 0402
1	R24	2.2k	VISHAY	CRCW04022K20JNED	RESISTOR, THICK FILM, 2.2k, -5%/+5%, 0.063W, 50V, -55°C/+155°C, 0402
2	R55, R56	100	VISHAY	CRCW0402100RJNED	RESISTOR, THICK FILM, 100, -5%/+5%, 0.063W, 50V, -55°C/+155°C, 0402
2	SW1, SW2	1188E-1K2	DIPTRONICS	1188E-1K2-V-TR	SWITCH, TACT, 0.05A@12VDC, 0.05A@250VAC
1	U1	CC2340R53E0RKPR	TEXAS INSTRUMENTS	CC2340R53E0RKPR	IC, MICROCONTROLLER, CC2340R5, 32-bit, SIMPLELINK WIRELESS MCU ARM CORTEX -M0+, 512kbyte FLASH, 1.71V TO 3.8V, VQFN40-EP
1	U3	MX25R8035FZUILO	MACRONIX	MX25R8035FZUILO	IC, MEMORY, FLASH, OTHER, 8Mbit, 8M X 1bit / 4M X 2bit / 2M X 4bit, 12ns, 1.65V TO 3.6V, uSON8
1	U4	CAT24C08TDI	ONSEMI	CAT24C08TDI-GT3	IC, MEMORY, ROM, EEPROM, SERIAL, I2C, 8kbit, 1k X 8bit, 900ns, 1.7V TO 5.5V, TSOT23-5
1	Y1	32.768KHz	TAI-SAW	TZ3359DAAO73	CRYSTAL, RESONATOR, 32.768KHz, -380PPM/+380PPM, -40°C/+125°C
1	Y2	48MHz	TAI-SAW	TZ3908AAAO43	CRYSTAL, RESONATOR, 48MHz, -30PPM/+30PPM, -40DEGC/+125DEGC
1	Z60	1.2pF	MURATA	GRM0335C1H1R2BA01J	CAPACITOR, CERAMIC C0G/NP0, 1.2pF, 50V, -0.1pF/+0.1pF, -55°C/+125°C, 0201
1	Z61	2nH	MURATA	LQP03TN2N0B02J	INDUCTOR, RF, CHIP, NON-MAGNETIC CORE, 2nH, -0.1nH/+0.1nH, 0.6A, -55°C/+125°C, 0201
1	Z62	0.7pF	MURATA	GRM0335C1HR70BA01J	CAPACITOR, CERAMIC C0G/NP0, 0.7pF, 50V, -0.1pF/+0.1pF, -55°C/+125°C, 0201

6 Compliance Information


6.1 CE Compliance

Texas Instruments declares that this product is in compliance with Directive 2014/53/EU. The compliance has been verified in the operating band of 2402MHz to 2480MHz at +9.4dBm at +6dBm Tx Power setting. If you configure the EUT to operate outside the test conditions, operate inside a protected and controlled environment (such as a shielded chamber). This evaluation board is only for development and not an end product. Developers and integrators that incorporate the chipset in any end products are responsible for obtaining applicable regulatory approvals for such end products. See the [EU Declaration of Conformity](#).

6.2 REACH Compliance

Texas Instruments declares that this product is in compliance with the EU REACH regulation.

6.3 Waste Electrical and Electronic Equipment (WEEE) Compliance

	<p>Waste Electrical and Electronic Equipment (WEEE)</p> <p>This symbol means that according to local laws and regulations your product and/or battery shall be disposed of separately from household waste. When this product reaches its end of life, take it to a collection point designated by local authorities. Proper recycling of your product will protect human health and the environment.</p>
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8 References

Development tools and software

- [LP-EM-CC2340R53 LaunchPad](#) Development Kit
- [SimpleLink Low Power F3 software development kit](#)
- [SmartRF™ Studio](#) for simple radio configuration
- [SysConfig](#) system configuration tool

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