

Migrating to xWR68xx and xWR18xx Millimeter Wave Sensors



Nitin Sakhija and Chethan Kumar Y.B.

ABSTRACT

This application report provides guidance for porting mm-wave hardware and application software to the xWR68xx ES2.0 and the xWR18xx devices.

Table of Contents

1 Introduction.....	3
2 xWR1843 Hardware/Software Migration.....	4
2.1 Migrating From xWR1642 to xWR1843.....	4
3 xWR6843AoP ES2.0 Migration.....	9
3.1 Hardware Changes From xWR6843AoP ES1.0 to xWR6843AoP ES2.0.....	9
3.2 Software Migration From xWR6843AoP ES1.0 to xWR6843AoP ES2.0.....	11
4 Helpful Resources.....	17
5 Code Snapshots.....	18
5.1 SDK 3.3 API Change for MMWave_open.....	18
5.2 SDK 3.3 API Change for ADCBuf_open.....	18
5.3 SDK 3.3 API Change for CANFD_init.....	19
5.4 SDK 3.3 68xx Secondary Bootloader Update.....	19
5.5 SDK 3.3 16xx vs 68xx: Calibration Frequency Update.....	20
5.6 SDK 3.3 16xx vs 68xx: SoC Definition Updates.....	20
5.7 SDK 3.3 16xx vs 18xx: SoC Definition Updates.....	21
5.8 SDK 3.4 xWR68xx Calibration Frequency Update.....	21
5.9 SDK 3.4 Object Detect HWA DPC Range FFT Scaling.....	22
5.10 SDK 3.4 Object Detect Range HWA DPC Radar Cube Format.....	22
5.11 xWR6843AoP ES1.0 Antenna Geometry.....	23
5.12 xWR6843AoP ES2.0 Antenna Geometry.....	23
5.13 xWR6843AoP ES2.0 Antenna Geometry Code Update.....	23
5.14 Antenna Geometry Structure Usage in mmw demo.....	24
5.15 xWR6843AoP ES2.0 RX Channel Phase Compensation.....	24
6 References.....	26
7 Revision History.....	26

List of Figures

Figure 2-1. xWR1642 Device Marking.....	4
Figure 2-2. xWR1843 Device Marking.....	4
Figure 2-3. xWR1642 Antenna Image.....	5
Figure 2-4. xWR1843 Antenna Image.....	5
Figure 3-1. Silicon Device Marking Difference Between xWR6843AoP ES1.0 and ES2.0.....	9
Figure 3-2. xWR6843AoP ES1.0 Antenna Geometry and Resulting MIMO Virtual Antenna Array.....	14
Figure 3-3. xWR6843AoP ES2.0 Antenna Geometry and Resulting MIMO Virtual Antenna Array.....	14
Figure 3-4. AoA2dProc HTML Documentation.....	15
Figure 3-5. RX Channel Phase Compensation: CompRangeBiasAndRxChanPhase CLI Command.....	16
Figure 5-1. SDK 3.3 API Change for MMWave_open.....	18
Figure 5-2. SDK 3.3 API Change for ADCBuf_open.....	18
Figure 5-3. SDK 3.3 API Change for CANFD_init.....	19
Figure 5-4. SDK 3.3 68xx Secondary Bootloader Update.....	19
Figure 5-5. SDK 3.3 16xx vs 68xx: Calibration Frequency Update.....	20

Trademarks

Figure 5-6. SDK 3.3 16xx vs 68xx: SoC Definition Updates.....	20
Figure 5-7. SDK 3.3 16xx vs 18xx: SoC Definition Updates.....	21
Figure 5-8. SDK 3.4 xWR68xx Calibration Frequency Update.....	21
Figure 5-9. SDK 3.4 Object Detection DPC FFT Range Scaling Configuration.....	22
Figure 5-10. SDK 3.4 Object Detect Range HWA DPC FFT Radar Cube Format.....	22
Figure 5-11. xWR6843AoP ES1.0 Antenna Geometry.....	23
Figure 5-12. xWR6843AoP ES2.0 Antenna Geometry.....	23
Figure 5-13. SDK 3.2.0.6 Vs SDK 3.4: Antenna Geometry Update for xWR6843AoP ES2.0.....	24
Figure 5-14. Antenna Geometry Structure Usage in mmw demo.....	24
Figure 5-15. SDK 3.2.0.6 Vs SDK 3.4: RX Channel Phase Compensation.....	25

List of Tables

Table 1-1. Migration Reference.....	3
Table 2-1. Device Feature Comparison Table.....	5
Table 2-2. xWR1642 to xWR1843 Software Migration.....	6
Table 3-1. xWR6843AoP ES1.0 to xWR6843AoP ES2.0 Hardware Changes.....	9
Table 3-2. xWR6843AoP ES2.0 Software - Platform Updates.....	11

Trademarks

All trademarks are the property of their respective owners.

1 Introduction

The information presented here is applicable to any of the following scenarios:

- Have hardware/software currently deployed on xWR6843 ES1.0 and want to migrate it to xWR6843 ES2.0
- Have hardware/software currently deployed on xWR1642 and want to migrate it to xWR6843 ES2.0
- Have hardware/software currently deployed on xWR1642 and want to migrate it to xWR1843
- Have hardware/software currently deployed on xWR6843AOP ES1.0 and want to migrate to xWR6843AOP ES2.0

The information presented in this document covers:

- Comparison of the base and the new target device along-with a description of how those differences impact existing hardware and software.
- SDK version required for the new target device and updates needed in application build infrastructure (makefiles and/or CCS projects, linker command files, and so forth)
- Updates needed in application source code, for example, API updates, new structure parameters, and so forth.
- Example source code comparison snapshots are provided for easy reference.

For information specific to your current and target device, see the following sections.

Table 1-1. Migration Reference

Current Device	Target Device	Section
xWR6843 ES1.	xWR6843 ES 2.0	Migrating from xWR6843 ES1.0 to xWR6843 ES2.0 : Section 3.2
xWR1642	xWR6843 ES2.0	Migrating from xWR1642 to xWR6843 ES2.0 : Section 2.1
xWR1642	xWR1843	Migrating from xWR1642 to xWR1843 : Section 2
xWR6843AoP ES1.0	xWR6843AoP Es2.0	Migrating from xWR6843AoP ES1.0 to xWR6843AoP ES2.0 : Section 3

2 xWR1843 Hardware/Software Migration

This section provides migration guidance to port Hardware and software from the xWR1642 to the xWR1843 device. The information provided here is meant to cover the major changes for migrating to a particular MMWAVE-SDK release at the time of writing. For more information, see the *Migration* section in the [MMWAVE-SDK Release Notes](#).

2.1 Migrating From xWR1642 to xWR1843

2.1.1 Device Comparison

Table 2-1 lists the key features of the xWR1642 and the xWR1843 devices that need to be considered from Hardware and software migration perspective. For more information, see the device-specific data sheets and the *Industrial mmWave Radar Family Technical Reference Manual* in [Section 6](#).

[Figure 2-1](#) and [Figure 2-2](#) show the device symbolization change from xWR1642 to xWR1843 on device part marking.

The left side device marking shows the xWR1642 silicon and the right side device marking shows the xWR1843 silicon. For more details on the device marking, see the device-specific Errata.

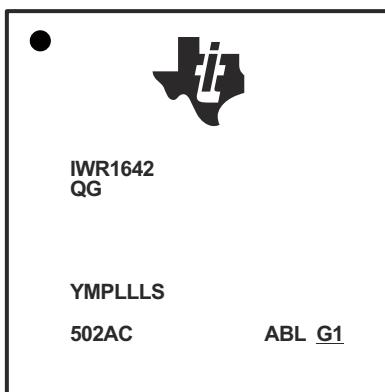


Figure 2-1. xWR1642 Device Marking

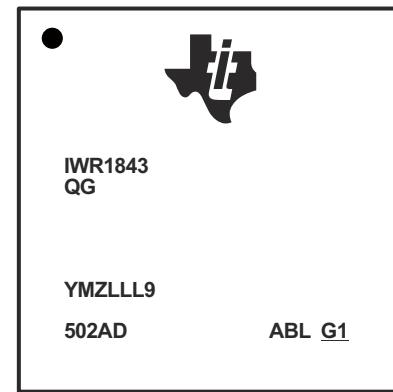


Figure 2-2. xWR1843 Device Marking

- [IWR1642 Device Errata](#)
- [AWR1642 Device Errata](#)
- [IWR1843 Device Errata](#)
- [AWR1843 Device Errata](#)

Table 2-1. Device Feature Comparison Table

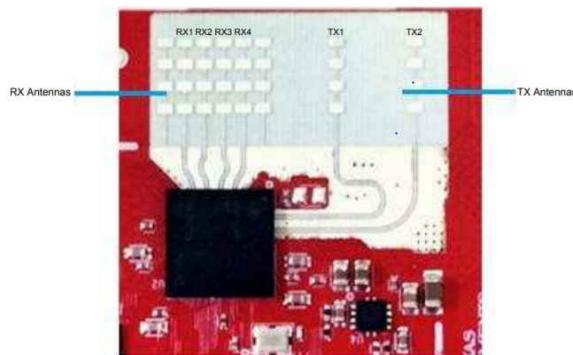
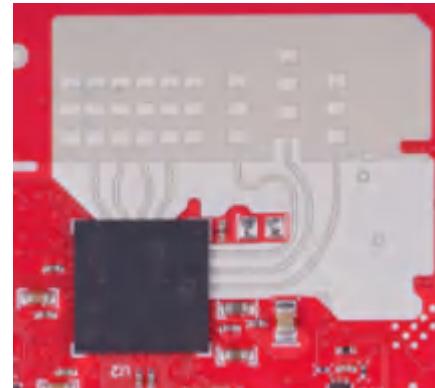
No	Device Feature Differences	xWR1642	xWR1843	Hardware and Software Impact
1	Number of Transmit Channels	2	3 ⁽¹⁾	3rd Transmitter Antenna need to be designed. Update TX bitmap in chirpCfg
2	Maximum Sampling Rate	6.25 MHz complex	12.5 MHz complex	Higher IF bandwidth and Sampling rates are available on xWR1843
3	Max I/F (Intermediate Frequency)	5 MHz	10 MHz	
4	On-chip memory	1.5MB	2.0MB	Software can leverage the additional memory if needed.
5	Radar Accelerator	Not Applicable	Hardware accelerator for FFT, filtering, and CFAR processing	xWR1843 has flexibility of data processing on Hardware accelerator or DSP
6	Tx beam forming	No support	Supported	xWR1843 has phase shifters which supports the steerable beams. Note: Antennas need to be designed to support TX beam forming operation
7	MMWAVE-SDK support	SDK 2.1 (LTS) and above	SDK 3.3.0 and above	General software porting required compiling for xWR1843. For more information, see the Section 2.1.4 .

- (1) Three Tx Simultaneous operation is supported only with 1-V LDO bypass and PA LDO disable mode. In this mode, the 1-V supply needs to be fed on the VOUT PA pin.

2.1.2 Hardware Migration Notes

2.1.2.1 Antenna Addition

From xWR1642 to xWR1843, the third antenna needs to be introduced. For more information, see the design file package that provides the antenna details. Detailed field of view and radiations can be found in the user's guides listed below.


Figure 2-3. xWR1642 Antenna Image

Figure 2-4. xWR1843 Antenna Image

- [xWR1642BOOST Layout and Design Files](#)
- [xWR1642 EVM \(xWR1642BOOST\) Single-Chip mmWave Sensing Solution User's Guide](#)
- [xWR1843BOOST Hardware Files](#)
- [xWR1843 Evaluation Module \(xWR1843BOOST\) Single-Chip mmWave Sensing Solution User's Guide](#)

2.1.3 Hardware Design Checklist

xWR1642 has the hardware design (schematic, Layout, bring-up/wakeup) checklist is available at <http://www.ti.com/lit/zip/swrr151> and for the xWR1843 hardware design (schematic, Layout, Bring up/wakeup) checklist is available at <http://www.ti.com/lit/zip/spracl2>.

2.1.4 Software Migration Notes

Table 2-2 lists the changes required to port existing xWR1642 application code to xWR1843.

Note

The scope of the migration notes provided in this section is limited to migrating to MMWAVE-SDK 3.3.

When migrating existing xWR1843 applications to SDK releases beyond MMWAVE-SDK 3.3, you should follow the incremental migration instructions provided in the corresponding SDK release notes.

Table 2-2. xWR1642 to xWR1843 Software Migration

No	Summary	Components Impacted	Required Changes
1	MMWAVE-SDK 3.2.1 or above required for xWR1843 NOTE: It is recommended to use SDK 3.3.0 or above to include the latest API updates.	Makefile OR CCS projects	<p>Application code must be re-compiled with MMWAVE-SDK 3.3.0 or above to run on xWR1843</p> <p>Makefile: No change required if you are using SDK makefiles, as this is automatically handled in the SDK 3.3 environment setup script: C:\ti\mmwave_sdk_03_03_xx_xx\packages\scripts\windows\setenv.bat</p> <p>OR</p> <p>CCS Projectspec: If the application is compiled using CCS projectspecs, you need to update the products property in DSS and MSS projectspecs as shown below.</p> <pre><property name="products" value="com.ti.rtsc.SYSBIOS:6.73.01.01;com.ti.MMWAVE_SDK:3.3.0.03;" /></pre> <p>Example: For reference CCS projects for xWR1843, see the 18xx – mmWave SDK Demo available in: MMWAVE Industrial Toolbox.</p>
2	Change device type	Makefile OR CCS projects	<p>Makefile: For SDK makefile based build, set MMWAVE_SDK_DEVICE=iwr18xx/awr18xx in setenv.bat. C:\ti\mmwave_sdk_03_03_xx_xx\packages\scripts\windows\setenv.bat</p> <p>OR</p> <p>CCS Projectspec: If the application is compiled using CCS projectspecs, change the define SOC_XWR16XX to SOC_XWR18XX in DSS and MSS projectspecs.</p> <p>Example: For reference CCS projects for xWR1843, see the 18xx – mmWave SDK Demo available in: MMWAVE Industrial Toolbox.</p>
3	Update RadarSS firmware file path	Makefile OR CCS projects (mss)	<p>Need to use xWR18xx_radarss_rprc.bin in the metaimage generation step. Makefile: No change required if you are using SDK makefiles, as this is automatically handled in the SDK 3.3 environment setup script based on the MMWAVE_SDK_DEVICE variable.</p> <p>OR</p> <p>CCS Projectspec: If the application is compiled using CCS projectspecs, replace xwr16xx_radarss_rprc.bin with xWR18xx_radarss_rprc.bin in the metaimage generation steps (postbuild steps)</p> <p>Example: For reference CCS projects for xWR1843, see the 18xx – mmWave SDK Demo available in: MMWAVE Industrial Toolbox.</p>

Table 2-2. xWR1642 to xWR1843 Software Migration (continued)

No	Summary	Components Impacted	Required Changes
4	Use xWR18xx platform linker command file	Makefile OR CCS projects	<p>Makefile: No change required if you are using SDK makefiles, as this is automatically handled in the SDK 3.3 environment setup script based on the MMWAVE_SDK_DEVICE variable.</p> <p>OR</p> <p>CCS Projectspec: If the application is compiled using CCS projectspecs, update the include paths for r4f_linker.cmd and c674x_linker.cmd to: COM_TI_MMWAVE_SDK_INSTALL_DIR/packages/ti/platform/xwr18xx/r4f_linker.cmd and COM_TI_MMWAVE_SDK_INSTALL_DIR/packages/ti/platform/xwr18xx/c674x_linker.cmd, respectively.</p> <p>Example: For reference CCS projects for xWR1843, see the 18xx – mmWave SDK Demo available in: MMWAVE Industrial Toolbox.</p>
5	Include xWR18xx driver and CLI libs	Makefile OR CCS projects	<p>Makefile: No change required if you are using SDK makefiles, as this is automatically handled in the SDK 3.3 environment setup script based on the MMWAVE_SDK_DEVICE variable.</p> <p>OR</p> <p>CCS Projectspec: If the application is compiled using CCS projectspecs, update the linker include paths to select the *_xwr18xx.aer4f and *_xwr18xx.xe674 lib versions, for example: -llibsoc_xwr18xx.ae674, -llibsoc_xwr18xx.xe674, -llibcli_xwr18xx.aer4f</p>
6	Update sensor front-end configuration parameters	CLI config file (.cfg) and/or source code	<p>Update TX channel bitmap in chirpCfg CLI command and/or API to account for the 3rd TX.</p> <p>Example: For more information, see the sample config files in C:\ti\mmwave_sdk_03_03_xx_xx\packages\ti\demo\xwr18xx\mmw\profiles.</p>
7	Replace 16xx SOC definitions with 18xx equivalents.	MSS/DSS source code	<p>Replace SOC_XWR16XX_* definitions/macros in source code with corresponding SOC_XWR18XX_* definitions.</p> <p>For instance: Replace SOC_XWR16XX_MSS_ADCBUF_BASE_ADDRESS with SOC_XWR18XX_MSS_ADCBUF_BASE_ADDRESS, Similarly, in Pinmux configuration code: Replace SOC_XWR16XX_PINN5_PADBE with SOC_XWR18XX_PINN5_PADBE and so forth. The image below shows reference code difference between the SDK 16xx and 18xx mmw demos File: mmwave_sdk_03_03_xx_xx\packages\ti\demo\xwr18xx\mmw\mss\mss_main.c Code Snapshot: see Section 5.7.</p>
8	API update for MMWave_open SDK 3.3 requires new parameter to be passed to MMWave_open	MSS/DSS start-up code	<p>MMWave_open: Application must set the value of calibMonTimeUnit parameter before calling MMWave_open as shown below. The image below shows reference code updates in the SDK 68xx mmw demo (same applies to 18xx mmw demo) File: mmwave_sdk_03_03_xx_xx\packages\ti\demo\xwr68xx\mmw\mss\mss_main.c Code Snapshot: see Section 5.1.</p>

Table 2-2. xWR1642 to xWR1843 Software Migration (continued)

No	Summary	Components Impacted	Required Changes
9	API update for ADCBuf_open SDK 3.3 requires new parameter to be passed to ADCBuf_open	MSS/DSS start-up code	ADCBUF_open: Application must set the value of soCHandle in the ADCBufparams structure before calling ADCBUF_open as shown below. The image below shows reference code updates in the SDK 68xx mmw demo (same applies to 18xx mmw demo). File: mmwave_sdk_03_03_xx_xx\packages\ti\demo\utils\mmwdemo_adccconfig.c Code Snapshot: see Section 5.2 .
10	API update for CANFD_init SDK 3.3 requires new parameter to be passed to CANFD_init	Drivers	CANFD_init: Applications using CANFD driver must pass instance ID to the CANFD_init API as shown below. Only a value of 0 is supported at this time. The image below shows reference code updates in the SDK CANFD driver test (same for 18xx). File: mmwave_sdk_03_03_xx_xx\packages\ti\drivers\canfd\test\xwr618xx\main.c Code Snapshot: see Section 5.3 .
11	General note on CLI configuration file	Sensor Configuration	For applications that re-use the mmWave demo framework, ensure that the configuration commands (profileCfg, chirpCfg, frameCfg, and so forth) follow the format provided in the sample configuration files provided in the mmw demo directory: C:\ti\mmwave_sdk_03_03_xx_xx\packages\ti\demo\xwr18xx\mmw\profiles. For more information, see the <i>Configuration File Format</i> section of the <i>mmwave SDK User's Guide</i> . See Section 6 .

3 xWR6843AoP ES2.0 Migration

This section provides migration guidance to port Hardware and software from the xWR6843AoP ES1.0 to the xWR6843AoP ES2.0 device. The information provided here is meant to cover the major changes for migrating to a particular MMWAVE-SDK release at the time of writing. For more information, see the *Migration* section in the [MMWAVE-SDK Release Notes](#).

3.1 Hardware Changes From xWR6843AoP ES1.0 to xWR6843AoP ES2.0

The changes described in this section are relevant when migrating xWR6843AoP ES1.0 hardware to xWR6843AoP ES2.0. [Figure 3-1](#) shows the device symbolization change from ES1.0 to ES2.0 on device part marking.

Left side device marking shows ES1.0 silicon and right side device marking shows ES2.0 silicon. For more details on the device marking, see the [xWR6843 Device Errata, Silicon Revisions 1. and 2.0](#).

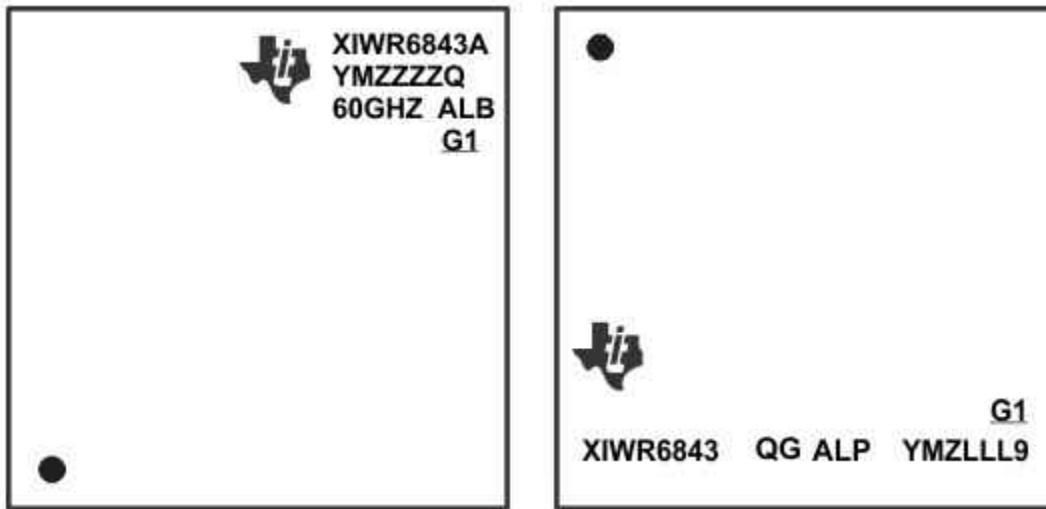


Figure 3-1. Silicon Device Marking Difference Between xWR6843AoP ES1.0 and ES2.0

Table 3-1. xWR6843AoP ES1.0 to xWR6843AoP ES2.0 Hardware Changes

No	Summary	xWR6843AoP ES1.0	xWR6843AoP ES2.0
1	QSPI interface speed has been improved. This enables faster boot loading, note that supported flashes are listed in the Flash Variants Supported by the mmWave Sensor .	Max 40 MHz	Max 80 MHz
2	Boot loader enhancement has been made. This allows faster boot and stability across devices	Boot loader code used to do the APLL calibration	Closed loop APLL calibration will be done by BSS
3	Tx beam scanning is introduced	No support	Supported
4	Memory compression (Depending upon the compression ratio of the RADAR data cube larger memory would be available for code and remaining data)	No support	Supported
5	Calibration is supported (This improves the performance and stability of the device across temperature)	No Calibration	Calibration supported
6	Clock gating at power-up and IP clock gating based on use-case, this should improve the power saving	No clock gating	Clock gated on unused peripherals. Device low level drivers un-gates the clock depending upon the peripheral used
7	RF Improvements –RX NF (Improved range and accuracy)	Baseline	Improved (Please refer to the data sheet for exact number)
8	RF Improvements –CLK PN (Improved accuracy)	Baseline	Improved (Please refer to the data sheet for exact number)

Table 3-1. xWR6843AoP ES1.0 to xWR6843AoP ES2.0 Hardware Changes (continued)

No	Summary	xWR6843AoP ES1.0	xWR6843AoP ES2.0
9	Package change	Baseline	Improved package (Please refer to the data sheet for detailed package information)
10	Changes in Antenna virtual Array	Baseline	Improvement in package routing caused changes in the antenna elements, hence there is change in virtual antenna array between ES1 and ES2.0. See Table 3-2

3.2 Software Migration From xWR6843AoP ES1.0 to xWR6843AoP ES2.0

The changes described in this section are relevant for migrating the xWR6843AoP ES1.0 software based on the SDK 3.2.0.6 to xWR6843AoP ES2.0 and SDK 3.4.

Besides the addition of the Antenna on Package and a different antenna pattern, xWR6843AoP ES2.0 re-uses the same silicon. Hence software migration from xWR6843AoP ES1.0 to xWR6843AoP ES2.0 broadly includes the following steps in order:

1. Initial migration of software to xWR6843ES2.0 (from MMWAVE-SDK 3.2.0.6 to MMWAVE-SDK 3.4).
(Referred to below as **Platform Software Updates**)
2. Angle of Arrival Processing updates for the updated antenna pattern on xWR6843AoP ES2.0. (Referred to below as **AoA Software Updates**)

Note

MMWAVE-SDK 3.4.0 is the first baseline SDK release for xWR6843AoP ES2.0 device and the scope of migration notes provided in this section is limited to migrating to MMWAVE-SDK 3.4.0

When migrating existing xWR6843 AoP ES2.0 applications to SDK releases beyond MMWAVE SDK 3.4, you should follow the incremental migration instructions provided in the corresponding SDK release notes.

3.2.1 xWR6843AoP ES2.0 - Platform Software Updates

Table 3-2. xWR6843AoP ES2.0 Software - Platform Updates

No	Summary	Components Impacted	Required Changes
1	MMWAVE-SDK 3.4.0 or above required for xWR6843AoP ES2.0	Makefile OR CCS projects	<p>Application code must be re-compiled with MMWAVE-SDK 3.4.0 or above to run on xWR6843AoP ES2.0 as prior SDK versions are not compatible with ES2.0. Conversely, SDK 3.4.0 is not compatible with xWR6843AoP ES1.0 devices.</p> <p>Makefile: No change required if you are using SDK makefiles, as this is automatically handled in the SDK 3.4 environment setup script: C:\ti\mmwave_sdk_03_04_xx_xx\packages\script\windows\setenv.bat OR</p> <p>CCS Projectspec: If the application is compiled using CCS projectspecs, you need to update the products property in DSS and MSS projectspecs as shown below.</p> <pre><property name="products" value="com.ti.rtsc.SYSBIOS:6.73.01.01;com.ti.MMWAVE_SDK:3.4.0.03;"></pre> <p>Example: For reference CCS projects for xWR6843AoP ES2.0, see the 68xx AoP – mmWave SDK Demo available in MMWAVE Industrial Toolbox.</p>
2	Change the value of SHMEM_ALLOC parameter in Metalimage (flashable) binary generation step.	Makefile OR CCS projects (mss).	<p>The value of SHMEM_ALLOC parameter should be set to 0x00000006 for ES2.0 (it was 0x02000006 for ES1.0 device).</p> <p>Makefile: No change required if you are using SDK makefiles build, as this is automatically handled in the SDK 3.4 device specific makefiles. OR</p> <p>CCS Projectspec: If the application is compiled using CCS projectspecs, update the postBuildStep in MSS projectspec to replace the value 0x02000006 with 0x00000006.</p> <p>Example: For reference CCS projects for xWR6843AoP ES2.0 , see the 68xx AoP – mmWave SDK Demo available in MMWAVE Industrial Toolbox.</p>

Table 3-2. xWR6843AoP ES2.0 Software - Platform Updates (continued)

No	Summary	Components Impacted	Required Changes
3	Update RadarSS firmware file name	Makefile OR CCS projects (mss)	<p>The RadarSS binary for xwr6xxx devices is now called xwr6xxx_radarss_rprc.bin instead of iwr6xxx_radarss_rprc.bin. Makefile: No change required if you are using SDK makefiles, as this is automatically handled in the SDK 3.4 environment setup script based on the MMWAVE_SDK_DEVICE variable.</p> <p>OR</p> <p>CCS Projectspec: If the application is compiled using CCS projectspecs, replace iwr6xxx_radarss_rprc.bin with xwr6xxx_radarss_rprc.bin in the metainimage generation steps (postbuild steps)</p> <p>Example: For reference CCS projects for xWR6843AoP ES2.0 , see the 68xx AoP – mmWave SDK Demo available in MMWAVE Industrial Toolbox.</p>
4	API update for MMWave_open SDK 3.3 and above requires a new parameter to be passed to MMWave_open	MSS/DSS start-up code	<p>MMWave_open: Application must set the value of calibMonTimeUnit parameter before calling MMWave_open as shown below. The image below shows reference code updates in the SDK 68xx mmw demo</p> <p>File: <code>mmwave_sdk_03_04_xx_xx\packages\ti\demo\xwr68xx\mmw\mss\mss_main.c</code></p> <p>Code Snapshot: see Section 5.1</p>
5	API update for ADCBuf_open SDK 3.3 and above requires a new parameter to be passed to ADCBuf_open	MSS/DSS start-up code	<p>ADCBUF_open: Application must set the value of socHandle in the ADCBufparams structure before calling ADCBUF_open as shown below. The image below shows reference code updates in the SDK 68xx mmw demo.</p> <p>File: <code>mmwave_sdk_03_04_xx_xx\packages\ti\demo\utils\mmwdemo_adcconfig.c</code></p> <p>Code Snapshot: see Section 5.2</p>
6	API update for CANFD_init SDK 3.3 and above requires new parameter to be passed to CANFD_init	Drivers	<p>CANFD_init: Applications using CANFD driver must pass instance ID to the CANFD_init API as shown below. Only a value of 0 is supported at this time. The image below shows reference code updates in the SDK CANFD driver test.</p> <p>File: <code>mmwave_sdk_03_04_xx_xx\packages\ti\drivers\canfd\test\xwr68xx\main.c</code></p> <p>Code Snapshot: see Section 5.3</p>
7	SDK 3.3 and above removes support for Bus error interrupt from the DMA driver for xWR6843 ES2 as that interrupt is not hooked up to the device.	Drivers	Application would get an error code back from the xwr68xx driver if DMA_enable Interrupt API is called for DMA_IntType_BER. You can either remove the call to the above API or ignore the error; however you should review the DMA usage to make sure there is no invalid memory access via MSS DMA engine.
8	General note on CLI configuration file	Sensor Configuration	For applications that re-use the mmWave demo/CLI framework, ensure that the configuration commands (for example, profileCfg, chirpCfg, frameCfg, and so forth) follow the format provided in sample configuration files provided in the mmw demo directory: <code>C:\ti\mmwave_sdk_03_04_xx_xx\packages\ti\demo\xwr68xx\mmw\profiles</code> . for more details, see the <i>Configuration File Format</i> section in the mmwave SDK User's Guide . Section 6

Table 3-2. xWR6843AoP ES2.0 Software - Platform Updates (continued)

No	Summary	Components Impacted	Required Changes
9	BSS clock un-gate required in Secondary bootloader	Secondary Bootloader	<p>Note: This update is not related to the main application. It is needed only if you are using a custom secondary bootloader in your system. The Secondary Bootloader must ungate BSS clock using SOC gate/ungate API before downloading image to RadarSS/BSS memory as shown below.</p> <p>The image below shows reference code updates in the SDK secondary bootloader example.</p> <p>File: <code>C:\ti\mmwave_sdk_03_04_xx_xx\packages\ti\utils\lsb\platform\lsb_xwr68xx.c</code></p> <p>Code Snapshot: see Section 5.4</p>
10	SDK 3.4 mmWave layer enables all valid init time and runtime calibrations for xwr6xxx devices	MSS/DSS start-up code	<p>Application should pass valid values for <code>freqLimitLow</code> and <code>freqLimitHigh</code> in <code>mmWave_Open</code> API and can now enable periodic calibrations in <code>mmWave_Start</code> API</p> <p>The image below shows reference code updates in the SDK 68xx mmw demo.</p> <p>File: <code>mmwave_sdk_03_04_00_03\packages\ti\demo\xwr68xx\mmw\mss\mss_main.c</code></p> <p>Code Snapshot: see Section 5.8</p>
11	Object detection DPC accepts antenna geometry to enable wider configurations of Tx/Rx antennas	DPCconfiguration	<p>This field is mandatory only for HWA-based Object detection DPC when compiled to use the new AoA 2D algorithm (in the xwr64xx AoP mmw demo). For DSP-based DPC and for HWA-based DPC that uses standard AoA DPU, this field is unused.</p> <p>The image below shows the reference code in the SDK 64xx mmw demo. File: <code>mmwave_sdk_03_04_00_03\packages\ti\demo\xwr64xx\mmw\main.c</code></p> <p>Code Snapshot: see Section 5.14</p>
12	Object Detection HWA DPC now accepts Range FFT Scaling Parameters	DPC configuration	<p>Range HWA-based DPU and Object detection HWA-based DPCs now allow you to set the scaling values for butterfly stages and converting from internal 24-bit to 16- bit output</p> <p>The image below shows the reference code in the SDK 64xx mmw demo. File: <code>mmwave_sdk_03_04_00_03\packages\ti\demo\xwr64xx\mmw\main.c</code></p> <p>Code Snapshot: see Section 5.9</p>
13	Object detection Range HWA DPC now allows user to specify the radar cube format	DPC Configuration	<p>ObjDetRangeHWA DPC allows user to specify the radar cube format to allow flexibility in integrating various DSP based algorithms/ processing chains</p> <p>Note: mmW demos support only <code>DPIF_RADARCUBE_FORMAT_</code></p> <p>The image below shows the reference code in the SDK 68xx mmw demo. File: <code>mmwave_sdk_03_04_00_03\packages\ti\demo\xwr68xx\mmw\mss\mss_main.c</code></p> <p>Code Snapshot: see Section 5.10</p>
14	Updates related to saving/restoring device calibration parameters (Phase shift calibration parameters)		<p>For more details on this and other calibration related updates, see the MMWAVE-SDK 3.4.0 release notes in the Migration Notes.</p>

3.2.2 xWR6843AoP ES2.0 - AoA Software Updates

Figure 3-2 and Figure 3-3 compare the antenna geometries of xWR6843AOP ES1.0 and xWR6843AOP ES2.0.

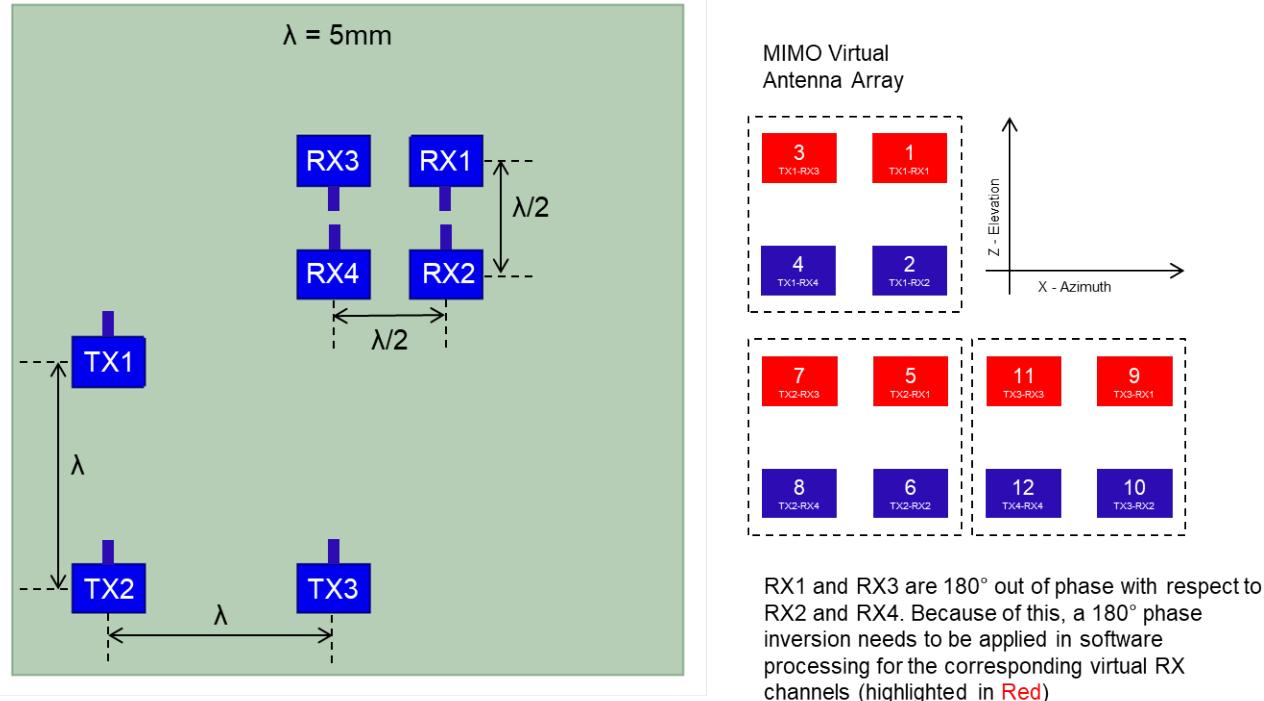


Figure 3-2. xWR6843AoP ES1.0 Antenna Geometry and Resulting MIMO Virtual Antenna Array

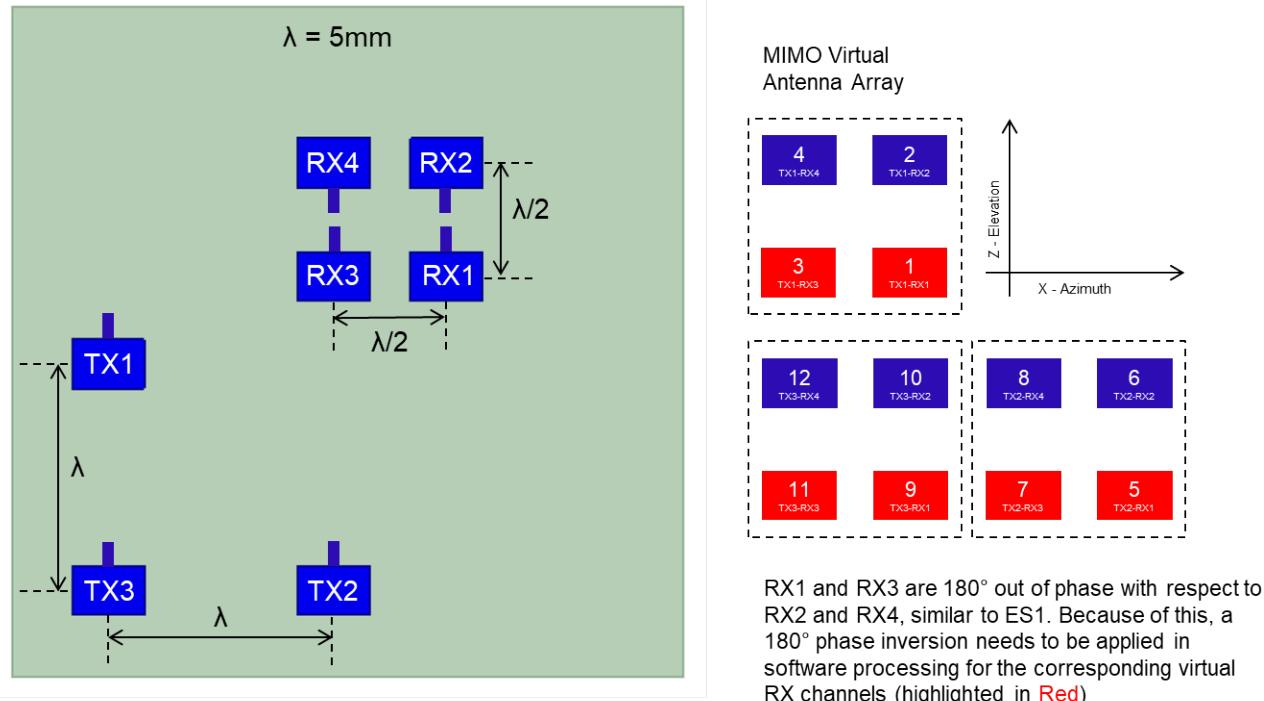


Figure 3-3. xWR6843AoP ES2.0 Antenna Geometry and Resulting MIMO Virtual Antenna Array

The key antenna updates in xWR6843AOP ES2, as shown above are:

- **RX Antennas:** RX1 and RX2 are swapped on xWR6843AOP ES2. Similarly RX3 and RX4 are swapped
- **TX Antennas:** TX2 and TX3 are swapped on xWR6843AOP ES2.
- **Line Feed:** The RX line feeds on xWR6843AOP ES2 are same as on ES1 i.e. RX1 and RX2 are fed from opposite ends, which results in a 180° phase difference between RX1 and RX2. Similarly, RX3 and RX4 are

out of phase by 180°. To compensate for the opposite line feeds, a 180° phase inversion needs to be applied in software processing for the corresponding virtual channels as shown in Figure 3-3.

MMWAVE-SDK 3.2.0.6 and MMWAVE-SDK 3.4 include the AoA2dProc DPU which performs Angle of Arrival processing for the xWR6843 AoP antenna array using the Hardware Accelerator. The AoA2dProc DPU (Datapath Processing Unit) is used in the xWR64xx AoP mmw demo for angle of arrival processing.

To understand the AoA updates needed for xWR6843AOP ES2, it is recommended to understand the antenna geometry concept defined in AoA2dProc DPU.

1. Navigate to C:\ti\mmwave_sdk_03_04_xx_xx\docs and open the file mmwave_sdk_module_documentation.html in a browser.
2. Click on the AoA using 2D FFT method link as highlighted in the picture below:

Datapath

- **Data Processing Chains (DPC)**
 - [Object Detection using DSP](#)
 - [Object Detection using HWA](#)
 - [Object Detection with only rangeProcHWA DPU](#)
- **Data Processing Chains (DPC) test**
 - [Unit test for Object Detection using DSP](#)
 - [Unit test for Object Detection using HWA](#)
- **Data Processing Units (DPU)**
 - [AoA \(angle of arrival\)](#)
 - [AoA \(angle of arrival\) using 2D FFT method](#) 
 - [CFAR](#)
 - [Doppler](#)
 - [Range](#)
 - [Static Clutter Removal](#)

Figure 3-4. AoA2dProc HTML Documentation

3. Scroll down to the section named Antenna Geometry Definition, which explains how the generic antenna geometry structure is defined and used by the HWA AoA2dProc DPU code. The antenna geometry for a specific antenna (for example, xWR6843AoP ES2.0) is defined in the corresponding C structure in **mmwave_sdk_03_04_xx_xx\packages\ti\board\antenna_geometry.c**.

The image below shows the antenna geometry structure update for xWR6843AoP ES2.0 as compared to xWR6843AoP ES1.0 in MMWAVE-SDK 3.2.0.6.

Code Snapshot: see [Section 5.13](#)

The antenna geometry structure is passed to the Object Detection DPC during initialization in **mmwave_sdk_03_04_xx_xx\packages\ti\demo\xwr64xx\mmw\main.c**

Code Snapshot: see [Section 5.14](#)

RX Channel Phase Compensation: To compensate for the opposite line feeds as shown in [Section 5.12](#), a 180° phase inversion is applied to the corresponding RX channels (including virtual channels) using the compRangeBiasAndRxChanPhase CLI command available in the mmw demo.

Figure 3-5, from the MMWAVE-SDK user's guide, explains the structure of this command.

compRangeBiasAndRxChanPhase	<p>Command for datapath to compensate for bias in the range estimation and receive channel gain and phase imperfections. Refer to the procedure mentioned here</p> <p>The values in this command can be changed between sensorStop and sensorStart and even when the sensor is running.</p> <p>This is a mandatory command.</p>	<rangeBias> Compensation for range estimation bias in meters	supported
		<Re(0,0)> <Im(0,0)> <Re(0,1)> <Im(0,1)> ... <Re(0,R-1)> <Im(0,R-1)> <Re(1,0)> <Im(1,0)> ... <Re(T-1,R-1)> <Im(T-1,R-1)> Set of Complex value representing compensation for virtual Rx channel phase bias in Q15 format. Pairs of I and Q should be provided for all Tx and Rx antennas in the device	For xwr1843, xwr6843 and xwr6443 demos: 12 pairs of values should be provided here since the device has 4 Rx and 3 Tx (total of 12 virtual antennas). Note the sign reversal required for phase compensation coefficients in xwr6443 demo running on IWR6843AOP device. For xwr1642 demo: 8 pairs of values should be provided here since the device has 4 Rx and 2 Tx (total of 8 virtual antennas)

Figure 3-5. RX Channel Phase Compensation: CompRangeBiasAndRxChanPhase CLI Command

To understand the CompRangeBiasAndRxChanPhase values configured in the example AoP profile configuration provided in MMWAVE-SDK, see [Section 5.15](#).

4 Helpful Resources

The following resources provide example source code, makefile and CCS projects for the xWR6843 ES2.0 and the xWR1843 devices.

Resource Name	File-System Path / Web URL	Content Reference
MMWAVE-SDK 3.3 mmw demo	68xx - C:\ti\mmwave_sdk_03_03_xx_xx\packages\ti\demo\xwr68xx\mmw 18xx - C:\ti\mmwave_sdk_03_03_xx_xx\packages\ti\demo\xwr18xx\mmw	Source code, Makefiles, Configuration files (.cfg)
MMWAVE-SDK 3.4 mmw demo	68xx - C:\ti\mmwave_sdk_03_04_xx_xx\packages\ti\demo\xwr68xx\mmw 64/68xxAoP C:\ti\mmwave_sdk_03_04_xx_xx\packages\ti\demo\xwr64xx\mmw	
MMWAVE Industrial Toolbox	MMWAVE Industrial Toolbox 68xx ISK – mmWave SDK Demo – DSP Version 64/68xx AoP - mmWave SDK Demo 68xx AoP 18xx – mmWave SDK Demo And various other demos included in Industrial Toolbox	Reference CCS Projectspecs for mmWave SDK mmw demos and other application specific demos.

5 Code Snapshots

This section provides code snapshots for the migrations notes presented in the previous sections.

5.1 SDK 3.3 API Change for MMWave_open

MMWAVE-SDK 3.2.1 vs MMWAVE-SDK 3.3.0

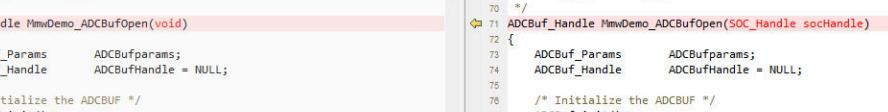
File: mmwave_sdk_03_03_00_0x\packages\ti\demo\xwr68xx\mmw\mss_main.c

Figure 5-1. SDK 3.3 API Change for MMWave_open

5.2 SDK 3.3 API Change for ADCBuf_open

MMWAVE-SDK 3.2.1 vs MMWAVE-SDK 3.3.0

File: mmwave_sdk_03_03_00_0x\packages\ti\demo\xwr68xx\mmw\mss_main.c



```
8/7/2019 11:27:49 AM 7,583 bytes C,C++,C# Source ▾ ANSI ▾ UNIX
69 * Fail NULL
70 */
71 ADCBuf_Handle MmwDemo_ADCBufOpen(void)
72 {
73     ADCBuf_Params    ADCBufparams;
74     ADCBuf_Handle   ADCBufHandle = NULL;
75
76     /* Initialize the ADCBUF */
77     ADCBuf_init();
78
79     /**
80      * Start ADCBUF driver:
81      */
82
83     /* ADCBUF Params initialize */
84     ADCBuf_params_init(&ADCBufparams);
85     ADCBufparams.chipThresholdPing = 1;
86     ADCBufparams.chipThresholdPong = 1;
87     ADCBufparams.continousMode = 0;
88
89     /* Open ADCBUF driver */
90     ADCBufHandle = ADCBuf_open(0, &ADCBufparams);
91
92     return ADCBufHandle;
93 }

9/3/2019 6:16:41 PM 7,644 bytes C,C++,C# Source ▾ ANSI ▾ UNIX
69 * Fail NULL
70 */
71 ADCBuf_Handle MmwDemo_ADCBufOpen(SOC_Handle socHandle)
72 {
73     ADCBuf_Params    ADCBufparams;
74     ADCBuf_Handle   ADCBufHandle = NULL;
75
76     /* Initialize the ADCBUF */
77     ADCBuf_init();
78
79     /**
80      * Start ADCBUF driver:
81      */
82
83     /* ADCBUF Params initialize */
84     ADCBuf_params_init(&ADCBufparams);
85     ADCBufparams.chipThresholdPing = 1;
86     ADCBufparams.chipThresholdPong = 1;
87     ADCBufparams.continousMode = 0;
88
89     /* Set SOC handle */
90     ADCBufparams.socHandle = socHandle;
91
92     /* Open ADCBUF driver */
93     ADCBufHandle = ADCBuf_open(0, &ADCBufparams);
94
95     return ADCBufHandle;
96 }
```

Figure 5-2. SDK 3.3 API Change for ADCBuf_open

5.3 SDK 3.3 API Change for CANFD_init

MMWAVE-SDK 3.2.1 vs MMWAVE-SDK 3.3.0

File: mmwave_sdk_03_03_00_0x\ packages\ti\drivers\canfd\test\xwr68xx\main.c

<pre> 395 static int32_t mcanLoopbackTest() 396 { 397 CANFD_Handle canHandle; 398 CANFD_MsgObjHandle txMsgObjHandle; 399 CANFD_MsgObjHandle rxMsgObjHandle; 400 int32_t retVal = 0; 401 int32_t errCode = 0; 402 CANFD_OptionTLV optionTLV; 403 uint8_t value; 404 CANFD_MCANInitParams mcanCfgParams; 405 CANFD_MCANBitTimingParams mcanBitTimingParams; 406 CANFD_MCANMsgObjCfgParams txMsgObjectParams; 407 CANFD_MCANMsgObjCfgParams rxMsgObjectParams; 408 CANFD_MCANLoopbackCfgParams mcanLoopbackParams; 409 CANFD_MCANMsgObjectStats msgObjStats; 410 411 gTxDoneFlag = 0; 412 413 MCANAppInitParams (&mcanCfgParams); 414 415 /* Initialize the CANFD driver */ 416 canHandle = CANFD_init(&mcanCfgParams, &errCode); 417 if (canHandle == NULL) 418 { 419 System_printf ("Error: CANFD Module Initialization failed [Error %d]\n", errCode); 420 return -1; 421 } </pre>	<pre> 398 static int32_t mcanLoopbackTest() 399 { 400 CANFD_Handle canHandle; 401 CANFD_MsgObjHandle txMsgObjHandle; 402 CANFD_MsgObjHandle rxMsgObjHandle; 403 int32_t retVal = 0; 404 int32_t errCode = 0; 405 CANFD_OptionTLV optionTLV; 406 uint8_t value; 407 CANFD_MCANInitParams mcanCfgParams; 408 CANFD_MCANBitTimingParams mcanBitTimingParams; 409 CANFD_MCANMsgObjCfgParams txMsgObjectParams; 410 CANFD_MCANMsgObjCfgParams rxMsgObjectParams; 411 CANFD_MCANLoopbackCfgParams mcanLoopbackParams; 412 CANFD_MCANMsgObjectStats msgObjStats; 413 414 gTxDoneFlag = 0; 415 416 MCANAppInitParams (&mcanCfgParams); 417 418 /* Initialize the CANFD driver */ 419 canHandle = CANFD_init(gInstanceId, &mcanCfgParams, &errCode); 420 if (canHandle == NULL) 421 { 422 System_printf ("Error: CANFD Module Initialization failed [Error %d]\n", errCode); 423 return -1; 424 } </pre>
--	---

Figure 5-3. SDK 3.3 API Change for CANFD_init

5.4 SDK 3.3 68xx Secondary Bootloader Update

MMWAVE-SDK 3.2.1 vs MMWAVE-SDK 3.3.0

File: mmwave_sdk_03_03_00_02\ packages\ti\utils\sbl\platform\sbl_xwr68xx.c

<pre> 432 { 433 offset = (uint32_t)SBL_BSS_SHARED_MEM_TCMB_OFFSET; 434 } 435 else if ((sectionPtr + sectionLen) <= SBL_BSS_SECTION_END_ADDR) 436 { 437 offset = (uint32_t)SBL_BSS_SHARED_MEM_OFFSET; 438 } 439 else 440 { 441 offset = 0U; 442 gSblMCB.errorStatus = SBL_RPRC_PARSER_BSS_FILE_OFFSET_MISMATCH; 443 } 444 445 /* Configure the MPU settings for BSS section */ 446 if (gSblMCB.bssMpuInit == 0) 447 { 448 gSblMCB.bssMpuInit = 1U; 449 450 /* Enable the regions */ 451 SBL_mpuConfigBSS(true); 452 } 453 } </pre>	<pre> 433 { 434 offset = (uint32_t)SBL_BSS_SHARED_MEM_TCMB_OFFSET; 435 } 436 else if ((sectionPtr + sectionLen) <= SBL_BSS_SECTION_END_ADDRESS) 437 { 438 offset = (uint32_t)SBL_BSS_SHARED_MEM_OFFSET; 439 } 440 else 441 { 442 offset = 0U; 443 gSblMCB.errorStatus = SBL_RPRC_PARSER_BSS_FILE_OFFSET_MISMATCH; 444 } 445 446 /* Configure the MPU settings for BSS section */ 447 if (gSblMCB.bssClockMpuInit == 0) 448 { 449 gSblMCB.bssClockMpuInit = 1U; 450 451 /* ungate clock */ 452 SOC_ungateClock(gSblMCB.socHandle, SOC_MODULE_BSS, &errCode); 453 454 /* Enable the regions */ 455 SBL_mpuConfigBSS(true); 456 } 457 } </pre>
---	---

Figure 5-4. SDK 3.3 68xx Secondary Bootloader Update

5.5 SDK 3.3 16xx vs 68xx: Calibration Frequency Update

MMWAVE-SDK 3.3.0 mmw demo (16xx vs 68xx)

File: mmwave_sdk_03_03_00_0x\packages\ti\demo\xwr68xx\mmw\mss_main.c

<pre>C:\ti\mmwave_sdk_03_03_00_02\packages\ti\demo\xwr16xx\mmw\mss\mss_main.c 9/3/2019 6:16:41 PM 150,676 bytes C,C++,C# Source ANSI UNIX 2935 2936 /* Open mmWave module, this is only done once */ 2937 /* Setup the calibration frequency:*/ 2938 2939 gMmwMssMCB.cfg.openCfg.freqLimitLow = 760U; 2940 gMmwMssMCB.cfg.openCfg.freqLimitHigh = 810U; 2941 2942 /* start/stop async events */ 2943 gMmwMssMCB.cfg.openCfg.disableFrameStartAsyncEvent = false; 2944 gMmwMssMCB.cfg.openCfg.disableFrameStopAsyncEvent = false; 2945 2946 /* No custom calibration: */ 2947 gMmwMssMCB.cfg.openCfg.useCustomCalibration = false; 2948 gMmwMssMCB.cfg.openCfg.customCalibrationEnableMask = 0x0; 2949 2950 /* calibration monitoring base time unit 2951 * setting it to one frame duration as the demo doesnt support any 2952 * monitoring related functionality 2953 */ 2954 2955 gMmwMssMCB.cfg.openCfg.calibMonTimeUnit = 1; 2956 2957 /* Open the mmWave module: */ 2958 if (MMWave_Open (gMmwMssMCB.ctrlHandle, &gMmwMssMCB.cfg.openCfg, NULL 2959 { 2960 /* Error: decode and Report the error */ 2961 MMWave_DecodeError (errCode, &errLevel, &mmwWaveErrorCode, &subs 2962 System_printf ("Error: mmWave Open failed %d level:%d [Error code 2963 errCode, errLevel, mmwWaveErrorCode, subsysError 2964 2965 /* Error: decode and Report the error */ 2966 MMWave_DecodeError (errCode, &errLevel, &mmwWaveErrorCode, &subs 2967 System_printf ("Error: mmWave Open failed [Error code: %d Subsystem: %d]\n 2968 mmwWaveErrorCode, subsysErrorCode); 2969 2970 } 2971 2972 }</pre>	<pre>C:\ti\mmwave_sdk_03_03_00_02\packages\ti\demo\xwr68xx\mmw\mss\mss_main.c 9/3/2019 6:16:41 PM 164,371 bytes C,C++,C# Source ANSI PC 3242 3243 return -1; 3244 3245 /* Open mmWave module, this is only done once */ 3246 /* Setup the calibration frequency: 3247 * TODO: Presently DFP does not support these for 68xx platform, 3248 * need to change when DFP is updated with the support */ 3249 gMmwMssMCB.cfg.openCfg.freqLimitLow = 0U; 3250 gMmwMssMCB.cfg.openCfg.freqLimitHigh = 0U; 3251 3252 /* start/stop async events */ 3253 gMmwMssMCB.cfg.openCfg.disableFrameStartAsyncEvent = false; 3254 gMmwMssMCB.cfg.openCfg.disableFrameStopAsyncEvent = false; 3255 3256 /* No custom calibration: */ 3257 gMmwMssMCB.cfg.openCfg.useCustomCalibration = false; 3258 gMmwMssMCB.cfg.openCfg.customCalibrationEnableMask = 0x0; 3259 3260 /* calibration monitoring base time unit 3261 * setting it to one frame duration as the demo doesnt support any 3262 * monitoring related functionality 3263 */ 3264 3265 gMmwMssMCB.cfg.openCfg.calibMonTimeUnit = 1; 3266 3267 /* Open the mmWave module: */ 3268 if (MMWave_Open (gMmwMssMCB.ctrlHandle, &gMmwMssMCB.cfg.openCfg, NULL, &errCo 3269 { 3270 /* Error: decode and Report the error */ 3271 MMWave_DecodeError (errCode, &errLevel, &mmwWaveErrorCode, &subs 3272 System_printf ("Error: mmWave Open failed [%d level:%d [Error code 3273 errCode, errLevel, mmwWaveErrorCode, subsysError 3274 3275 /* Error: decode and Report the error */ 3276 MMWave_DecodeError (errCode, &errLevel, &mmwWaveErrorCode, &subs 3277 System_printf ("Error: mmWave Open failed [Error code: %d Subsystem: %d]\n 3278 mmwWaveErrorCode, subsysErrorCode); 3279 3280 } 3281 3282 }</pre>
--	--

Figure 5-5. SDK 3.3 16xx vs 68xx: Calibration Frequency Update

5.6 SDK 3.3 16xx vs 68xx: SoC Definition Updates

MMWAVE-SDK 3.3.0 mmw demo (16xx vs 68xx)

File: mmwave_sdk_03_03_xx_xx\packages\ti\demo\xwr68xx\mmw\mss_main.c

<pre>C:\ti\mmwave_sdk_03_03_00_02\packages\ti\demo\xwr16xx\mmw\mss\mss_main.c 9/3/2019 6:16:41 PM 150,676 bytes C,C++,C# Source ANSI UNIX 3215 3216 static void MmwDemo_PlatformInit(MmwDemo_PlatformCfg *config) 3217 { 3218 3219 /* Setup the PINMUX to bring out the UART-1 */ 3220 Pinmux_Set_OverrideCtrl(SOC_XIR16XX_PINNS_PADBE, PINMUX_OUTEN_RETAIN_HV_CTRL, PINMUX_INPEN_RETAIN 3221 Pinmux_Set_FuncSel(SOC_XIR16XX_PINNS_PADBE, SOC_XIR16XX_PINNS_PADBE_MSS_UARTA_TX); 3222 Pinmux_Set_OverrideCtrl(SOC_XIR16XX_PIN4_PADBD, PINMUX_OUTEN_RETAIN_HV_CTRL, PINMUX_INPEN_RETAIN 3223 Pinmux_Set_FuncSel(SOC_XIR16XX_PIN4_PADBD, SOC_XIR16XX_PIN4_PADBD_MSS_UARTA_RX); 3224 3225 /* Setup the PINMUX to bring out the UART-3 */ 3226 Pinmux_Set_OverrideCtrl(SOC_XIR16XX_PINF14_PADAD, PINMUX_OUTEN_RETAIN_HV_CTRL, PINMUX_INPEN_RETAIN 3227 Pinmux_Set_FuncSel(SOC_XIR16XX_PINF14_PADAD, SOC_XIR16XX_PINF14_PADAD_MSS_UARTB_TX); 3228 3229 //***** 3230 /* Setup the PINMUX: 3231 * GPIO Output: Configure pin K13 as GPIO_2 output 3232 *****/ 3233 Pinmux_Set_OverrideCtrl(SOC_XIR16XX_PINK13_PADADZ, PINMUX_OUTEN_RETAIN_HV_CTRL, PINMUX_INPEN_RETAIN 3234 Pinmux_Set_FuncSel(SOC_XIR16XX_PINK13_PADADZ, SOC_XIR16XX_PINK13_PADADZ_GPIO_2); 3235 3236 //***** 3237 /* Setup the GPIO: 3238 * GPIO Output: Configure pin K13 as GPIO_2 output 3239 *****/ 3240 config->SensorStatusGPIO = SOC_XIR16XX_GPIO_2; 3241 3242 /* Initialize the DEMO configuration: */ 3243 config->sysClockFrequency = MSS_SYS_VCLK; 3244 config->loggingBaudRate = 921600; 3245 config->commandBaudRate = 115200; 3246 3247 }</pre>	<pre>C:\ti\mmwave_sdk_03_03_00_02\packages\ti\demo\xwr68xx\mmw\mss\mss_main.c 9/3/2019 6:16:41 PM 164,371 bytes C,C++,C# Source ANSI PC 3682 3683 static void MmwDemo_PlatformInit(MmwDemo_PlatformCfg *config) 3684 3685 3686 /* Setup the PINMUX to bring out the UART-1 */ 3687 Pinmux_Set_OverrideCtrl(SOC_XIR68XX_PINNS_PADBE, PINMUX_OUTEN_RETAIN_HV_CTRL, PINMUX_INPEN_RETAIN 3688 Pinmux_Set_FuncSel(SOC_XIR68XX_PINNS_PADBE, SOC_XIR68XX_PINNS_PADBE_MSS_UARTA_TX); 3689 Pinmux_Set_OverrideCtrl(SOC_XIR68XX_PIN4_PADBD, PINMUX_OUTEN_RETAIN_HV_CTRL, PINMUX_INPEN_RETAIN 3690 Pinmux_Set_FuncSel(SOC_XIR68XX_PIN4_PADBD, SOC_XIR68XX_PIN4_PADBD_MSS_UARTA_RX); 3691 3692 /* Setup the PINMUX to bring out the UART-3 */ 3693 Pinmux_Set_OverrideCtrl(SOC_XIR68XX_PINF14_PADAD, PINMUX_OUTEN_RETAIN_HV_CTRL, PINMUX_INPEN_RETAIN 3694 Pinmux_Set_FuncSel(SOC_XIR68XX_PINF14_PADAD, SOC_XIR68XX_PINF14_PADAD_MSS_UARTB_TX); 3695 3696 //***** 3697 /* Setup the PINMUX: 3698 * GPIO Output: Configure pin K13 as GPIO_2 output 3699 *****/ 3700 Pinmux_Set_OverrideCtrl(SOC_XIR68XX_PINK13_PADADZ, PINMUX_OUTEN_RETAIN_HV_CTRL, PINMUX_INPEN_RETAIN 3701 Pinmux_Set_FuncSel(SOC_XIR68XX_PINK13_PADADZ, SOC_XIR68XX_PINK13_PADADZ_GPIO_2); 3702 3703 //***** 3704 /* Setup the GPIO: 3705 * GPIO Output: Configure pin K13 as GPIO_2 output 3706 *****/ 3707 config->SensorStatusGPIO = SOC_XIR68XX_GPIO_2; 3708 3709 /* Initialize the DEMO configuration: */ 3710 config->sysClockFrequency = MSS_SYS_VCLK; 3711 config->loggingBaudRate = 921600; 3712 config->commandBaudRate = 115200; 3713 3714 }</pre>
---	---

Figure 5-6. SDK 3.3 16xx vs 68xx: SoC Definition Updates

5.7 SDK 3.3 16xx vs 18xx: SoC Definition Updates

MMWAVE-SDK 3.3.0 mmw demo (16xx vs 18xx)

File: mmwave_sdk_03_03_xx_xx\packages\ti\demo\xwr18xx\mmw\mss_main.c

<pre>C:\ti\mmwave_sdk_03_03_00_02\packages\ti\demo\xwr16xx\mmw\mss_main.c 9/3/2019 6:16:41 PM 160,676 bytes C,C++,C# Source ▾ ANSI ▾ UNIX 3213 */ 3214 static void MmxDemo_platformInit(MmxDemo_platformCfg *config) 3215 { 3216 /* Setup the PINMUX to bring out the UART-3 */ 3217 Pinmux_Set_OVERRIDEctrl(SOC_XR16XX_PINNS_PADB, PINMUX_OUTEN_RETAIN_HU_CTRL, PINMUX_INPEN_RETAIN, 3218 Pinmux_Set_FuncSel(SOC_XR16XX_PINNS_PADB, SOC_XR16XX_PINNS_PADB_MSS_UARTA_RX); 3219 Pinmux_Set_OVERRIDEctrl(SOC_XR16XX_PINNA_PADB, PINMUX_OUTEN_RETAIN_HU_CTRL, PINMUX_INPEN_RETAIN, 3220 Pinmux_Set_FuncSel(SOC_XR16XX_PINNA_PADB, SOC_XR16XX_PINNA_PADB_MSS_UARTA_RX); 3221 3222 /* Setup the PINMUX to bring out the UART-3 */ 3223 Pinmux_Set_OVERRIDEctrl(SOC_XR16XX_PINF14_PADB, PINMUX_OUTEN_RETAIN_HU_CTRL, PINMUX_INPEN_RETAIN, 3224 Pinmux_Set_FuncSel(SOC_XR16XX_PINF14_PADB, SOC_XR16XX_PINF14_PADB_MSS_UARTB_RX); 3225 3226 //***** 3227 /* Setup the PINMUX: 3228 * - GPIO Output: Configure pin K13 as GPIO_2 output 3229 */ 3230 Pinmux_Set_OVERRIDEctrl(SOC_XR16XX_PINK13_PADB, PINMUX_OUTEN_RETAIN_HU_CTRL, PINMUX_INPEN_RETAIN, 3231 Pinmux_Set_FuncSel(SOC_XR16XX_PINK13_PADB, SOC_XR16XX_PINK13_PADB_GPIO_2); 3232 3233 //***** 3234 /* Setup the GPIO: 3235 * - GPIO Output: Configure pin K13 as GPIO_2 output 3236 */ 3237 config->SensorStatusGPIO = SOC_XR16XX_GPIO_2; 3238 3239 /* Initialize the DEMO configuration: */ 3240 config->sysClockFrequency = MSS_SYS_VCLK; 3241 config->loggingBaudRate = 921600; 3242 config->commandBaudRate = 115200; 3243 }</pre>	<pre>C:\ti\mmwave_sdk_03_03_00_02\packages\ti\demo\xwr18xx\mmw\mss_main.c 9/3/2019 6:16:41 PM 146,314 bytes C,C++,C# Source ▾ ANSI ▾ UNIX 3112 */ 3113 static void MmxDemo_platformInit(MmxDemo_platformCfg *config) 3114 { 3115 /* Setup the PINMUX to bring out the UART-3 */ 3116 Pinmux_Set_OVERRIDEctrl(SOC_XR18XX_PINNS_PADB, PINMUX_OUTEN_RETAIN_HU_CTRL, PINMUX_INPEN_RETAIN, 3117 Pinmux_Set_FuncSel(SOC_XR18XX_PINNS_PADB, SOC_XR18XX_PINNS_PADB_MSS_UARTA_RX); 3118 Pinmux_Set_OVERRIDEctrl(SOC_XR18XX_PINNA_PADB, PINMUX_OUTEN_RETAIN_HU_CTRL, PINMUX_INPEN_RETAIN, 3119 Pinmux_Set_FuncSel(SOC_XR18XX_PINNA_PADB, SOC_XR18XX_PINNA_PADB_MSS_UARTA_RX); 3120 3121 /* Setup the PINMUX to bring out the UART-3 */ 3122 Pinmux_Set_OVERRIDEctrl(SOC_XR18XX_PINF14_PADB, PINMUX_OUTEN_RETAIN_HU_CTRL, PINMUX_INPEN_RETAIN, 3123 Pinmux_Set_FuncSel(SOC_XR18XX_PINF14_PADB, SOC_XR18XX_PINF14_PADB_MSS_UARTB_RX); 3124 3125 //***** 3126 /* Setup the PINMUX: 3127 * - GPIO Output: Configure pin K13 as GPIO_2 output 3128 */ 3129 Pinmux_Set_OVERRIDEctrl(SOC_XR18XX_PINK13_PADB, PINMUX_OUTEN_RETAIN_HU_CTRL, PINMUX_INPEN_RETAIN, 3130 Pinmux_Set_FuncSel(SOC_XR18XX_PINK13_PADB, SOC_XR18XX_PINK13_PADB_GPIO_2); 3131 3132 //***** 3133 /* Setup the GPIO: 3134 * - GPIO Output: Configure pin K13 as GPIO_2 output 3135 */ 3136 config->SensorStatusGPIO = SOC_XR18XX_GPIO_2; 3137 3138 /* Initialize the DEMO configuration: */ 3139 config->sysClockFrequency = MSS_SYS_VCLK; 3140 config->loggingBaudRate = 921600; 3141 config->commandBaudRate = 115200; 3142 }</pre>
---	---

Figure 5-7. SDK 3.3 16xx vs 18xx: SoC Definition Updates

5.8 SDK 3.4 xWR68xx Calibration Frequency Update

MMWAVE-SDK 3.3 vs MMWAVE-SDK 3.4

File: mmwave_sdk_03_04_00_0x\packages\ti\demo\xwr68xx\mmw\mss_main.c

<pre>C:\ti\mmwave_sdk_03_03_00_03\packages\ti\demo\xwr68xx\mmw\mss_main.c 9/16/2019 1:01:15 PM 164,371 bytes C,C++,C# Source ▾ ANSI ▾ PC 3240 { 3241 System_printf("Error: rlrFSetLdoBypassConfig retVal=%d\n", ret 3242 return -1; 3243 } 3245 /* Open mmWave module, this is only done once */ 3246 /* Setup the calibration frequency: 3247 * TODO: Presently DFP does not support these for 68xx platform, 3248 * need to change when DFP is updated with the support */ 3249 gMmwMssMCB.cfg.openCfg.freqLimitLow = 0U; 3250 gMmwMssMCB.cfg.openCfg.freqLimitHigh = 0U; 3251 3252 /* start/stop async events */ 3253 gMmwMssMCB.cfg.openCfg.disableFrameStartAsyncEvent = false; 3254 gMmwMssMCB.cfg.openCfg.disableFrameStopAsyncEvent = false; 3255 3256 /* No custom calibration: */ 3257 gMmwMssMCB.cfg.openCfg.useCustomCalibration = false; 3258 gMmwMssMCB.cfg.openCfg.customCalibrationEnableMask = 0x0; 3259 3260 /* calibration monitoring base time unit 3261 * setting it to one frame duration as the demo doesnt support any 3262 * monitoring related functionality 3263 */ 3264 gMmwMssMCB.cfg.openCfg.calibMonTimeUnit = 1; 3265</pre>	<pre>C:\ti\mmwave_sdk_03_04_00_03\packages\ti\demo\xwr68xx\mmw\mss_main.c 3/30/2020 6:14:33 PM 167,431 bytes C,C++,C# Source ▾ ANSI ▾ PC 3310 { 3311 System_printf("Error: rlrFSetLdoBypassConfig retVal=%d\n", ret 3312 return -1; 3313 } 3315 /* Open mmWave module, this is only done once */ 3316 /* Setup the calibration frequency */ 3317 gMmwMssMCB.cfg.openCfg.freqLimitLow = 600U; 3318 gMmwMssMCB.cfg.openCfg.freqLimitHigh = 640U; 3319 3320 /* start/stop async events */ 3321 gMmwMssMCB.cfg.openCfg.disableFrameStartAsyncEvent = false; 3322 gMmwMssMCB.cfg.openCfg.disableFrameStopAsyncEvent = false; 3323 3324 /* No custom calibration: */ 3325 gMmwMssMCB.cfg.openCfg.useCustomCalibration = false; 3326 gMmwMssMCB.cfg.openCfg.customCalibrationEnableMask = 0x0; 3327 3328 /* calibration monitoring base time unit 3329 * setting it to one frame duration as the demo doesnt support any 3330 * monitoring related functionality 3331 */ 3332 gMmwMssMCB.cfg.openCfg.calibMonTimeUnit = 1; 3333</pre>
---	--

Figure 5-8. SDK 3.4 xWR68xx Calibration Frequency Update

5.9 SDK 3.4 Object Detect HWA DPC Range FFT Scaling

MMWAVE-SDK 3.3 vs MMWAVE-SDK 3.4

File: mmwave_sdk_03_04_00_0x\packages\ti\demo\xwr64xx\mmw\main.c

C:\ti\mmwave_sdk_03_03_00_03\packages\ti\demo\xwr64xx\mmw\main.c	C:\ti\mmwave_sdk_03_04_00_03\packages\ti\demo\xwr64xx\mmw\main.c
9/16/2019 1:01:15 PM 132,920 bytes C,C++,C# Source ▾ ANSI ▾ UNIX	3/30/2020 6:14:33 PM 137,509 bytes C,C++,C# Source ▾ ANSI ▾ UNIX
<pre> 1667 staticCfg->numVirtualAntAzim = RFparserOutParams.numVirtualAntAzim; 1668 staticCfg->numVirtualAntElev = RFparserOutParams.numVirtualAntElev; 1669 staticCfg->numVirtualAntennas = RFparserOutParams.numVirtualAntennas; 1670 staticCfg->rangeStep = RFparserOutParams.rangeStep; 1671 for (i = 0; i < RFparserOutParams.numRxAntennas; i++) 1672 { </pre>	<pre> 1705 staticCfg->numVirtualAntAzim = RFparserOutParams.numVirtualAntAzim; 1706 staticCfg->numVirtualAntElev = RFparserOutParams.numVirtualAntElev; 1707 staticCfg->numVirtualAntennas = RFparserOutParams.numVirtualAntennas; 1708 staticCfg->rangeStep = RFparserOutParams.rangeStep; 1709 /* 1710 * Current 64xx/68xx SOC has higher receive level as compared to 18xx and 1711 * fftOutputDivShift to avoid overflow when converting from 24-bit to 16-bit 1712 * TODO: Future RadarSS firmware should be evaluated to assess if these 1713 */ 1714 if (RFparserOutParams.numRangeBins >= 1022) 1715 { 1716 staticCfg->rangeFFTuning.fftOutputDivShift = 1; 1717 /* scale only 2 stages */ 1718 staticCfg->rangeFFTuning.numLastButterflyStagesToScale = 2; 1719 } 1720 else if (RFparserOutParams.numRangeBins == 512) 1721 { 1722 staticCfg->rangeFFTuning.fftOutputDivShift = 2; 1723 /* scale last stage */ 1724 staticCfg->rangeFFTuning.numLastButterflyStagesToScale = 1; 1725 } 1726 else 1727 { 1728 staticCfg->rangeFFTuning.fftOutputDivShift = 3; 1729 /* no scaling needed as ADC data is 16-bit and we have 8 bits to grow */ 1730 staticCfg->rangeFFTuning.numLastButterflyStagesToScale = 0; 1731 } 1732 for (i = 0; i < RFparserOutParams.numRxAntennas; i++) 1733 { </pre>

Figure 5-9. SDK 3.4 Object Detection DPC FFT Range Scaling Configuration

5.10 SDK 3.4 Object Detect Range HWA DPC Radar Cube Format

MMWAVE-SDK 3.3 vs MMWAVE-SDK 3.4

File: mmwave_sdk_03_04_00_0x\packages\ti\demo\xwr68xx\mmw\mss\mss_main.c

C:\ti\mmwave_sdk_03_03_00_03\packages\ti\demo\xwr68xx\mmw\mss\mss_main.c	C:\ti\mmwave_sdk_03_04_00_03\packages\ti\demo\xwr68xx\mmw\mss\mss_main.c
9/16/2019 1:01:15 PM 164,371 bytes C,C++,C# Source ▾ ANSI ▾ PC	3/30/2020 6:14:33 PM 167,431 bytes C,C++,C# Source ▾ ANSI ▾ PC
<pre> 2078 staticCfg->numRangeBins = RFparserOutParams.numRangeBins; 2079 staticCfg->numTxAntennas = RFparserOutParams.numTxAntennas; 2080 staticCfg->numVirtualAntennas = RFparserOutParams.numVirtualAntennas; 2081 2082 /* Fill dynamic configuration for the sub-frame */ 2083 objDetPreStartR4fCfg.dynCfg = subFrameCfg->objDetDynCfg.r4fDynCfg; </pre>	<pre> 2110 staticCfg->numRangeBins = RFparserOutParams.numRangeBins; 2111 staticCfg->numTxAntennas = RFparserOutParams.numTxAntennas; 2112 staticCfg->numVirtualAntennas = RFparserOutParams.numVirtualAntennas; 2113 2114 /* 2115 * Current 68xx SOC has higher receive level as compared to 18xx and hence using 2116 * fftOutputDivShift to avoid overflow when converting from 24-bit to 16-bit 2117 * TODO: Future RadarSS firmware should be evaluated to assess if these settings 2118 */ 2119 if (RFparserOutParams.numRangeBins >= 1022) 2120 { 2121 staticCfg->rangeFFTuning.fftOutputDivShift = 1; 2122 /* scale only 2 stages */ 2123 staticCfg->rangeFFTuning.numLastButterflyStagesToScale = 2; 2124 } 2125 else if (RFparserOutParams.numRangeBins == 512) 2126 { 2127 staticCfg->rangeFFTuning.fftOutputDivShift = 2; 2128 /* scale last stage */ 2129 staticCfg->rangeFFTuning.numLastButterflyStagesToScale = 1; 2130 } 2131 else 2132 { 2133 staticCfg->rangeFFTuning.fftOutputDivShift = 3; 2134 /* no scaling needed as ADC data is 16-bit and we have 8 bits to grow */ 2135 staticCfg->rangeFFTuning.numLastButterflyStagesToScale = 0; 2136 } 2137 2138 /* objectdetection DSP DPC needs radacube in format DPIF_RADARCUBE_FORMAT_1 */ 2139 staticCfg->radarCubeFormat = DPIF_RADARCUBE_FORMAT_1; 2140 2141 /* Fill dynamic configuration for the sub-frame */ 2142 objDetPreStartR4fCfg.dynCfg = subFrameCfg->objDetDynCfg.r4fDynCfg; </pre>

Figure 5-10. SDK 3.4 Object Detect Range HWA DPC FFT Radar Cube Format

5.11 xWR6843AoP ES1.0 Antenna Geometry

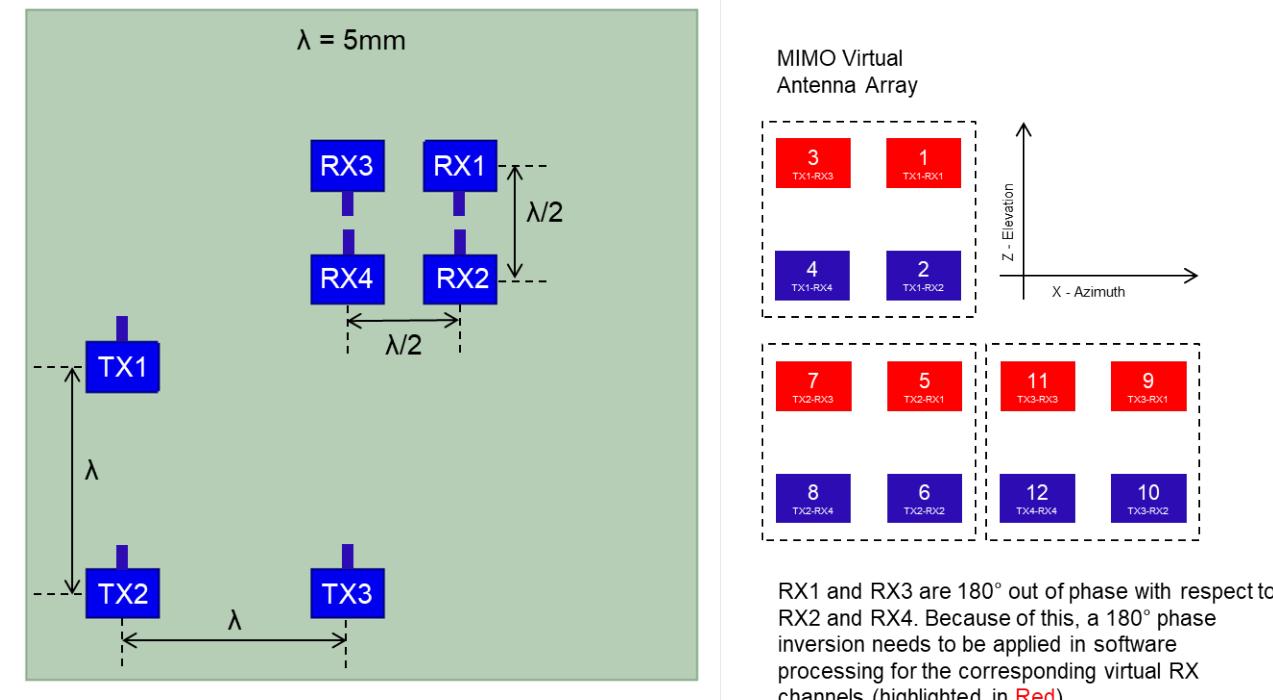


Figure 5-11. xWR6843AoP ES1.0 Antenna Geometry

5.12 xWR6843AoP ES2.0 Antenna Geometry

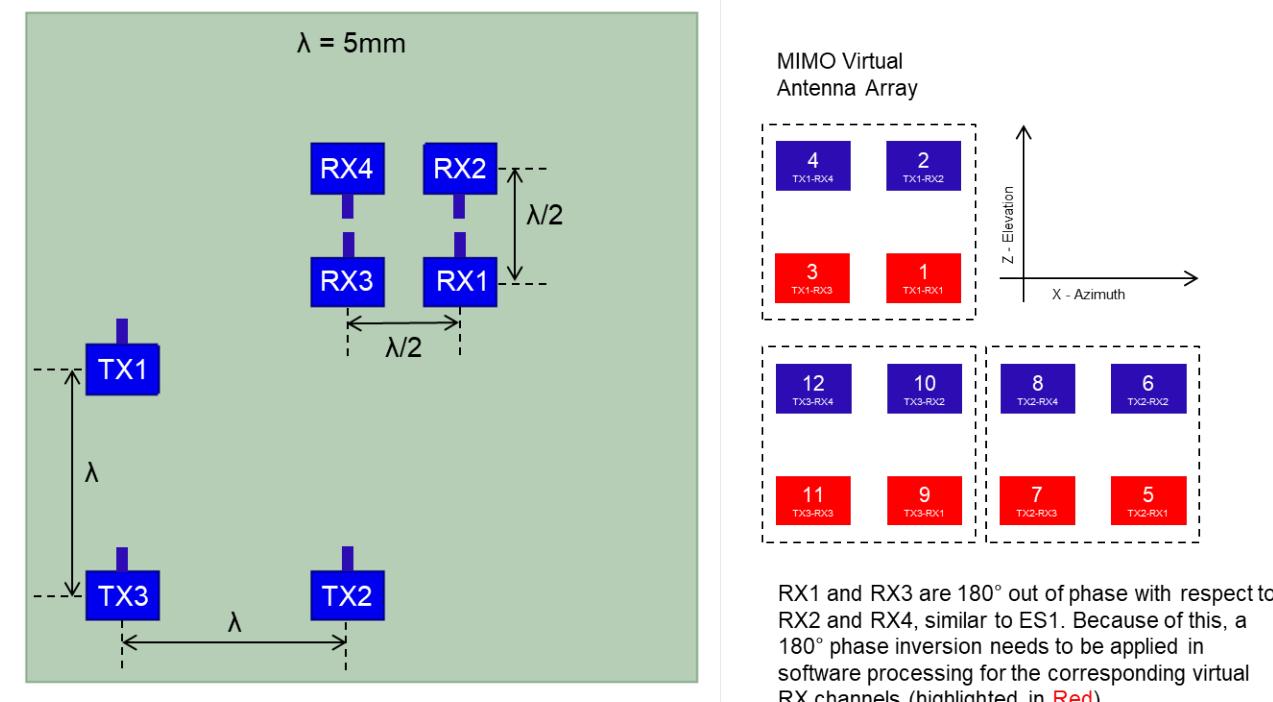
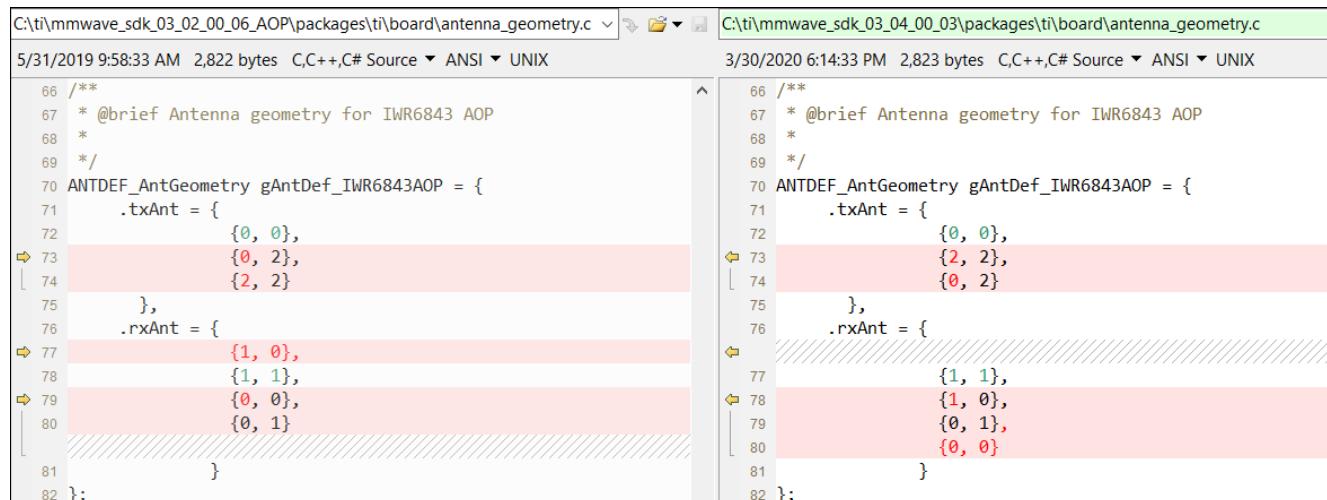


Figure 5-12. xWR6843AoP ES2.0 Antenna Geometry

5.13 xWR6843AoP ES2.0 Antenna Geometry Code Update

MMWAVE-SDK 3.2.0.6 vs MMWAVE-SDK 3.4

File: mmwave_sdk_03_04_00_0x\packages\ti\board\antenna_geometry.c



```

C:\ti\mmwave_sdk_03_02_00_06_AOP\packages\ti\board\antenna_geometry.c
5/31/2019 9:58:33 AM 2,822 bytes C,C++,C# Source ▾ ANSI ▾ UNIX
66 /**
67 * @brief Antenna geometry for IWR6843 AOP
68 *
69 */
70 ANTDEF_AntGeometry gAntDef_IWR6843AOP = {
71     .txAnt = {
72         {0, 0},
73         {0, 2},
74         {2, 2}
75     },
76     .rxAnt = {
77         {1, 0},
78         {1, 1},
79         {0, 0},
80         {0, 1}
81     }
82 };

```

```

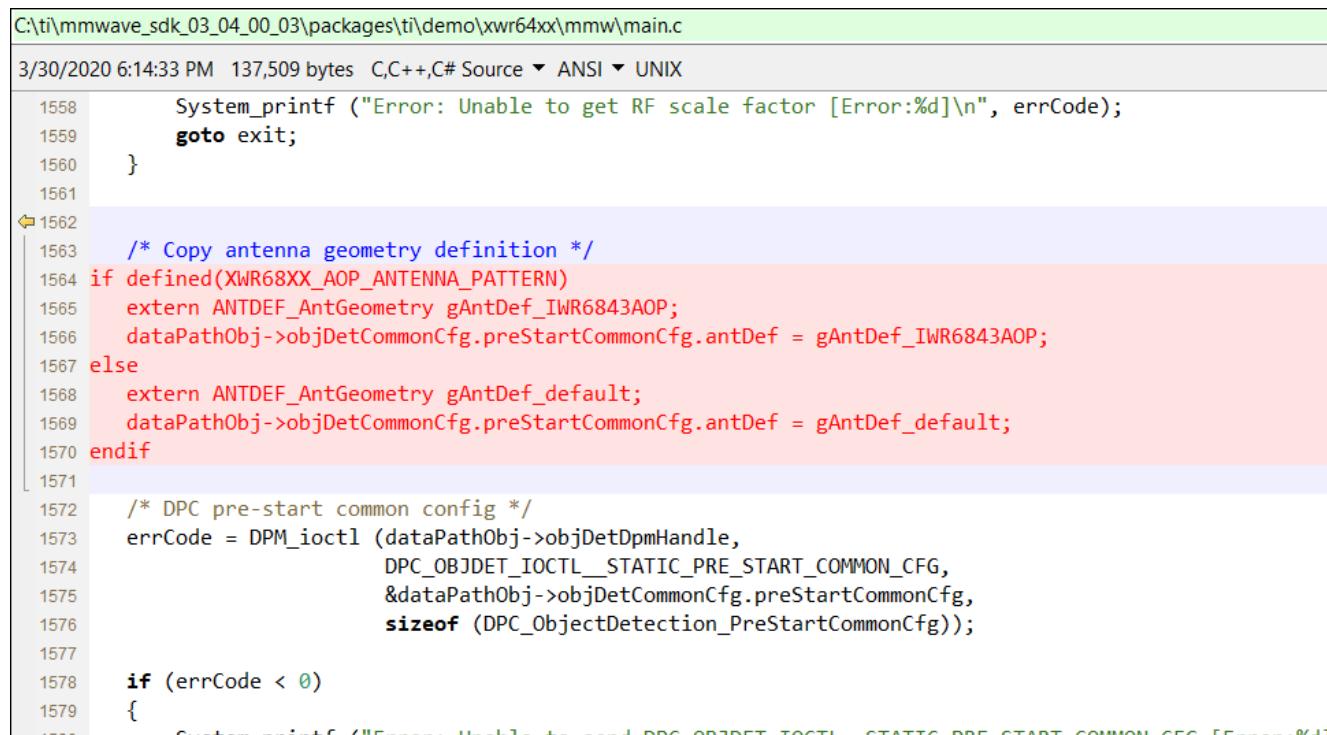
C:\ti\mmwave_sdk_03_04_00_03\packages\ti\board\antenna_geometry.c
3/30/2020 6:14:33 PM 2,823 bytes C,C++,C# Source ▾ ANSI ▾ UNIX
66 /**
67 * @brief Antenna geometry for IWR6843 AOP
68 *
69 */
70 ANTDEF_AntGeometry gAntDef_IWR6843AOP = {
71     .txAnt = {
72         {0, 0},
73         {2, 2},
74         {0, 2}
75     },
76     .rxAnt = {
77         {1, 1},
78         {1, 0},
79         {0, 1},
80         {0, 0}
81     }
82 };

```

Figure 5-13. SDK 3.2.0.6 Vs SDK 3.4: Antenna Geometry Update for xWR6843AoP ES2.0

5.14 Antenna Geometry Structure Usage in mmw demo

File: mmwave_sdk_03_04_00_0x\packages\ti\demo\xwr64xx\mmw\main.c



```

C:\ti\mmwave_sdk_03_04_00_03\packages\ti\demo\xwr64xx\mmw\main.c
3/30/2020 6:14:33 PM 137,509 bytes C,C++,C# Source ▾ ANSI ▾ UNIX
1558     System_printf ("Error: Unable to get RF scale factor [Error:%d]\n", errCode);
1559     goto exit;
1560 }
1561
1562 /* Copy antenna geometry definition */
1563 if defined(XWR68XX_AOP_ANTENNA_PATTERN)
1564     extern ANTDEF_AntGeometry gAntDef_IWR6843AOP;
1565     dataPathObj->objDetCommonCfg.preStartCommonCfg.antDef = gAntDef_IWR6843AOP;
1566 else
1567     extern ANTDEF_AntGeometry gAntDef_default;
1568     dataPathObj->objDetCommonCfg.preStartCommonCfg.antDef = gAntDef_default;
1569 endif
1570
1571 /* DPC pre-start common config */
1572 errCode = DPM_ioctl (dataPathObj->objDetDpmHandle,
1573                         DPC_OBJDET_IOCTL_STATIC_PRE_START_COMMON_CFG,
1574                         &dataPathObj->objDetCommonCfg.preStartCommonCfg,
1575                         sizeof (DPC_ObjectDetection_PreStartCommonCfg));
1576
1577
1578 if (errCode < 0)
1579 {
    System_printf ("Error: Unable to send DPC OBJECT_IOCTL_STATIC_DPC_START_COMMON_CFC [Error:%d]\n");
}

```

Figure 5-14. Antenna Geometry Structure Usage in mmw demo

5.15 xWR6843AoP ES2.0 RX Channel Phase Compensation

MMWAVE-SDK 3.2.0.6 vs MMWAVE-SDK 3.4

File: mmwave_sdk_03_04_00_03\packages\ti\demo\xwr64xx\mmw\profiles\profile_3d_aop.cfg

<pre>C:\ti\mmwave_sdk_03_02_00_06_AOP\packages\ti\demo\xwr64xx\mmw\profiles\profile_3d_aop.cfg 5/31/2019 9:58:33 AM 2,717 bytes <default> ▾ ANSI ▾ UNIX 37 flushCfg 38 dfeDataOutputMode 1 39 channelCfg 15 7 0 40 adccfg 2 1 41 adcbuffcfg -1 0 1 1 1 42 lowPower 0 0 43 profileCfg 0 60.25 7 3 24 0 0 156 1 256 12500 0 0 30 44 chirpCfg 0 0 0 0 0 0 0 1 45 chirpCfg 1 1 0 0 0 0 0 2 46 chirpCfg 2 2 0 0 0 0 0 4 47 frameCfg 0 2 32 0 100 1 0 48 guiMonitor -1 1 1 1 0 0 1 49 cfarCfg -1 0 2 8 4 3 0 15.0 0 50 cfarCfg -1 1 0 4 2 3 1 1 15.0 0 51 multiObjBeamForming -1 1 0.5 52 calibDcRangeSig -1 0 -5 8 256 53 clutterRemoval -1 0 54 55 compRangeBiasAndRxChanPhase 0.0 -1 0 1 0 -1 0 1 0 -1 0 1 0 -1 0 1 0 -1 0 1 0 -1 0 1 0 56 measureRangeBiasAndRxChanPhase 0 1. 0.2 57 58 aoaFovCfg -1 -90 90 -90 90 59 cfarFovCfg -1 0 0.25 15 60 cfarFovCfg -1 1 -13.39 13.39 61 extendedMaxVelocity -1 0 62 63 CQRxSatMonitor 0 3 4 63 0 64 CQSigImgMonitor 0 127 4 65 analogMonitor 0 0 66 lvdsStreamCfg -1 0 0 0 67 sensorStart</pre>	<pre>C:\ti\mmwave_sdk_03_04_00_03\packages\ti\demo\xwr64xx\mmw\profiles\profile_3d_aop.cfg 3/30/2020 6:14:33 PM 2,717 bytes <default> ▾ ANSI ▾ UNIX 37 flushCfg 38 dfeDataOutputMode 1 39 channelCfg 15 7 0 40 adccfg 2 1 41 adcbuffcfg -1 0 1 1 1 42 lowPower 0 0 43 profileCfg 0 60.25 7 3 24 0 0 156 1 256 12500 0 0 30 44 chirpCfg 0 0 0 0 0 0 0 1 45 chirpCfg 1 1 0 0 0 0 0 2 46 chirpCfg 2 2 0 0 0 0 0 4 47 frameCfg 0 2 32 0 100 1 0 48 guiMonitor -1 1 1 1 0 0 1 49 cfarCfg -1 0 2 8 4 3 0 15.0 0 50 cfarCfg -1 1 0 4 2 3 1 1 15.0 0 51 multiObjBeamForming -1 1 0.5 52 calibDcRangeSig -1 0 -5 8 256 53 clutterRemoval -1 0 54 55 compRangeBiasAndRxChanPhase 0.0 1 0 -1 0 1 0 -1 0 1 0 -1 0 1 0 -1 0 1 0 -1 0 1 0 -1 0 1 0 -1 0 1 0 56 measureRangeBiasAndRxChanPhase 0 1. 0.2 57 58 aoaFovCfg -1 -90 90 -90 90 59 cfarFovCfg -1 0 0.25 15 60 cfarFovCfg -1 1 -13.39 13.39 61 extendedMaxVelocity -1 0 62 63 CQRxSatMonitor 0 3 4 63 0 64 CQSigImgMonitor 0 127 4 65 analogMonitor 0 0 66 lvdsStreamCfg -1 0 0 0 67 sensorStart</pre>
---	--

Figure 5-15. SDK 3.2.0.6 Vs SDK 3.4: RX Channel Phase Compensation

6 References

- Texas Instruments: [IWR1642 Device Errata](#)
- Texas Instruments: [AWR1642 Device Errata](#)
- Texas Instruments: [IWR1843 Device Errata](#)
- Texas Instruments: [AWR1843 Device Errata](#)
- Texas Instruments: [IWR6843 Device Errata](#)
- Texas Instruments: [AWR6843 Device Errata](#)
- [xWR1642BOOST Layout and Design Files](#)
- [xWR6843AOPEVM Schematic, Assembly and Bill of Materials](#)
- Texas Instruments: [xWR1642 EVM \(xWR1642BOOST\) Single-Chip mmWave Sensing Solution User's Guide](#)
- Texas Instruments: [MMWAVEICBOOST and Antenna Module User's Guide](#)
- [xWR6843 Checklist for Schematic Review, Layout Review, Bringup/Wakeup](#)
- [xWR1843BOOST Hardware Files](#)
- Texas Instruments: [xWR1843 Evaluation Module \(xWR1843BOOST\) Single-Chip mmWave Sensing Solution User's Guide](#)
- [xWR6843 Product Page](#) (Device data sheet, Silicon Errata)
- [xWR6843AoP Product Page](#) (Device data sheet, Silicon Errata)
- [xWR1843 Product Page](#) (Device data sheet, Silicon Errata)
- [xWR1642 Product Page](#) (Device data sheet, Silicon Errata)
- Texas Instruments: [IWR14xx/16xx/18xx/68xx Industrial Radar Family Technical Reference Manual](#)
- [MMWAVE-SDK Product Page](#)
- [MMWAVE-SDK 3.3.0 download page](#) (Release notes, User guide and SDK download)
- [MMWAVE-SDK 3.4.0 download page](#) (Release notes, User guide and SDK download)

7 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision B (May 2022) to Revision C (October 2022)	Page
• Updated the numbering format for tables, figures, and cross-references throughout the document.....	3
<hr/>	
Changes from Revision * (November 2019) to Revision A (May 2020)	Page
• Updates were made in Device Comparison topic.....	4
• Added new Section 2.1.2	5
• Updates were made in Section 2.1.2.1	5
• Added new Section 2.1.3	6
• Updates were made in Hardware Changes From xWR6843AoP ES1.0 to xWR6843AoP ES2.0 topic.....	9
• Update was made in Software Migration From xWR6843AoP ES1.0 to xWR6843AoP ES2.0 topic.....	11

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to [TI's Terms of Sale](#) or other applicable terms available either on [ti.com](#) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2022, Texas Instruments Incorporated