

## CC11xx Settings for FCC 15.247 Solutions

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### Keywords

- *FCC 15.247 and 15.249*
- *Wideband Requirements*
- *CC11xx System Parameters*
- *AN001 SRD Regulations*
- *Digital Modulation*
- *CC1100*
- *CC1101*
- *CC1110*
- *CC1111*
- *CC1150*
- *CC430*

### 1 Introduction

FCC CRF Section 15.249 restricts the fundamental radiated power to 50 mV/m at 3 meters distance (approximately -1 dBm EIRP) in the US 902-928 MHz ISM band.

FCC CRF Section 15.247 specifies requirements that allow for up to 1 W transmit output power. High output power can be used if the system employs Frequency Hopping Spread Spectrum (FHSS) or uses a digital modulation technique that gives a 6 dB bandwidth of minimum 500 kHz [1][1].

This design note shows how CC11xx can be configured for output powers above -1 dBm and meet FCC Section 15.247 requirements without the need for frequency hopping.

The CC1101EM 868-915 MHz reference design [2] is used as an example in this design note, but the results are also applicable for CC1100, CC1110, CC1111, CC430, or CC1150.

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**2 Abbreviations**

CRF	Code of Federal Regulations
EM	Evaluation Module
FCC	Federal Communications Commission
FHSS	Frequency hopping Spread Spectrum
IF	Intermediate Frequency
ISM	Industrial, Scientific, Medical
PSD	Power Spectral Density
RBW	Resolution Bandwidth
VBW	Video Bandwidth

## 3 FCC 15.247 Measurements

Table 1 gives the FCC 15.247 digital modulation requirements

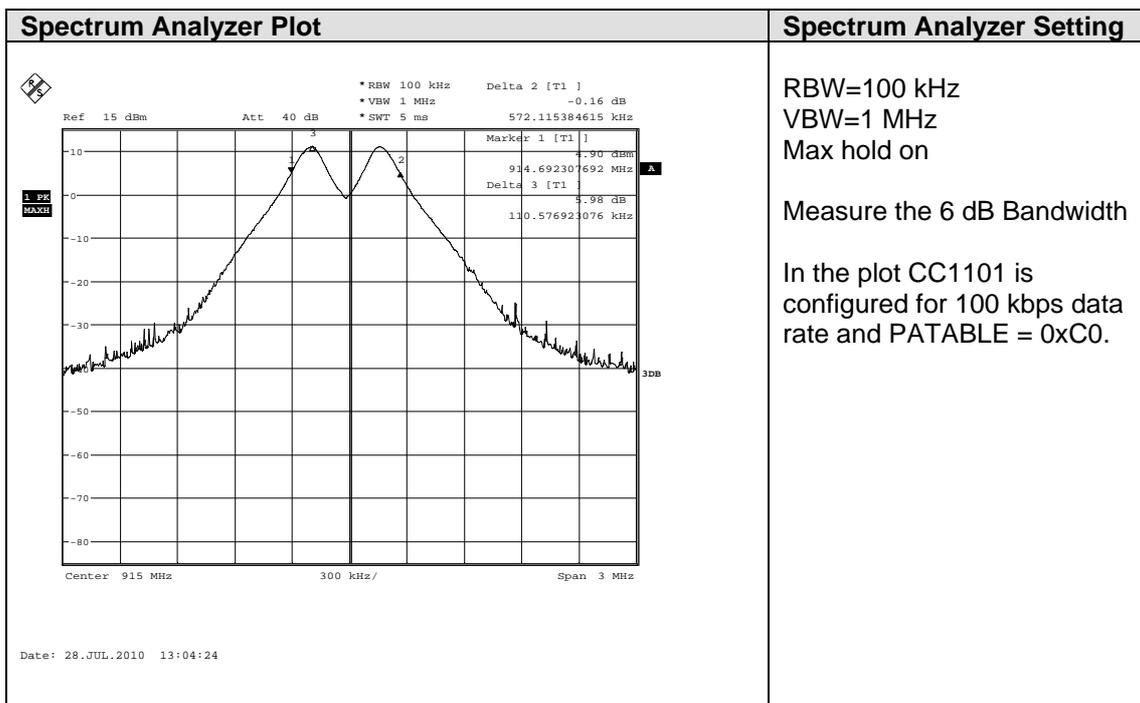
Section	Requirement
15.247a2	The 6 dB bandwidth shall be at least 500 kHz
15.247b3	The maximum conducted power shall not exceed 1 W (+30 dBm)
15.247e	The power spectral density (PSD) shall not be greater than 8 dBm in any 3 kHz band during any time interval during continuous transmission

**Table 1. FCC 15.247 Digital Modulation Requirements**

In this design note the PSD sets an upper limit on the maximum output power.

### 3.1 FCC 15.247a2 Bandwidth Measurement

Most spectrum analyzers have a measurement option that automatically measures a fixed dB bandwidth. If this is not available the 6 dB bandwidth must be measured manually by setting up markers. The spectrum analyzer should be configured as shown in Figure 1.



**Figure 1. Test Setup for FCC 15.247a2**

## 3.2 FCC 15.247b Power Measurement

The maximum allowed output power is 1 W, but when a wideband digital modulation technique is used the Power Spectral Density (PSD) described in Section 3.3 will limit the output power to less than 1 W.

Output power is measured by setting the RBW higher than the measured 6 dB bandwidth. The spectrum analyzer should be configured as described in Figure 2.

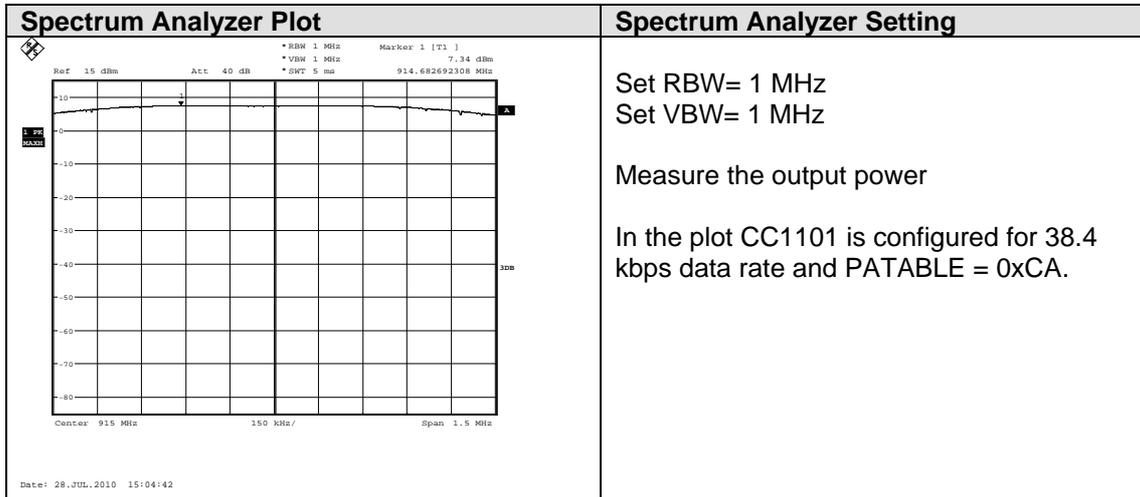


Figure 2. Test Setup for FCC 15.247b

## 3.3 FCC 15.247d Power Spectral Density Measurement

The peak power spectral density at any 3 kHz bandwidth shall not exceed +8 dBm. This should be measured over at least 3 times the modulation bandwidth. The spectrum analyser should be configured as given in Figure 3.

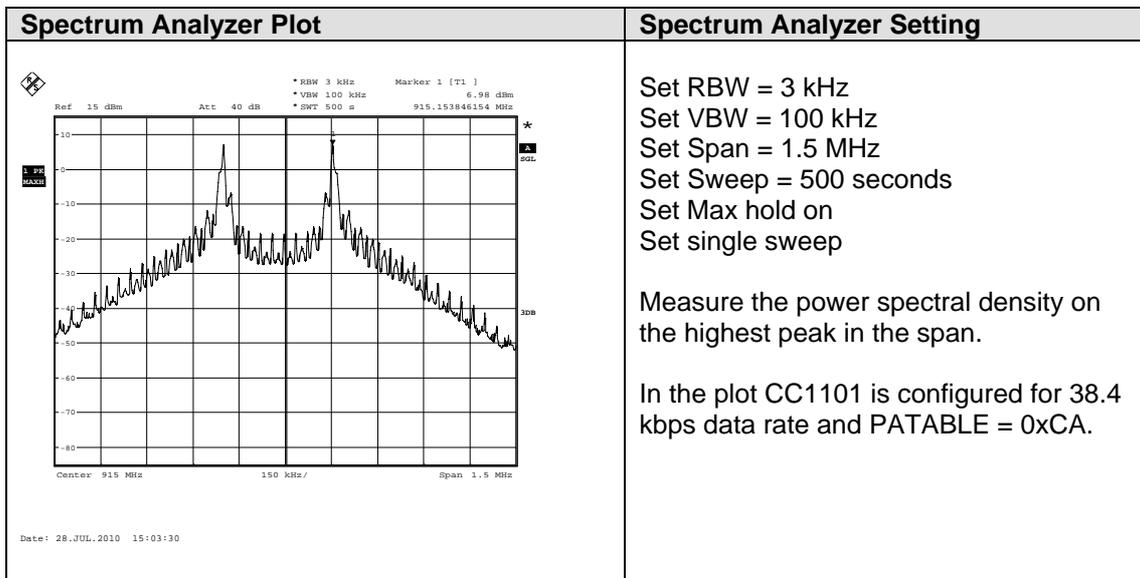


Figure 3. Test Setup for FCC 15.247d

Note that the sweep time is slow in order to ensure that the max peak is captured. A faster test approach with more or less same results could be to reduce the sweep time to 300 us and use max hold instead. The official test however, requires a sweep time of 500 seconds.

## 4 Measurement Setup

- 5 devices were tested
- All measurements were performed at 3V, 25°C
- Measurements were performed on the CC1101EM 868-915 MHz reference design [2]
- An automated test setup was used and LabView was used to control the measuring equipment
- The results presented in Section 5 are the average numbers of the 5 devices tested

## 5 CC1101 Test Results for FCC 15.247

The SmartRF® Studio Software tool from Texas Instruments includes a list of preferred settings for a wide range of data rates. However to comply with the FCC 15.247 minimum 500 kHz 6 dB bandwidth requirement, the frequency deviation needs to be wider compared to the preferred settings. When increasing the frequency deviation the RX filter bandwidth needs to be increased. The IF frequency (register FSCTRL1) and registers FREQ1, TEST2, TEST1, AGCCTRL2, AGCCTRL1 as well as AGCCTRL0 must be set depending on the RX filter bandwidth for optimum performance. For data rates at and below 250 kbps, the 250 kbps preferred settings were used as a starting point and the data rate and frequency deviation changed for the different test cases.

Data Rate	Modulation	Deviation	PATABLE	6 dB BW (15.247 a2)	Power (15.247 b)	PSD (15.247 d)
500 kbps	MSK	Phase 0	C0	650 kHz	11.4 dBm	0.00 dBm
250 kbps	2-FSK	165 kHz	C0	647 kHz	11.4 dBm	3.10 dBm
100 kbps	2-FSK	177 kHz	C0	577 kHz	11.4 dBm	6.68 dBm
76.8 kbps	2-FSK	177 kHz	C2	547 kHz	10.5 dBm	7.27 dBm
38.4 kbps	2-FSK	177 kHz	C9	523 kHz	8.1 dBm	7.70 dBm
10 kbps	2-FSK	177 kHz	CA	516 kHz	7.8 dBm	7.74 dBm
4.8 kbps	2-FSK	177 kHz	CA	505 kHz	7.8 dBm	7.74 dBm
2.4 kbps	2-FSK	177 kHz	CB	513 kHz	7.5 dBm	7.47 dBm

**Table 2. CC1101 TX Test Results for FCC 15.247**

Data Rate	Modulation	Deviation	RX Filter	Sensitivity
500 kbps	MSK	Phase 0	812 kHz	-89 dBm
250 kbps	2-FSK	165 kHz	650 kHz	-95 dBm
100 kbps	2-FSK	177 kHz	540 kHz	-96 dBm
76.8 kbps	2-FSK	177 kHz	540 kHz	-98 dBm
38.4 kbps	2-FSK	177 kHz	540 kHz	-100 dBm
10 kbps	2-FSK	177 kHz	540 kHz	-104 dBm
4.8 kbps	2-FSK	177 kHz	540 kHz	-107 dBm
2.4 kbps	2-FSK	177 kHz	540 kHz	-108 dBm

**Table 3. CC1101 RX Test Results for FCC 15.247**

Data Rate	Modulation	Deviation	RX Filter	Sensitivity
500 kbps	MSK	Phase 0	812 kHz	-89 dBm
250 kbps	2-FSK	127 kHz	540 kHz	-96 dBm
100 kbps	2-FSK	47 kHz	325 kHz	-100 dBm
76.8 kbps	2-FSK	32 kHz	232 kHz	-101 dBm
38.4 kbps	2-FSK	20 kHz	100 kHz	-104 dBm
10 kbps	2-FSK	19 kHz	100 kHz	-105 dBm
4.8 kbps	2-FSK	25.4 kHz	100 kHz	-110 dBm
2.4 kbps	2-FSK	5.2 kHz	58 kHz	-109 dbm

**Table 4. CC1101 RX Test Results Preferred Settings**

## 6 Conclusion

This design note has shown that CC11xx is suited for systems targeting compliance with digital modulation systems defined under FCC Section 15.247. Comparing Table 5 and Table 6 we see that there is a significant improvement in link budget, and hence range, when operating under FCC Section 15.247 compared to FCC Section 15.249. As an example, at 500 kbps the difference in link budget is 12 dB, which corresponds to a *theoretical* improvement in range of a factor 4. Note also from Table 2 that for data rates above 100 kbps it is possible to increase the output power further by using an external power amplifier and still meet PSD requirements. As an example, for 500 kbps it is possible to transmit at +18 dBm, using an external power amplifier, and still comply with FCC Section 15.247.

Data Rate	Sensitivity	Output power	Link Budget
500 kbps	-89 dBm	11.4 dBm	100 dB
250 kbps	-95 dBm	11.4 dBm	106 dB
100 kbps	-96 dBm	11.4 dBm	107 dB
76.8 kbps	-98 dBm	10.5 dBm	108.5 dB
38.4 kbps	-100 dBm	8.1 dBm	108 dB
10 kbps	-104 dBm	7.8 dBm	112 dB
4.8 kbps	-107 dBm	7.8 dBm	115 dB
2.4 kbps	-108 dBm	7.5 dBm	115.5 dB

**Table 5. CC1101 FCC 15.247 Settings Link Budget**

Data Rate	Sensitivity	Output power	Link Budget
500 kbps	-89 dBm	-1 dBm	88 dB
250 kbps	-96 dBm	-1 dBm	95 dB
100 kbps	-100 dBm	-1 dBm	99 dB
76.8 kbps	-101 dBm	-1 dBm	100 dB
38.4 kbps	-104 dBm	-1 dBm	103 dB
10 kbps	-105 dBm	-1 dBm	104 dB
4.8 kbps	-110 dBm	-1 dBm	109 dB
2.4 kbps	-109 dbm	-1 dBm	108 db

**Table 6. CC1101 Preferred Settings Link Budget**

## 7 References

- [1] SRD regulations for license free transceiver operation (swra090.pdf)
- [2] CC1101EM 868-915MHz Reference Design (swrr045.zip)

## 8 General Information

### 8.1 Document History

Revision	Date	Description/Changes
SWRA123B	2010.08.17	Significant update to the design note. <ul style="list-style-type: none"><li>- Measurements performed on CC1101</li><li>- Changes to frequency deviation to meet 6 dB BW requirement</li><li>- Updated PSD and output power measurement results</li><li>- Sensitivity figures significantly improved. Non-optimum register settings used in previous revision</li><li>- Added CC430 to keywords</li></ul>
SWRA123A	2007.12.17	Added CC1100, CC1101, CC1110, CC1111, CC1150 to keywords Removed logo from header
SWRA123	2007.01.04	Initial release.

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