

How Ethernet Technology Is Shifting Modern Markets 40 Years after Its Inception



Garrett Yamasaki



In my last blog post, “[Three things you should know about Ethernet PHYs](#),” I discussed the evolution of Ethernet and the most essential information about the Ethernet physical layer transceiver (PHY). In this post, I’ll pick up where I left off and discuss how Ethernet PHYs are shifting modern markets.

Ethernet was invented over 40 years ago. Fast-forward through the standardization of IEEE 802.3 (10Mbps Ethernet PHYs) and into the world of 1995 – the era of “fast Ethernet,” where nominal data rates could reach up to 100Mbps. As part of this technological boom, National Semiconductor invented the industry’s first 10/100Mbps Ethernet PHY, a speed that’s still used predominantly even in today’s modern markets. What was even more impressive was the fact that National included a feature called auto-negotiation, which allowed the Ethernet PHY to operate at 10Mbps or 100Mbps without the need for human intervention. Both of these facets were considered a massive reinvention for Ethernet technology. As a result, new markets began emerging for the Ethernet PHY.



Figure 1. Ethernet PHYs Are Integrated into Applications Such as Automotive Diagnostics, Robotic Assembly Lines and Set-top Boxes

In today’s markets, Ethernet technology is drastically changing the dynamics of how we efficiently transfer data. [Ethernet PHYs](#) are integrated into everything from automotive diagnostics to robotic assembly lines to set-top boxes, as shown in Figure 1. Ethernet PHYs are evolving to meet market demand due to three main factors:

- Real-time communication
- Robustness and reliability against external factors
- Economical solution
- Access edge nodes without having to translate data between different protocols

Real-time Communication

Today's technological applications require electronics to provide a more real-time response. An Ethernet PHY architected and designed for such applications can provide improved link times and low latency. You can find an example of why low deterministic latency is important in a robotics assembly line. Robots need to be timed extremely precisely in order to optimize assembly time and reduce defects. If the Ethernet PHY did not have low deterministic latency, the processor would have to increase the delay between each action the robot takes. Multiplied hundreds or thousands of times, such delays would drastically increase assembly time and reduce throughput.

Robustness and Reliability against External Factors

Industrial and automotive equipment manufacturers have adopted the IEEE 802.3 Ethernet standard because of its functionality. To better address these markets, some suppliers have developed Ethernet PHYs that exceed the IEEE specification with extended temperature ranges, rigorous electrostatic discharge (ESD) testing, the ability to exceed electromagnetic interference (EMI) and electromagnetic compatibility (EMC) specifications, and excellent signal integrity. Ethernet PHYs are also undergoing stringent performance tests to become automotive qualified (AEC-Q100). Applications such as industrial data concentrators, industrial protective relays, and automotive gateways for on-board diagnostics and firmware upgrades all require robust testing.

Economical Solution

Since Ethernet is a widely adopted and known standard, it is a cost-effective way to reliably transfer data quickly through a dedicated medium. This, along with Ethernet's ease of use, has led to commercial products offering Ethernet connections such as set-top boxes, network printers and smart TVs.

In the meantime, where do you see Ethernet technology going in the next 10 years? Share your vision in the comments below.

Additional Resources

- Ease development with TI's [Gigabit PHY evaluation module](#).
- Check out TI's [Ethernet portfolio](#).
- Find out more about the [DP83825I](#) 10/100Mbps Ethernet PHY and automotive-qualified [DP83TC811R-Q1](#) Ethernet PHY.
- Check out the [EN55011-Compliant, Industrial Temperature, 10/100Mbps Ethernet PHY Brick TI Design](#) reference design.

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to [TI's Terms of Sale](#) or other applicable terms available either on [ti.com](https://www.ti.com) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2023, Texas Instruments Incorporated