

RF Sampling: How Over-sampling Is Cheating Physics



RJ Hopper

RF sampling converters can capture high-frequency signals and large-bandwidth signals; however, not every application utilizes signals that require very high-speed sampling. For cases where the bandwidth or the output frequency is not excessive, there is still an advantage to utilizing the high sampling rate capabilities of [RF sampling converters](#).

The sampling theorem states that the sampling rate must be at least twice the largest bandwidth of the signal. Sampling below this rate is called under-sampling and causes aliasing; the benefits of this approach were discussed in [my previous blog](#). Sampling above this rate is called over-sampling. Over-sampling offers some processing advantages that seemingly let you defy physics.

One of the key measurement parameters for [analog-to-digital converters](#) (ADCs) is signal-to-noise ratio (SNR). SNR measures the relative level between the desired signal power and the entirety of the noise power within the first Nyquist zone. The Nyquist zone bandwidth is the sampling rate divided by two ($F_s/2$). Recall that all signals and noise will fold back into the first Nyquist zone. This zone effectively represents the entire bandwidth of the device.

One benefit of over-sampling is that the image components are separated farther in frequency space. This allows easier analog filtering to eliminate interfering signals that can alias down into the captured bandwidth and desensitize the receiver. [Figure 1](#) illustrates two cases: one signal sampled near the Nyquist rate and one that is over-sampled. The over-sampled case provides a more realizable analog anti-aliasing filter.

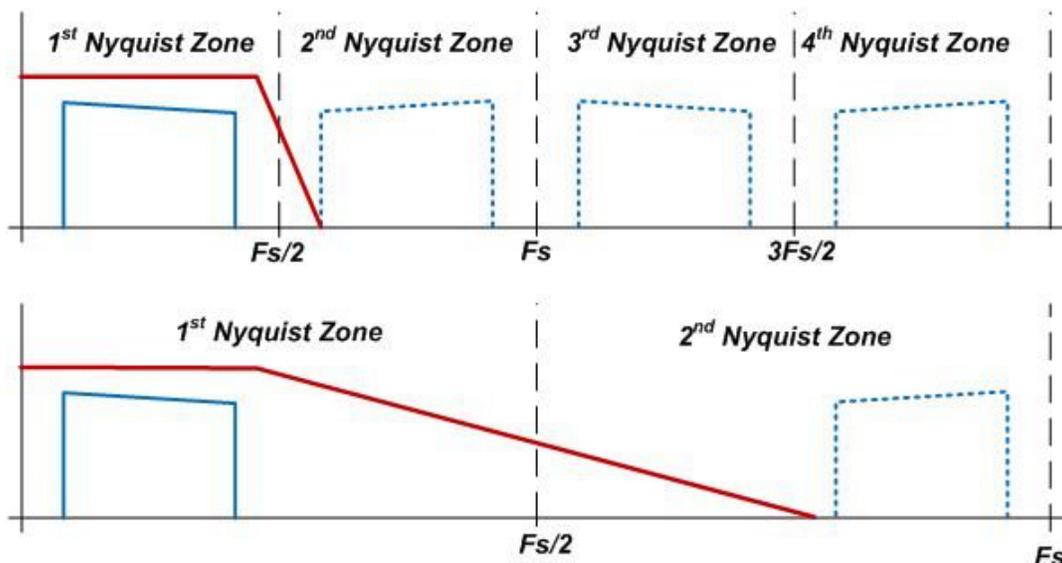


Figure 1. Filter Impact for Nyquist Rate Sampling vs. Over-sampling

Over-sampling can improve the SNR performance of the device beyond the theoretical quantization noise limitations. The quantization noise is equally distributed across the Nyquist bandwidth. By increasing the sampling rate, the same quantization noise is spread over a larger Nyquist bandwidth. The desired signal remains fixed. Decimation coupled with digital filtering decrease the noise bandwidth without impacting the desired signal. Note, decimation implies over-sampling since there must be additional samples available to remove. In RF sampling ADCs, it is more common to refer to a decimation factor rather than an over-sampling rate; however, these parameters are effectively equivalent.

For example, decimating by two must have the signal over-sampled by at least two. In this example, the signal power remains the same but the Nyquist bandwidth is cut in half. This eliminates half of the noise power, which improves the ADC SNR by 3 dB. The first equation represents the ideal SNR due to quantization noise where N is the number of bits of the converter. The second equation represents the SNR improvement related to the decimation factor D .

$$SNR_Q = 6.02N + 1.76 \quad (\text{Eq. 1})$$

$$SNR_{DEC} = SNR_Q + 10 \log(D) \quad (\text{Eq. 2})$$

From a pure quantization noise analysis, each fourfold increase in sampling rate equates to one effective bit of resolution improvement. In theory, a 12-bit data converter can achieve the SNR performance of a 14-bit converter by sampling at 16 times the minimum Nyquist rate. In practice, RF sampling data converters do not achieve SNR performance equivalent to the quantization noise limit due to other impairments related to aperture jitter, clock jitter and thermal noise; however, the over-sampling technique still provides nearly the same relative SNR improvement. In many communication systems, this benefit is critical. For example, the [ADS54J60](#) is a 16-bit, 1-GSPS ADC that has options for decimation by two or four. The designer can make the decision to increase the sampling speed and introduce decimation in order to improve the SNR performance.

Check back next month when I will discuss the details of the digital mixers in RF sampling data converters.

Additional Resources

- Check out the [ADC12J4000](#) and [ADS54J60](#) high-speed ADCs.
- Learn more about TI's [RF sampling ADCs](#).
- Watch my video on the [benefits and advantages RF sampling](#).
- Learn about TI's [data converter](#) portfolio and find technical resources.

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to [TI's Terms of Sale](#) or other applicable terms available either on [ti.com](https://www.ti.com) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2023, Texas Instruments Incorporated