

# How to Modify a Step-down Converter to the Inverting Buck-boost Topology



Chris Glaser

When looking for a DC/DC converter to create a negative voltage, in many cases you will use a step-down converter in the inverting buck-boost topology. While dedicated inverter devices such as the very-low-noise [TPS63710](#) are easier to design with and generally a better solution, there are numerous reasons to use a step-down converter as an inverting buck-boost converter instead. First, there are relatively few dedicated voltage inverter devices in the market, compared to the ubiquitous step-down converter. You may not find a specific feature or characteristic which is required in your application. In other cases, it greatly simplifies the procurement effort to use another instance of an existing step-down converter for another socket in the design through utilizing it in the inverting buck-boost topology. Since there are generally very few inverting buck-boost circuits available for lab testing, you will need to modify the readily-available step-down converter EVM into the inverting buck-boost topology to measure the circuit for your design. This blog walks you through the steps required to take the standard [TPS82130 evaluation module \(EVM\)](#), which is configured as a step-down converter, and create an inverting power supply based on [3- to 11.5-VIN, -5-VOUT, 1.5-A Inverting Power Module reference design](#).

Using a step-down converter as an inverting buck-boost converter is a valid application use case, supported by numerous reference designs and applications notes. The [TPS82130](#) step-down power module is used as the example, because of its high integration level and simple design. It also contains two inverting buck-boost reference designs, [TIDA-01457](#) and [TIDA-01405](#), with full test data and documentation. Please see the [reference design guide](#) section 2.3 for a detailed technical discussion of using step-down converters as inverting buck-boost converters.

To begin, [Figure 1](#) shows the standard EVM as it would be connected as a step-down converter.

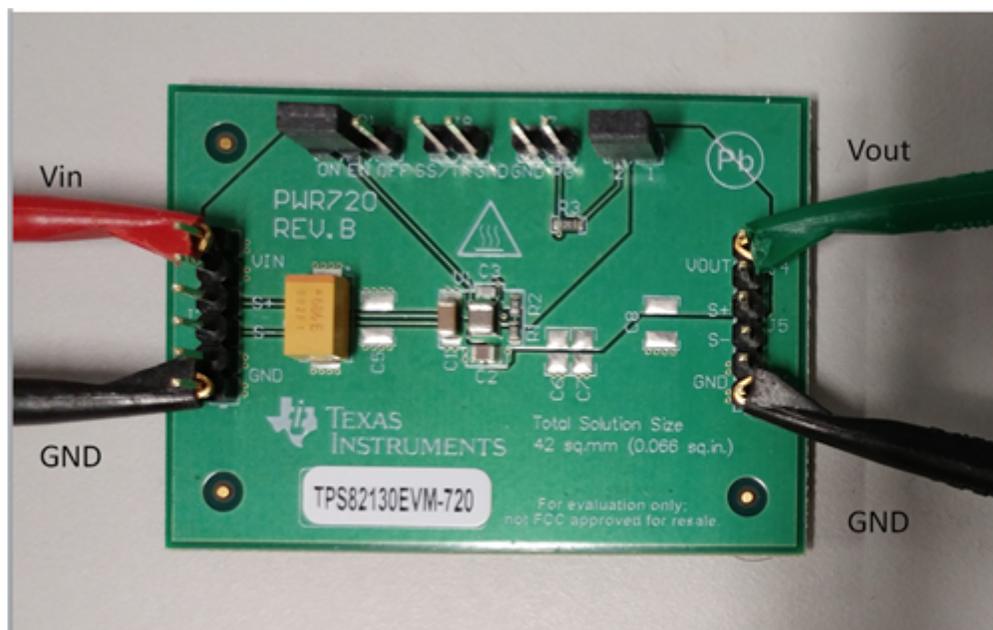
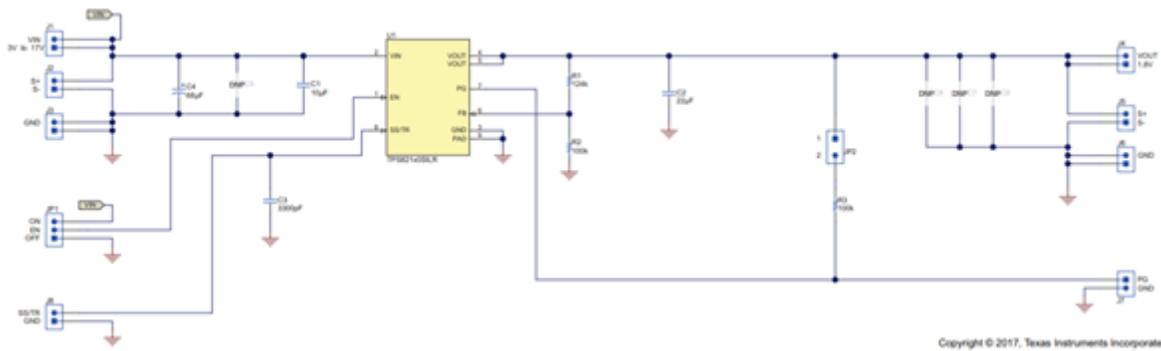
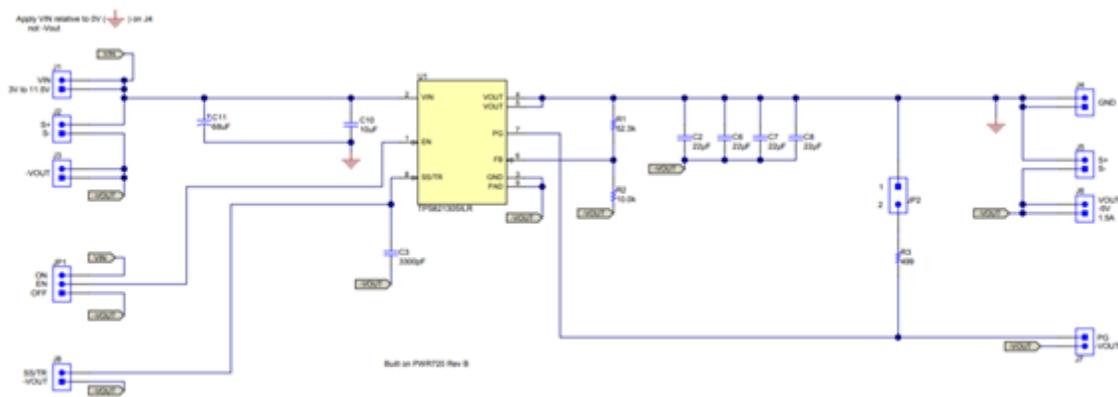


Figure 1. TPS82130EVM-720 Connected as a Step-down Converter

To achieve the TIDA-01457 design, its [inverting schematic](#) in [Figure 3](#) is compared to the normal step-down EVM's schematic in [Figure 2](#).



**Figure 2. Step-down Converter (TPS82130EVM-720) Schematic**



**Figure 3. Inverting Buck-boost (TIDA-01457) Schematic**

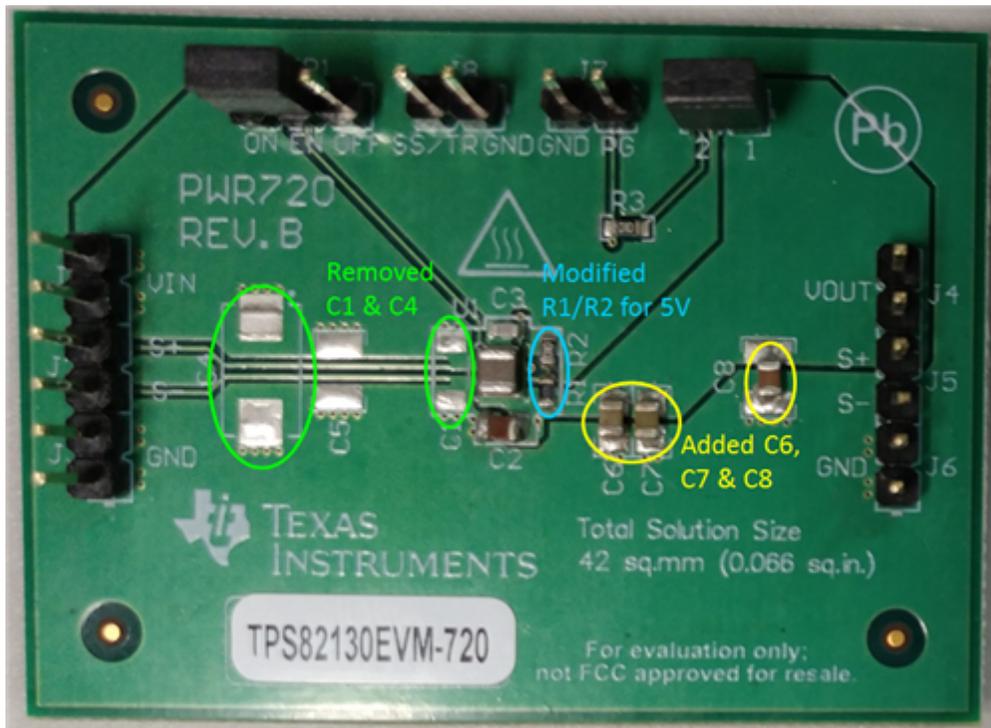
To achieve the inverting function, the output terminals are swapped such that  $V_{OUT}$  (J4) becomes GND and the GND terminal (J6) becomes  $-V_{OUT}$ . Everything that was GND is now labeled  $-V_{OUT}$ . In addition, the input voltage is applied from  $V_{IN}$  to  $V_{OUT}$ , which is now GND.

Further comparing the two schematics, we find the remaining changes required to transform the EVM into the inverting circuit. Note that the reference designators are identical between the designs, except for the input capacitors C1 and C4 whose connections need to change from what the existing printed circuit board (PCB) provides.

Here are the required changes, which are shown in the following images:

- Remove C1 and C4, but save them to re-install later
- Install a 22- $\mu$ F ceramic output capacitor at C6, C7, and C8
- Change the value of R1 and R2 to set the appropriate output voltage
- Install the input capacitors, C10 and C11, which were saved from earlier

First, the input capacitors are removed, the additional output capacitors installed, and the two feedback (FB) resistors changed. [Figure 4](#) shows the resulting EVM:



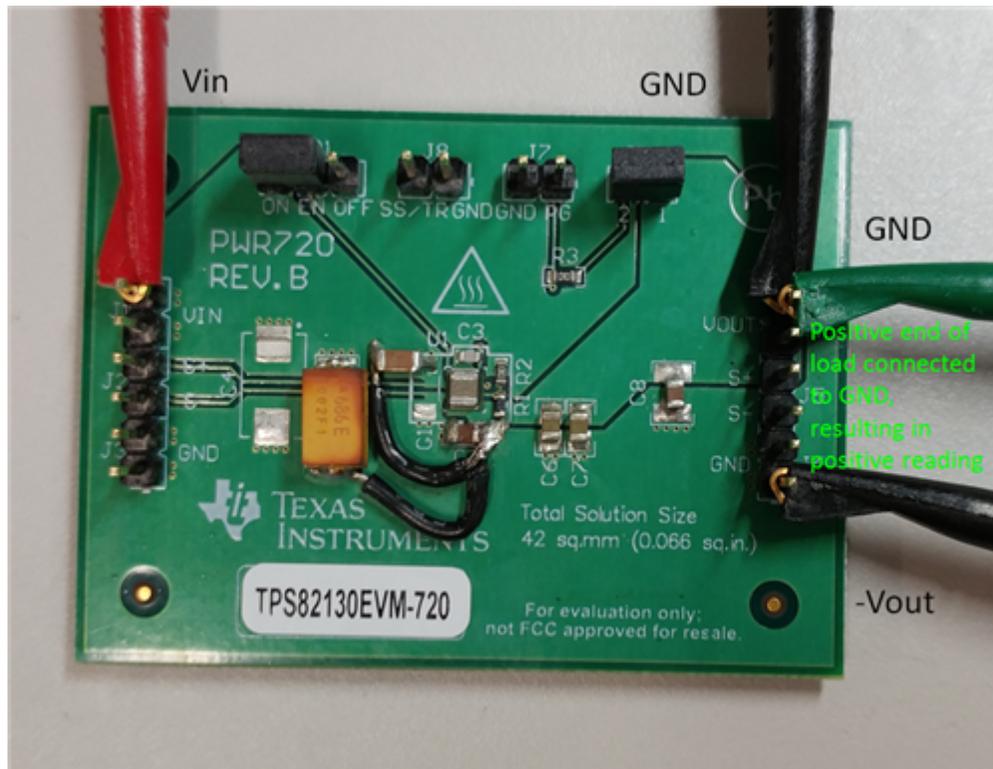
**Figure 4. TPS82130EVM-720 Partially Modified to an Inverting Circuit**

Next, the input capacitors, C10 and C11, are installed. There are no pads at the correct electrical locations on the existing EVM, so the existing pads for the  $V_{IN}$  connection are used and a wire is added to complete the connection to GND, which is  $V_{OUT}$  on the PCB. Figure 5 shows the result, which has completed all modifications and is now an inverting buck-boost power supply.



**Figure 5. TPS82130EVM-720 Completely Modified to an Inverting Circuit**

Figure 6 shows this same EVM wired in the correct way and ready to be powered on. Note that the positive terminal of the load connects to GND (J4) and the negative terminal to GND (J6). This presents the load with a positive voltage, instead of a negative one.



**Figure 6. TIDA-01457 Connected as an Inverting Buck-boost Converter**

These same steps and procedures are applicable to most step-down converter EVMs and allow you to quickly evaluate them as inverting buck-boost converters, without having to make a PCB of your own.

#### Additional Resources

- Browse TI's [inverting charge pump solutions](#).
- Browse TI's [inverting buck-boost solutions](#).

## IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to [TI's Terms of Sale](#) or other applicable terms available either on [ti.com](https://www.ti.com) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265  
Copyright © 2023, Texas Instruments Incorporated