# LM65460EVM Evaluation Module

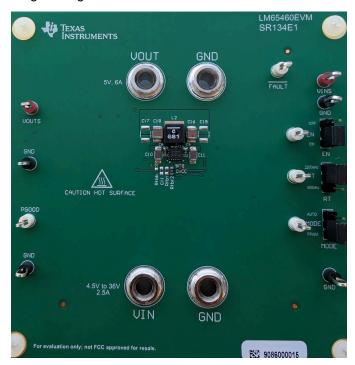


### **Description**

The Texas Instruments LM65460EVM evaluation module (EVM) helps designers evaluate the operation and performance of the LM654x0-Q1 family of wide input voltage buck converters. The LM654x0-Q1 family are easy to use, synchronous, step-down converters capable of supplying up to 4A, 6A, or 8A of load current from an input voltage as high as 36V.

### **Features**

- 3V to 36V wide input voltage range
- 5V, 3.3V, and adjustable output voltage options
- Up to 6A output current
- 300kHz to 2.2MHz switching frequency
- Minimized switch node ringing to reduce Electromagnetic Interference (EMI)



LM65460EVM



### 1 Evaluation Module Overview

### 1.1 Introduction

The LM65460EVM is configured to deliver a 5V output to a load requiring 6A or less. The LM65460EVM can be used in many different configurations by substituting other versions of the LM654x0-Q1 and re-configuring the board components. See Section 1.4 for more details.

### 1.2 Kit Contents

This kit includes one LM65460EVM.

### 1.3 Specification

Performance characteristics for the LM65460EVM are found in Table 1-1.

Unless otherwise stated: V<sub>IN</sub> = 12V, V<sub>OUT</sub> = 5V, T<sub>A</sub> = 25°C.

Table 1-1. LM65460EVM Electrical Performance Characteristics

Parameter	Test Cond	Test Conditions			MAX	Units
INPUT CHARACTERISTICS	,				•	
Input voltage range, V <sub>VIN</sub>	EVM input voltage operating	6	12	36	V	
Input current, no load, I <sub>IN(NL)</sub>	I <sub>OUT</sub> = 0A	auto mode	8.7			μA
Input current, disabled, I <sub>IN(OFF)</sub>	V <sub>EN/UVLO</sub> = 0V, no EN divider	V <sub>IN</sub> = 12V	1.0			μA
OUTPUT CHARACTERISTICS	,				'	
Output voltage, V <sub>O</sub>	I <sub>OUT</sub> = 0A, auto mode	I <sub>OUT</sub> = 0A, auto mode				V
	I <sub>OUT</sub> = 6A	I <sub>OUT</sub> = 6A				V
Output voltage regulation, ΔV <sub>OUT</sub>	Load regulation, auto mode	I <sub>OUT</sub> = 0A to 6A	18			mV
Output voltage regulation, ΔV <sub>OUT</sub>	Load regulation, FPWM mode	I <sub>OUT</sub> = 0A to 6A	9			
Output voltage regulation, ΔV <sub>OUT</sub>	Line regulation, V <sub>IN</sub> = 6V to 36V	I <sub>OUT</sub> = 6A		4		
Maximum output current	V <sub>IN</sub> = 12V			8.3		Α
SYSTEM CHARACTERISTICS					'	
Switching frequency	I <sub>OUT1</sub> = 6A		2200		kHz	
Peak efficiency	I <sub>OUT</sub> = 3.4A	V <sub>IN</sub> = 12V	94.2%			
Full load efficiency	I <sub>OUT</sub> = 6A	V <sub>IN</sub> = 12V		93.3%		

### 1.4 Device Information

The default EVM incorporates the LM65460-Q1. Table 1-2 provides a list of additional devices that can be used with the LM65460EVM. Appropriate passive component changes must be made to use another device in the EVM.

Table 1-2. LM65460EVM Device Options

Device OPN	Output Current	Q-Grade?
LM65460SRZTRQ1	6A	Y

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### 2 Hardware

### 2.1 Additional Images

Figure 2-1 and Figure 2-2 show the front and back of the LM65460EVM respectively.

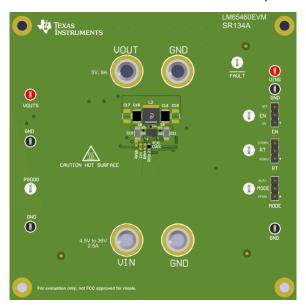


Figure 2-1. LM65460EVM Top Side

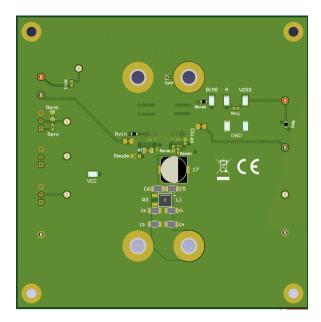


Figure 2-2. LM65460EVM Bottom Side

## 2.2 Power Requirements

Any power source in the range of 6V to 36V, and capable of delivering 3A, can be used to evaluate the LM65460EVM, under normal conditions.

### 2.3 Setup and Operation

This section describes the connectors, test points, and jumpers on the EVM and how to properly connect, set up, and use the LM65460EVM See Figure 2-3 for location of connectors and jumpers and typical setup.

**VOUT** Output voltage of the converter

VOUT banana post. Apply load to this connector.

**VOUTS** The VOUTS test point is used to monitor output voltage.

GND (Test Test point next to VOUTS test point

**Point)** This GND test point is used as the negative DMM connection for VOUT sensing.

VIN Input voltage to the converter

VIN banana post. Apply input voltage to this connector.

**VINS** The VINS test point is used to monitor input voltage.

GND (Test Test point next to VINS test point

**Point)** This GND test point is used as the negative DMM connection for VIN sensing.

**GND** Ground of the converter

GND banana posts. Apply load ground and input voltage ground to these connectors.

**EN** The use of the EN jumper is self-explanatory.

To supply an external signal to the EN input of the device, remove the EN jumper shunt and

apply the signal to the EN test point.

To use the external UVLO feature, populate Rent and Renb as desired and remove the EN jumper shunt. Note that for accurate shutdown current measurement, these resistors must be

removed (if used) and the EN jumper shunt moved to "OFF".

**RT** The RT jumper is used to select the switching frequency and is self explanatory.

The default inductor on the EVM is designed for 2200kHz operation. Other frequencies

require a different value of inductance.

To adjust the switching frequency, remove the RT jumper shunt and populate RT with the

desired value. See the LM654x0-Q1 data sheet for frequency vs. RT resistor value.

**MODE** MODE jumper is used to select the operating mode of the device. With MODE in the AUTO

position, the device operates in automatic PFM/FPWM mode depending on load current. With the MODE in the FPWM position, the device operates at fixed frequency at all load currents. The MODE pin is also the frequency synchronization input. To synchronize the device to an external clock, remove the MODE jumper shunt and apply the clock to the MODE test point.

The EVM is set for a fixed 5V output, with Rfbt2,  $0\Omega$ , populated. To set the output voltage to

**Connections** 3.3V, remove Rfbt2 and populate Rfbb with a  $0\Omega$  resistor.

To use the adjustable output voltage mode, populate Rfbt and Rfbb with the appropriate value

resistors. Rinj must also be populated with a  $10\Omega$  to  $50\Omega$  resistor. The reference voltage is 0.8V. See the LM654x0-Q1 data sheet for appropriate values of feed-back resistors. When using the adjustable output voltage mode, a Bode plot can be taken using the Rinj

resistor. This resistor becomes the injection point for the frequency response analyzer, allowing the loop frequency response to be taken in the usual way. In the fixed output voltage

mode, a loop response can not be taken.

**PGOOD** The PGOOD test point is used to monitor the power-good indicator. This flag indicates

whether the output voltage has reached the regulation level. PGOOD is an open-drain output

that is tied to VOUT through a  $100k\Omega$ , resistor Rpg.

VCC vCC test point

Feed-back

The VCC pin is the output of the internal LDO. The LDO voltage is typically 3.3V. This point

can be used for logic input and/or logic pull-ups. Do not connect to external loads.

BIAS Auxiliary input to LDO regulator

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Connected to VOUT through Rbias on EVM. To change the input supply of the LDO, remove Rbias and connect external input to BIAS pin, or ground BIAS pin as required. Populate Cbias with a  $0.1\mu F$  capacitor when using external supply to BIAS pin. See LM654x0-Q1 data sheet for more information.

**Loop Response** When using the adjustable output voltage mode, a Bode plot can be taken using the **Plot** connection shown in Figure 2-4. Rinj must be populated for this test.

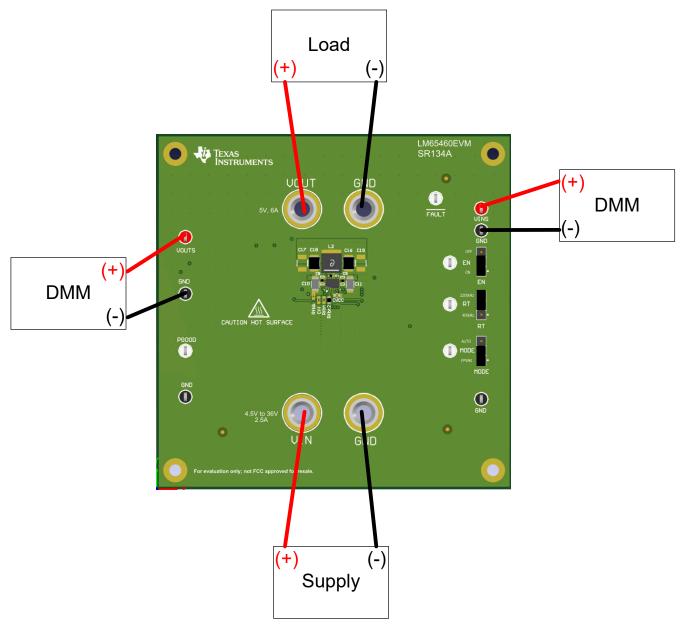


Figure 2-3. LM65460EVM Setup



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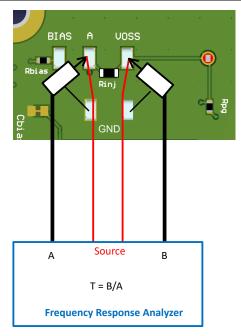


Figure 2-4. LM65460EVM Loop Response Connections

6



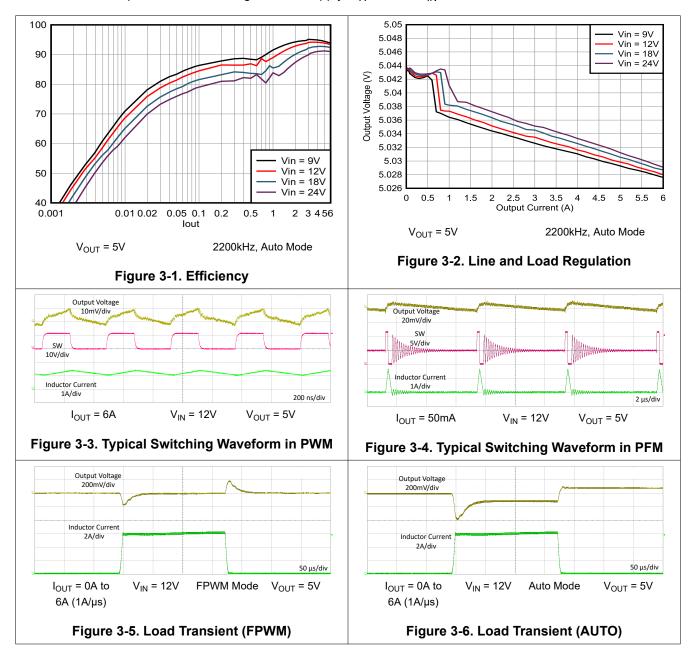
## 3 Implementation Results

### 3.1 Evaluation Setup

The LM65460EVM was used to take the following data with the setup shown in Figure 2-3.

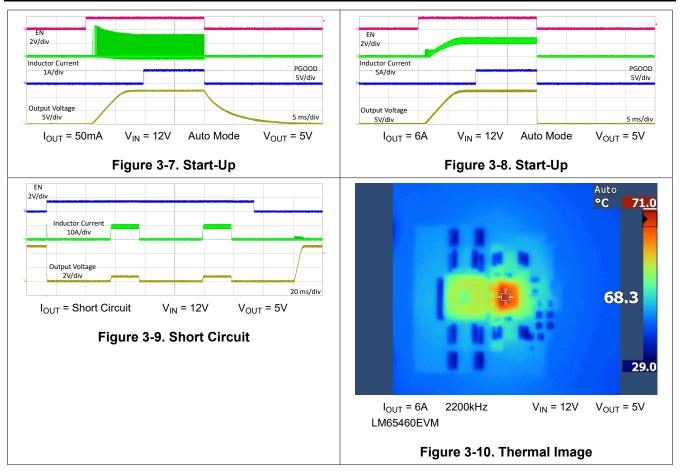
### 3.2 Performance Data and Results

Unless otherwise specified the following condition apply:  $T_A = 25$ °C,  $V_{IN} = 12$ V, 2200kHz.





Implementation Results www.ti.com





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## 4 Hardware Design Files

## 4.1 Schematics

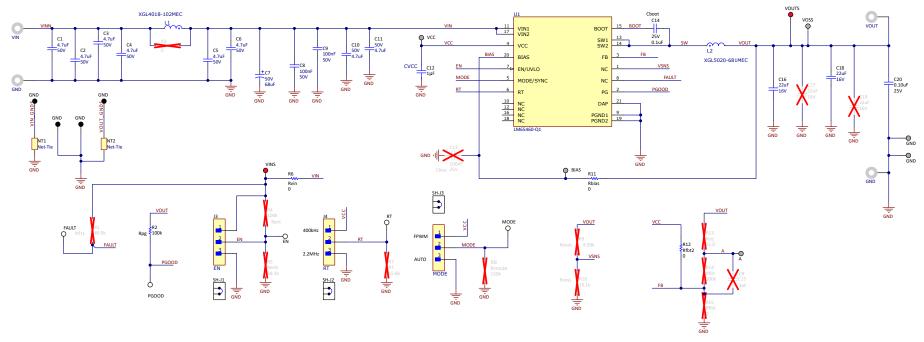


Figure 4-1. LM65460EVM Schematic



## **4.2 PCB Layouts**

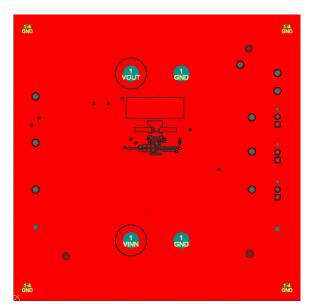


Figure 4-2. PCB Top Layer

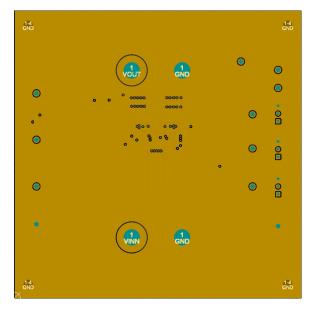


Figure 4-3. PCB Ground Layer (Directly Below Top Layer)

10



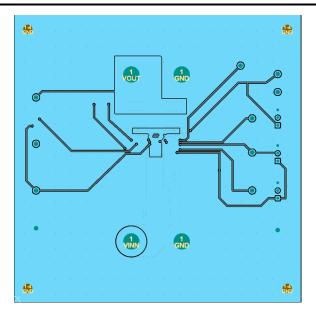


Figure 4-4. PCB Signal Layer

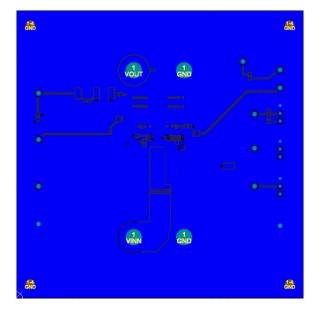


Figure 4-5. PCB Bottom Layer



## 4.3 Bill of Materials (BOM)

## Table 4-1. LM65460EVM BOM

Docienctes	Alias	Quantitu	Value	Description	Port Number
Designator	Allas	Quantity	value	Description	Part Number
C1, C2, C3, C4, C5, C6, C10, C11		8	4.7uF	CAP, CERM, 4.7uF, 50V, +/- 10%, X7R, 1206	GRM31CR71H475KA12L
C7		1	68uF	CAP, AL, 68uF, 50V, +/- 20%, 0.34ohm, SMD	UUD1H680MNL1GS
C8, C9		2	0.1uF	CAP, CERM, 0.1uF, 50V, +/- 10%, X7R, AEC-Q200 Grade 1, 0402	GCM155R71H104KE02D
C12	vcc	1	1µF	Cap Ceramic 1uF 16V X7R 20% Pad SMD 0603 +125°C Automotive T/R	CGA3E1X7R1C105M080AC
C14		1	0.1uF	CAP, CERM, 0.1uF, 25V, +/- 20%, X7R, 0402	C1005X7R1E104M050BB
C16, C18		2	22uF	CAP, CERM, 22uF, 16V, +/- 10%, X7R, 1210	GCM32ER71C226KE19L
C20		1	100nF	Chip Multilayer Ceramic Capacitors for General Purpose, 0402, 0.10uF, X7R, 15%, 10%, 25V	GRM155R71E104KE14J
H1, H2, H3, H4		4		Standoff, Hex, 0.5"L #4-40 Nylon	1902C
H5, H6, H7, H8		4		Screw, Pan Head , 4-40, 3/8", Nylon	NY PMS 440 0038 PH
J1, J2, J6, J7		4		Standard Banana Jack, Uninsulated, 8.9mm	575-8
J3, J4, J5		3			HTSW-103-07-F-S
L1		1	1uH	1μH Shielded Molded Inductor 7.3A 12.9mOhm Max 1616 (4040 Metric)	XGL4018-102MEC
L2		1	0.68uH	680nH Shielded Molded Inductor 12.9A 6.1mOhm Max Nonstandard	XGL5020-681MEC
R2	Rpg	1	100k	RES, 100 k, 1%, 0.1 W, 0603	RC0603FR-07100KL
R6, R11, R12	Rvin, Rbias, Rfbt2	3	0	RES, 0, 1%, 0.1 W, AEC-Q200 Grade 0, 0603	RMCF0603ZT0R00
SH-J1, SH-J2, SH- J3		3	1x2	Shunt, 100mil, Gold plated, Black	SNT-100-BK-G
TP1, TP2, TP4, TP5		4		Test Point, Multipurpose, Black, TH	5011
TP3, TP6, TP8, TP9, TP11		5		Test Point, Multipurpose, White, TH	5012
TP7, TP14		2		Test Point, Multipurpose, Red, TH	5010
TP10, TP12, TP13, TP15, TP16, TP17		6		Test Lead clips and hooks, SMT	S1751-46
U1		1		6A Synchronous Step-Down Voltage Regulator	LM65460SRZTRQ1
C13	Cbias	0	0.1uF	CAP, CERM, 0.1uF, 25V, +/- 10%, X7R, 0603	06033C104KAT2A
C15	Cff	0	3pF	CAP, CERM, 3pF, 50V, +/- 8.3%, C0G/ NP0, 0603	C0603C309C5GACTU
C17, C19		0	22uF	CAP, CERM, 22uF, 16V, +/- 10%, X7R, 1210	GCM32ER71C226KE19L
FID1, FID2, FID3		0		Fiducial mark. There is nothing to buy or mount.	N/A
R1	Rflt1	0	49.9k	RES, 49.9 k, 1%, 0.1 W, 0603	RC0603FR-0749K9L
R3		0	0	RES, 0, 1%, 0.5 W, 1206	5108
R4, R14	Rent, Rfbt	0	100k	RES, 100 k, 1%, 0.1 W, 0603	RC0603FR-07100KL
R5	Renb	0	38.3k	RES, 38.3 k, 1%, 0.1 W, 0603	RC0603FR-0738K3L
R7	RT	0	15.8k	RES, 15.8 k, 1%, 0.1 W, 0603	RC0603FR-0715K8L



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## Table 4-1. LM65460EVM BOM (continued)

Designator	Alias	Quantity	Value	Description	Part Number
R8	Rmode	0	150k	Res Thin Film 0805 150K Ohm 0.1% 1/8W ±25ppm/°C Molded SMD SMD Punched Carrier T/R	ERA-6AEB154V
R9	Rsnsh	0	4.99k	RES, 4.99 k, 1%, 0.1 W, 0603	CRCW06034K99FKEA
R10	RsnsL	0	19.1k	RES, 19.1 k, 1%, 0.1 W, 0603	RC0603FR-0719K1L
R13	Rinj	0	51	RES, 51.0, 1%, 0.1 W, 0603	RC0603FR-0751RL
R15	Rfbb	0	0	RES, 0, 1%, 0.1 W, AEC-Q200 Grade 0, 0603	RMCF0603ZT0R00



## **5 Additional Information**

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  - 2.3 Tl's sole liability shall be at its option to repair or replace EVMs that fail to conform to the warranty set forth above, or credit User's account for such EVM. Tl's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by Tl and that are determined by Tl not to conform to such warranty. If Tl elects to repair or replace such EVM, Tl shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

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NOTE:

EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGREDATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.

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#### 3.1 United States

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**FCC NOTICE:** This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

#### CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

#### FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

#### FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- · Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

#### 3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

#### **Concerning EVMs Including Radio Transmitters:**

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

### Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

### **Concerning EVMs Including Detachable Antennas:**

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types lated in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

#### Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

#### 3.3 Japan

- 3.3.1 Notice for EVMs delivered in Japan: Please see http://www.tij.co.jp/lsds/ti\_ja/general/eStore/notice\_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。
  - https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-delivered-in-japan.html
- 3.3.2 Notice for Users of EVMs Considered "Radio Frequency Products" in Japan: EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

- 1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
- 2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
- 3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above. User will be subject to penalties of Radio Law of Japan.

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- 3.3.3 Notice for EVMs for Power Line Communication: Please see http://www.tij.co.jp/lsds/ti\_ja/general/eStore/notice\_02.page 電力線搬送波通信についての開発キットをお使いになる際の注意事項については、次のところをご覧ください。https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-for-power-line-communication.html
- 3.4 European Union
  - 3.4.1 For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive):

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

- 4 EVM Use Restrictions and Warnings:
  - 4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.
  - 4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.
  - 4.3 Safety-Related Warnings and Restrictions:
    - 4.3.1 User shall operate the EVM within TI's recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.
    - 4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.
  - 4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User's handling and use of the EVM and, if applicable, User assumes all responsibility and liability for compliance in all respects with such laws and regulations. User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.
- 5. Accuracy of Information: To the extent TI provides information on the availability and function of EVMs, TI attempts to be as accurate as possible. However, TI does not warrant the accuracy of EVM descriptions, EVM availability or other information on its websites as accurate, complete, reliable, current, or error-free.

#### 6. Disclaimers:

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