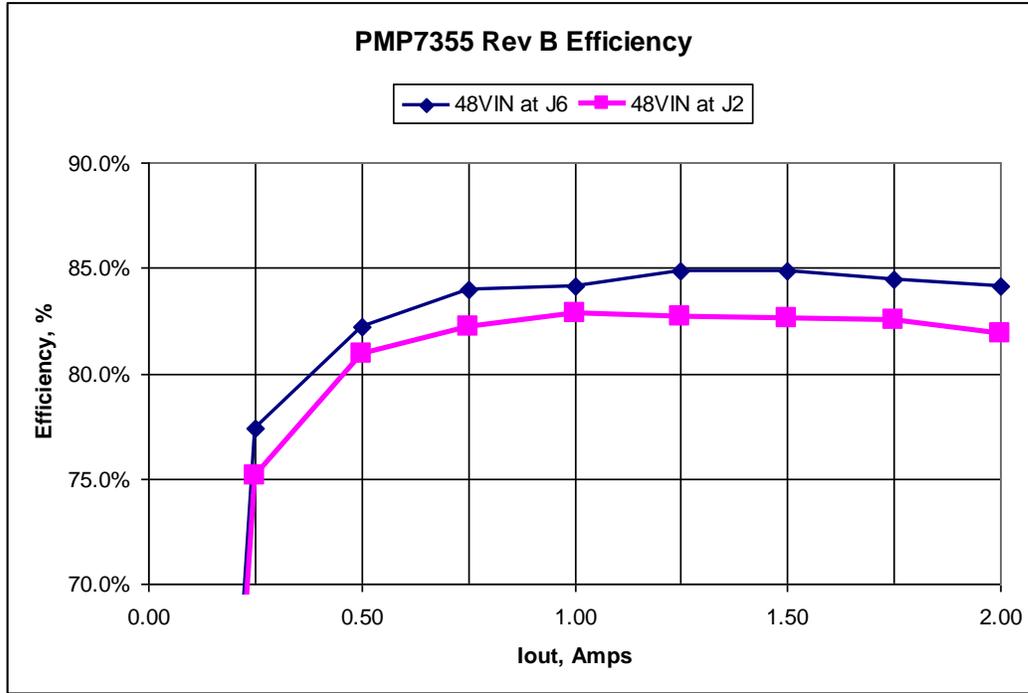


**Efficiency**

The efficiency of the converter is shown below:

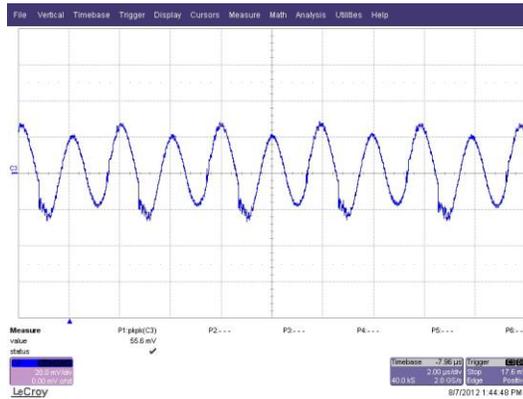


**Ripple and Noise**

Ripple measurements taken with 48VIN at J2, 2A load, and 20MHz BWL.

Output Ripple (C28), 20mV/div, 2us/div:  
 Measured 21.2mV pk-pk

Input Ripple (C24), 20mV/div, 2us/div:  
 Measured 55.6mV pk-pk



### Dynamic Loading

Load Step, 48VIN at J2 100mV/div, 1msec/div  
1A to 2A Load Step, 5mA/usec  
Measured 256mV peak to peak:



### Turn On Response

48VIN, 2A Load, 500usec/div:

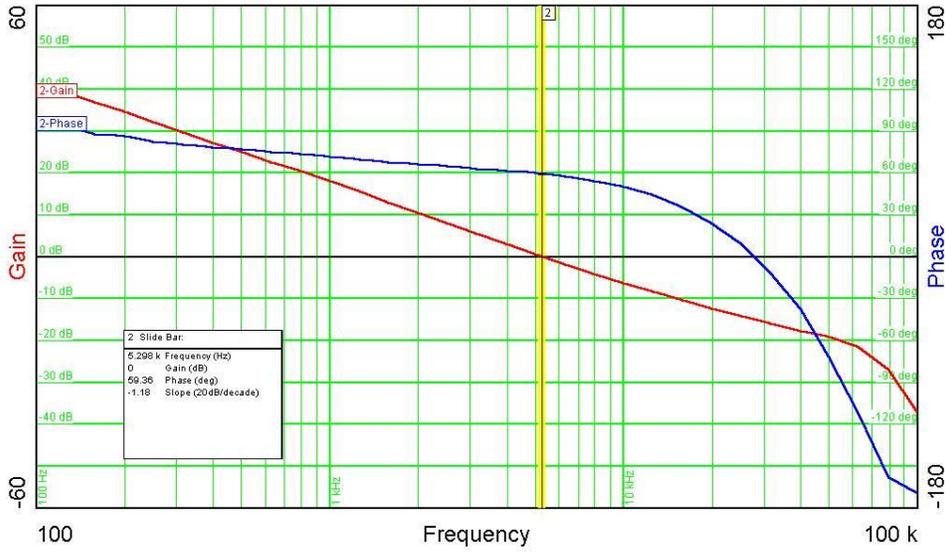


48VIN, 0A Load, 500usec/div:

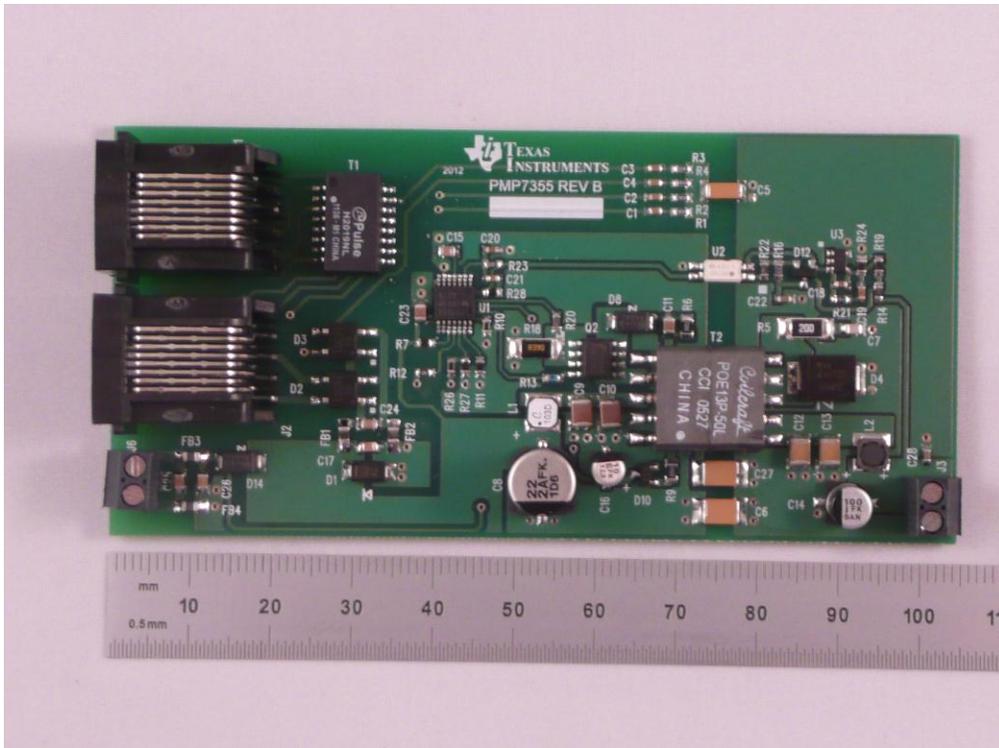


**Stability (Loop Gain)**

The figure below is the loop gain of the converter with a 48V input and 2A load. The Bandwidth is 5.3 KHz, the Phase Margin is 59 degrees, and the Gain Margin is 15 dB.



**Photo:**



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