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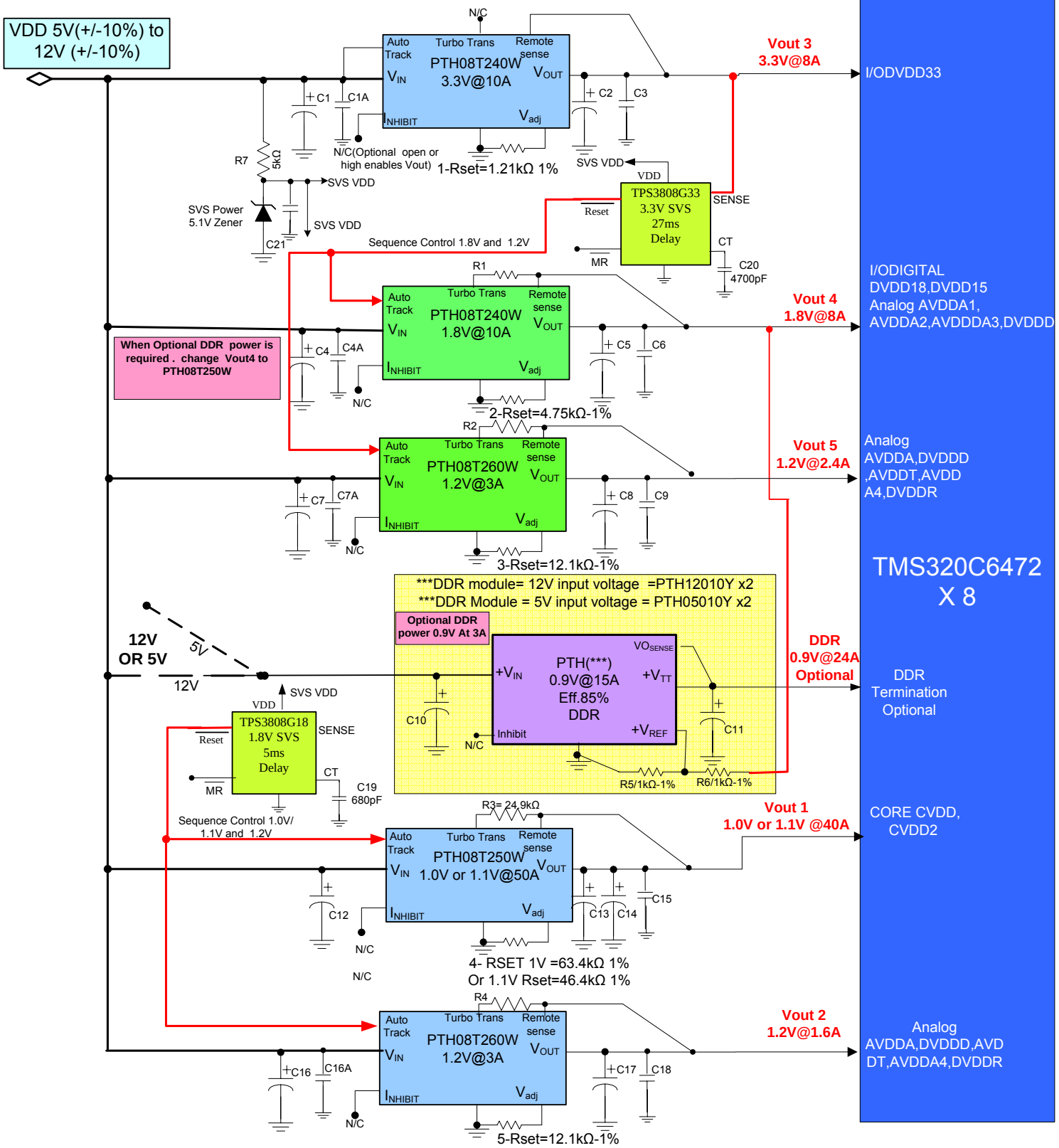
TMS320C6472 x 8

(8) x TMS320C6472 Power Modules



Sequence of Power : 1st Vout 3(3.3V) then 2nd Vout4 (1.8V),Vout 5 (1.2V) ,then 3rd Vout 1(1.0/1.1V) , Vout 2(1.2V)

TMS320C6472x8 Power (BOM)

TMS320C6472 x8 Power BOM		
Description	**Function**	**BOM/ Quantity**
Vin	5V ± 10% & 12V ± 10%	Bus
Vout1	1.0V/1.1V ± 5% @ 40A	PTH08T250W
Vout2	1.2V ± 5% @ 1600mA	PTH08T260W
Vout3	3.3V ± 5% @ 8A	PTH08T240W
Vout4	1.8V ± 5% @ 8A	PTH08T240W
Vout5	1.2V ± 5% @ 2400mA	PTH08T260W
DDR Termination (label as optional)	0.9V @ 24 A (8 DDR at 3A each)	PTH12010Y x2 (12Vin)or PTH05010Y x 2 (5Vin)
If DDR is present change Vout 4 to higher power Component	1.8V ± 5% @ 8A + 24A= 32A	PTH08T250W
3.3V Supervisor	SVS Vout 4, Vout 5	TPS3808G33
1.8V Supervisor	SVS Vout 1, Vout 2	TPS3808G18
Zener Diode 5.1V clamp	SVS power VDD	1- 5.1V Zener
Resistors 1% Voltage adjust	Each Power regulator requires Voltage Rset resistor-1% tolerance	1-Rset=1.21kΩ :: 2-Rset=4.75kΩ 1% :: 3-Rset 12.1kΩ:: 4-Rset=63.4kΩ(1V0),or 46.4kΩ(1V1):: 5-Rset=1.21kΩ
Capacitors for CT timing SVS TPS 3808	680pf(1) , 4700pf(1)	C19=680pF , C20=4700pF., C21=0.1µF
Polarized Capacitors Input capacitors	See product specifcation for details Low ESR<50mΩ	C1,C4,=220µF ,C7,C16=330µF, C12= 560µF (12Vin) or 1000µF (5Vin), C10= 470µF(DDR)
Polarized Capacitors output capacitors	See product specifcation for details Low ESR <30mΩ ::DDR optional > 40mΩ	C2,C5=220µF::C8, C17=100µF:: C13, C14=680µF x2 ESR ≤6mΩ :: C11=470-940µF > 30mΩ(DDR)
Identified Ceramic capacitors	See product specifcation for details	C3,C6,C9,C15,C18 =100µF Output Capacitors C1A, C4A, C7A C16A=22µF Input Capacitors
Resistors misc	See product specificattion for details. R1 and R2 are set based upon the output capacitance	R1=Open, R2=open. R3=24.9kΩ, R4=open, R5 /R6= 1kΩ-1%, R7=5kΩ

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