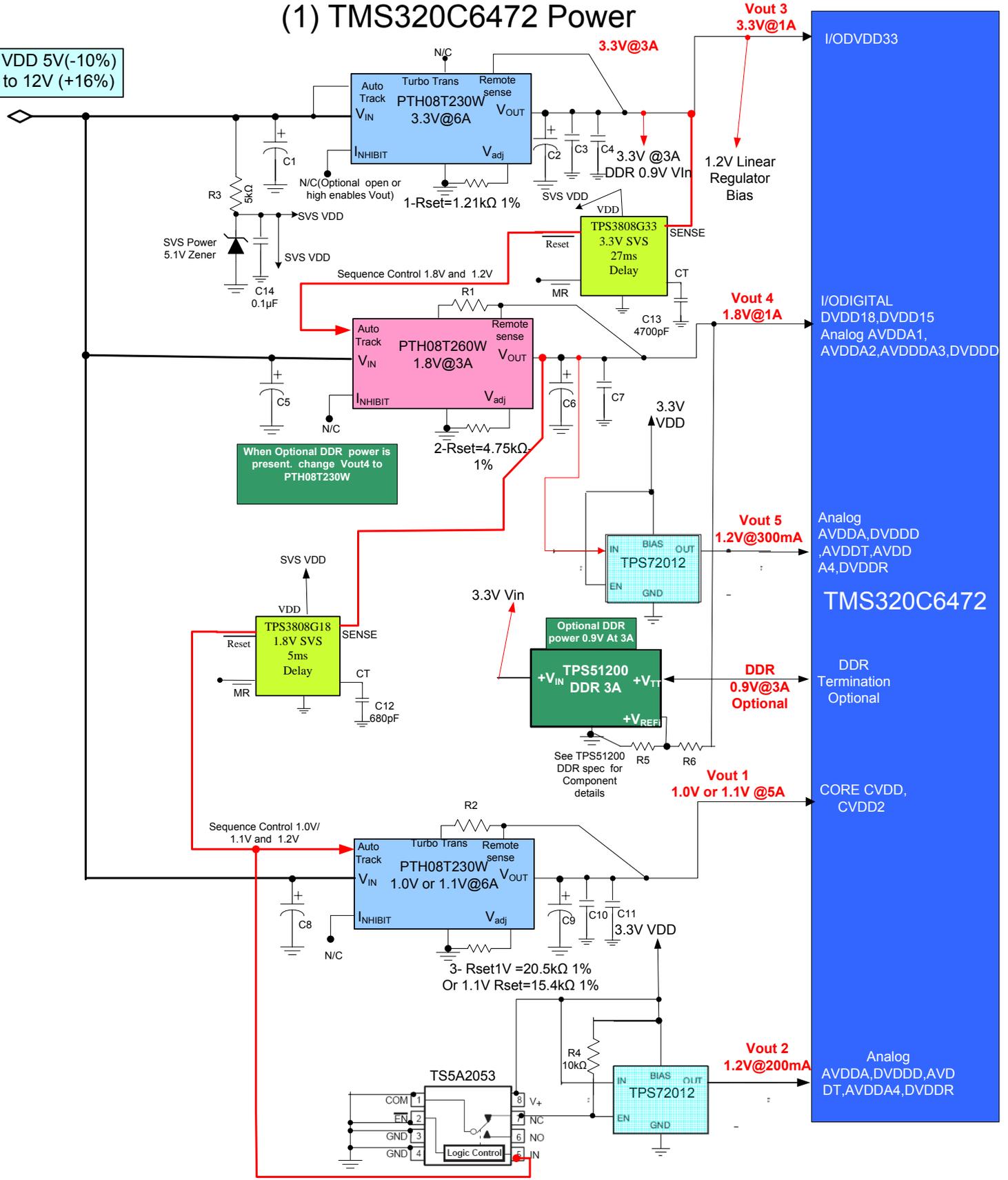


(1) TMS320C6472 Power

VDD 5V(-10%)
to 12V(+16%)



Sequence of Power : 1st Vout 3(3.3V) then 2nd Vout4 (1.8V),Vout 5 (1.2V) ,then 3rd Vout 1(1.0/1.1V) , Vout 2(1.2V)

(1) TMS320C6472 Power (BOM)

TMS320C6472 Power		
Description	Function	BOM/ Quantity
Vin	5V ± 10% & 12V ± 10%	Bus
Vout1	1.0V/1.1V ± 5% @ 5A	PTH08T230W
Vout2	1.2V ± 5% @ 300mA	TPS72012
Vout3	3.3V ± 5% @ 1A	PTH08T230W
Vout4	1.8V ± 5% @ 1A	PTH08T260W (DDR drive)
Vout5	1.2V ± 5% @ 200mA	TPS72012
DDR Termination (label as optional)	0.9V @ 3000mA	TPS51200 from 3.3V See TPS51200 DDR spec for Component details R5 and R6
3.3V Supervisor	SVS Vout 4, Vout 5	TPS3808G33
1.8V Supervisor	SVS Vout 1, Vout 2	TPS3808G18
TS5A2053	SPDT sw itch for core CVDD1	1- LDO enable
Zener Diode 5.1V clamp	SVS power VDD	1- 5.1V Zener
Resistors 1% Voltage adjust	Each Pow er Source requires 1RSET 1%	1-Rset=1.21kΩ 2-Rset=4.75kΩ 1% 3-Rset=20.5kΩ(1V0),or 15.4kΩ(1V1)
Capacitors for CT timing SVSTPS 3808	680pf(1) , 4700pf(1)	C12=680pf , C13=4700pf., C14=0.1μF
Polarized Capacitors	See product specifcation for details	C1,C5, C8 =330μF C2,C6,C9=100μF
Identified Ceramic capacitors	See product specifcation for details	C3,C4,C7,C10,C11= 100μF
Resistors misc	See product specifcattion for details. R1 and R2 are set based upon the output capacitance	R1=Open, R2=open. R3=5kΩ, R4=10kΩ, R5 /R6 see TPS51200

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