

In current limit operation, the gm amplifier monitors the voltage across sense resistor and compares it with an internal reference, if the sense voltage is greater than the threshold the gm amplifier gradually reduces the peak current capability of the DC/DC converter until the differential voltage is equal to the reference voltage, we use this feature to regulate a constant current into the load which in our case is LEDs.

Settings

The [LM5177EVM-HP](#) is used for this application brief, as shown in [Table 1](#), the configuration of the current limiting operation.

Table 1. Positive Current Limit Configuration Overview

LM5177 Pins	Action
Avg current limit on CFG pin	Enabled
SYNC pin	Pulled high (to VCC for example)
IMONOUT pin	Place the Compensation network

The current monitoring is enabled by default, although it can get disabled by forcing the IMONOUT pin to VCC during the device startup (pull-up IMONOUT to VCC). With the resistor connected to the CFG pin, the IMON function can be selected between monitoring and limiting function (limiting is ignored if IMONOUT is pulled to VCC). If limiting is selected, then the SYNC pin pulled to high at startup enables positive current limit.

Results

Functioning 14.2 V LED load (5 LEDs in series) with proper switching and almost no visible current ripple with 500 mA constant DC current fed to LED load through 100 mΩ for R_{CS} going into the ISNSN and ISNSP, as shown in [Figure 2](#). As for the startup, we can see in [Figure 3](#) the switching nodes before and after the inductor or buck and boost switch nodes alongside the Vout and Iout waveforms. We can see that the soft-start time due to the soft-start capacitor is around 8.6 ms to 9 ms.

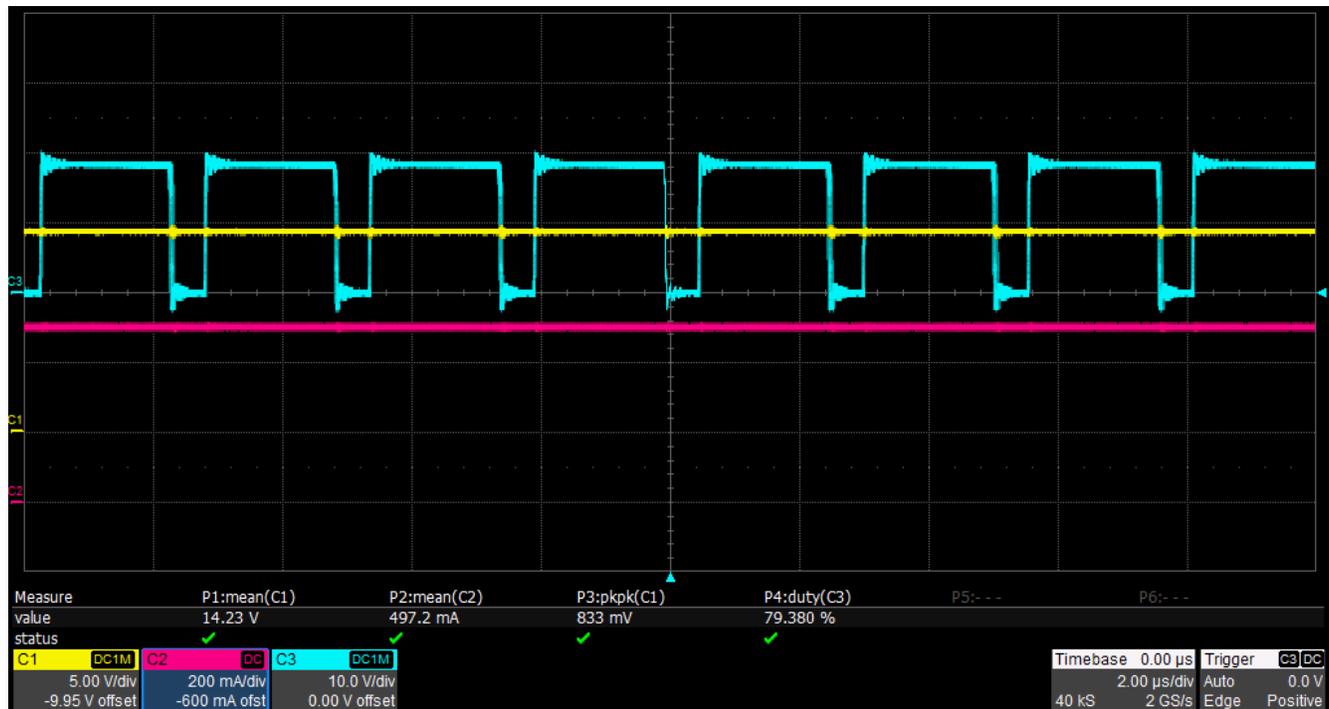


Figure 2. Steady-State Vin = 18 V, Vout =14.2 V (5 LEDs) at the Output

(Blue: Buck SW node, Yellow: Vout, Purple: Iout)

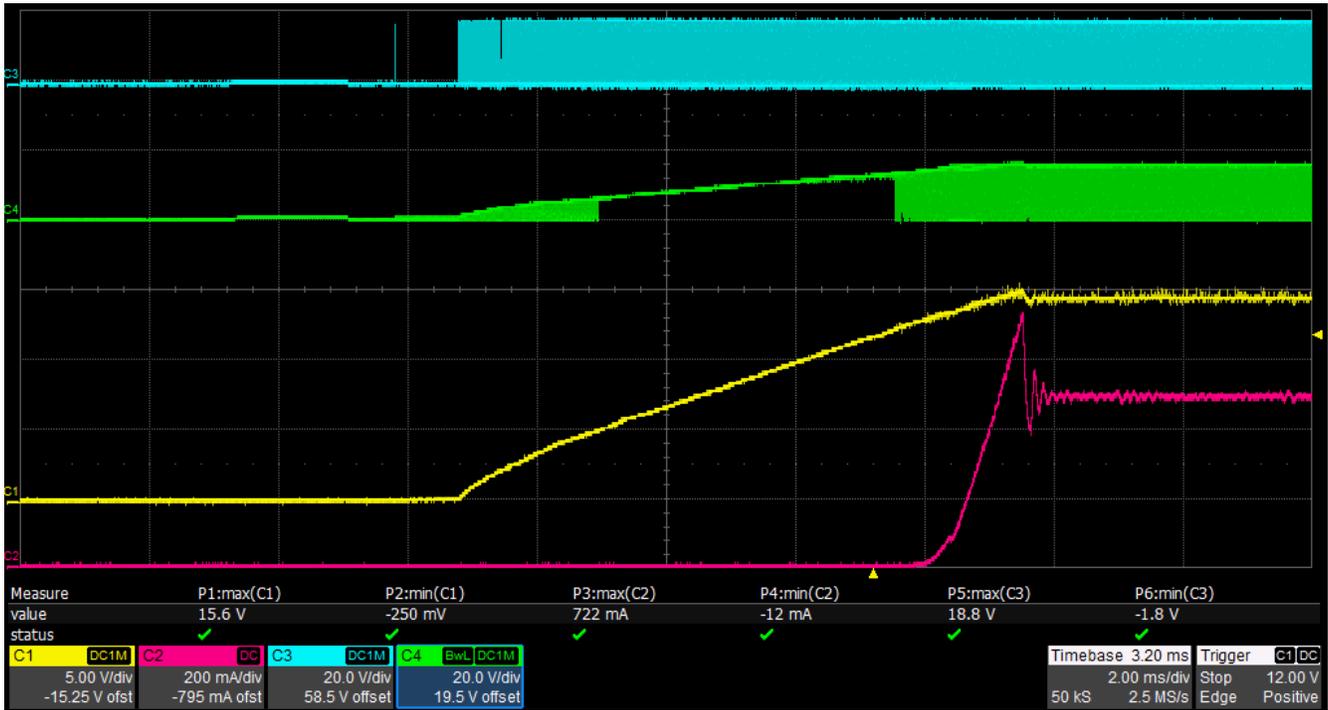


Figure 3. Startup $V_{in} = 18\text{ V}$, $V_{out} = 14.2\text{ V}$ (5 LEDs) at the Output

(Blue: Buck SW node, Green: Boost SW node, Yellow: Vout, Purple: Iout)

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