EVM User's Guide: TPS6286B10EVM-049

TPS6286B10 Buck Converter Evaluation Module

Description

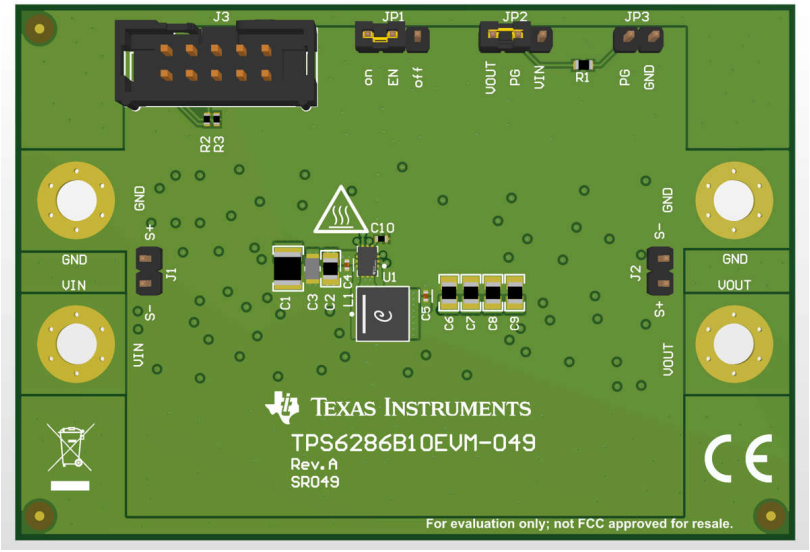
The TPS6286B10EVM-049 is designed to help the user easily evaluate and test the operation and functionality of the TPS6286B10 buck converters. The EVM can also be used to evaluate the TPS6286B08 devices. The EVM converts 2.4V to 5.5V input voltage to a regulated output voltage. The output current can go up to 10A for the TPS6286B10EVM-049.

Features

- DCS-Control topology for fast transient response
- 8mΩ internal HS and LS power MOSFETs
- 0.7% output voltage accuracy
- 4µA operating quiescent current
- 2.4V to 5.5V input voltage range
- Available output voltage ranges for dynamic voltage scaling (DVS) through I2C
- Output voltage range from 0.4V to 1.675V with 5mV step size
- 1.2MHz switching frequency
- Selection by I2C interface
- Hiccup or latching short-circuit protection
- Output voltage ramp speed
- Forced PWM or power save mode
- Output voltage discharge
- 100% duty cycle mode
- Power-good indicator with window comparator
- Thermal shutdown
- Available in 2mm × 3mm QFN

Applications

- Core supply for FPGAs, CPUs, ASICs, or video chipsets
- Machine vision cameras
- IP network cameras
- Solid-state drives
- Optical modules
- Multifunction printers



TPS6286B10EVM-049

1 Evaluation Module Overview

1.1 Introduction

The TPS6286B10 device is a high-frequency, synchronous step-down converter designed for a small solution-size and high efficiency. The devices focus on high-efficiency, step-down conversion for up to 10A output current. At medium to heavy loads, the converter operates in PWM mode and automatically enters *power save mode* operation at light load to maintain high efficiency over the entire load-current range. The device is available in a 2mm × 3mm QFN package.

This user's guide describes the characteristics, operation, and use of TI's evaluation module (EVM) for the TPS6286B10 devices. This user's guide includes setup instructions for the hardware, a printed-circuit board (PCB) layout, a schematic diagram, and a bill of materials (BOM).

1.2 Kit Contents

Table 1-1. TPS6286B10EVM-049 Kit Contents

Item	Description	Quantity
TPS6286B10EVM-049	PCB	1

1.3 Specification

Table 1-2 provides a summary of the TPS6286B10EVM-049 performance specifications.

Table 1-2. Performance Specification Summary

Specification		Test Conditions	MIN	TYP	MAX	Unit
Input voltage			2.4		5.5	V
Output voltage setpoint				0.9		V
Output current	TPS6286B10EVM-049		0		10.0	A

1.4 Device Information

Th purpose of the TPS6286B10EVM-049 is to support the evaluation of the TPS6286Bx device family. The TPS6286B08 and TPS6286B10 are family of pin-to-pin 8A and 10A synchronous step-down DC/DC converters with I2C interface. All devices offer high efficiency and ease of use. The I2C compatible interface offers several control and monitoring features. Devices can operate in power-save mode for maximum efficiency, or forcedPWM mode for best transient performance and lowest output voltage ripple.

2 Hardware

2.1 Setup

This section describes how to correctly use the TPS6286B10EVM-049.

2.1.1 Connector Descriptions

MH1 - VIN	Positive input voltage connection from the input supply for the EVM
J1-1 – VINsense, J1-2 – GNDsense	Input voltage sense connections, measure the input voltage at this point.
MH2 – GND	Input return connection from the input supply for the EVM
MH3 – VOUT	Positive output voltage connection
J2-1 – VOUTsense, J2-2 - GNDsense	Output voltage sense connections, measure the output voltage at this point.
MH4 – GND	Output return connection
JP1 – EN	EN pin jumper. Place the supplied jumper across ON and EN to turn on the IC. Place the jumper across OFF and EN to turn off the IC.
JP2 – PG	PG pin jumper through a pull up resistor to Vin or Vout.
J3 – PG	The PG output appears on pin 1 of this header with a convenient ground on pin 2.
J3 – I2C	I^2C connection configured for being used with the USB2ANY interface.

2.1.2 Hardware Setup

To operate the EVM, set jumpers JP1 and JP2 to the desired positions per Section 2.1.1. Connect the input supply to MH1 and MH2, between VIN and GND, and connect the load to MH3 and MH4 between VOUT and GND.

To evaluate the I^2C features a USB2ANY interface can be connected to J3. For this interface, a software GUI is available on ti.com.

2.2 Warning and Caution

Caution

Hot surface.
Contact can cause burns.
Do not touch!

3 Implementation Results

3.1 TPS6286B10EVM-049 Test Results

The TPS6286B10EVM-049 was used to take the typical characteristics data in the TPS6286B10 data sheet. See the data sheet on ti.com (SLUSFG2) for the performance of this EVM.

3.2 Modifications

The printed-circuit board (PCB) for this EVM is designed to accommodate the different output current versions (8A and 10A) of this integrated circuit (IC). On the EVM, the default output voltage can be changed through I2C interface.

3.2.1 Output Voltage Setting

The output voltage of U1 is set to a default of 0.9V. During operation, the output voltage can be changed using the I^2C interface. For more details, see the data sheet on ti.com (SLUSFG2)

3.2.2 I^2C Interface

Output voltage, output voltage ramp time, and various control features can be controlled through the I^2C interface. IC status information is also available. For more details, see the data sheet on ti.com (SLUSFG2)

4 Hardware Design Files

4.1 Schematic

Figure 4-1 shows the schematic of TPS6286B10EVM-049.

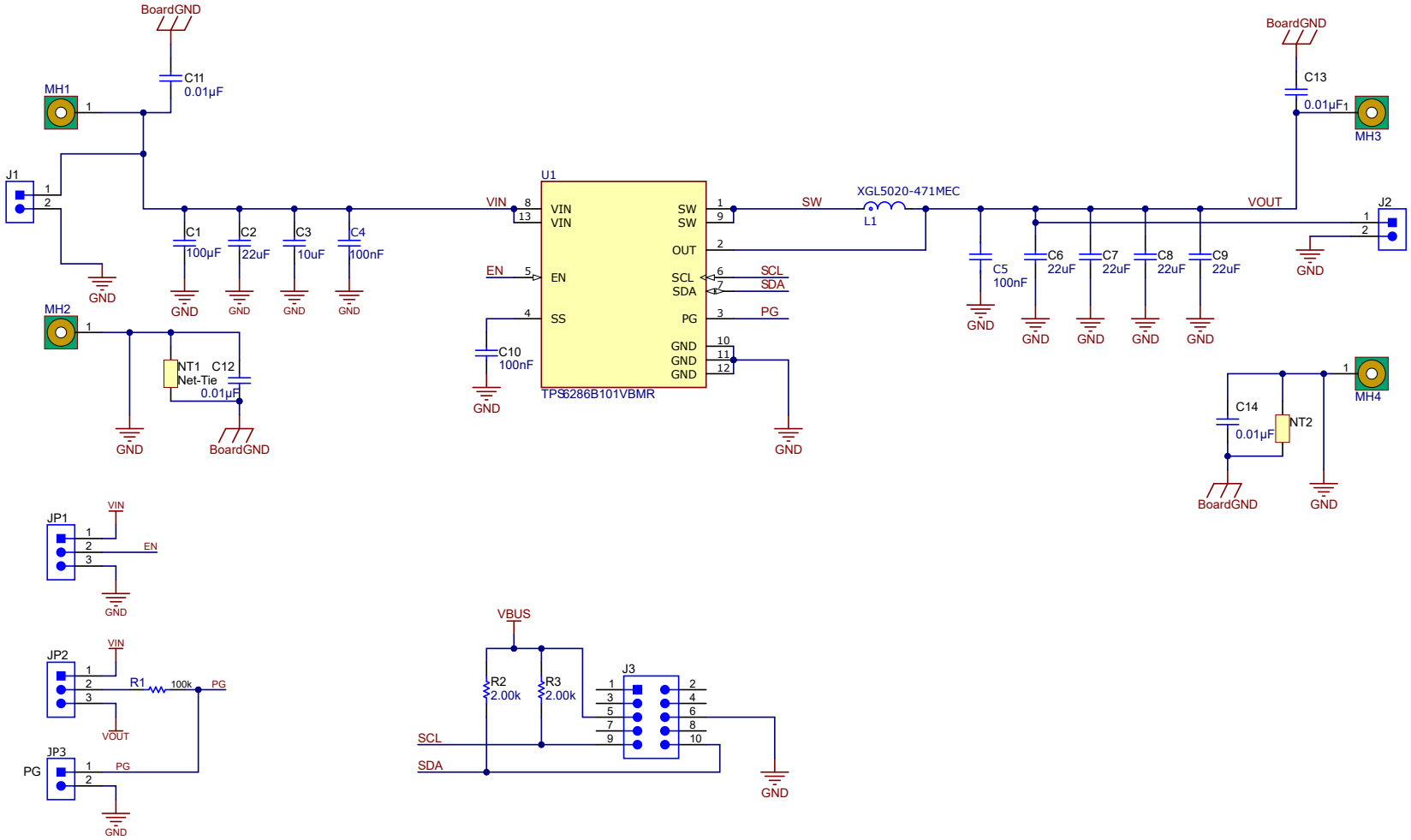


Figure 4-1. TPS6286B10EVM-049 Schematic

4.2 Board Layout

This section provides the TPS6286B10EVM-049 board layout.

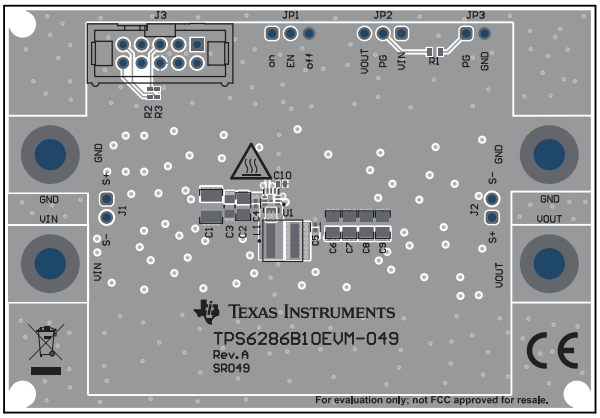


Figure 4-2. Top Layer Composite

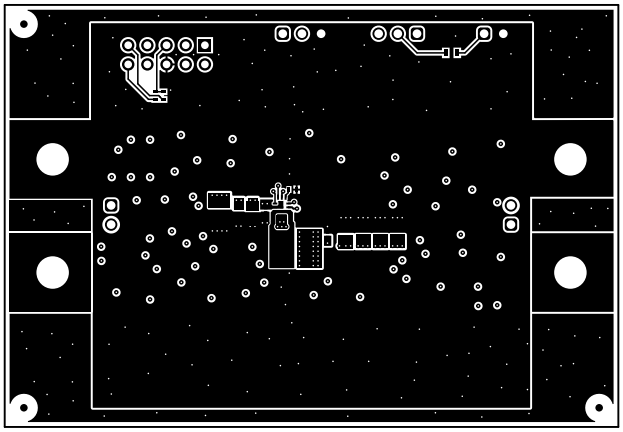


Figure 4-3. Top Layer

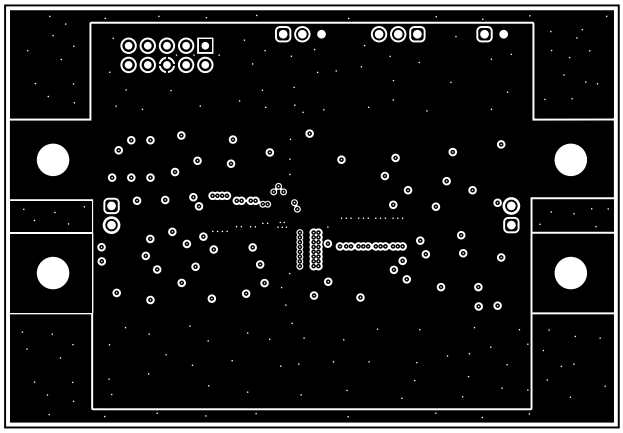


Figure 4-4. Signal Layer 1

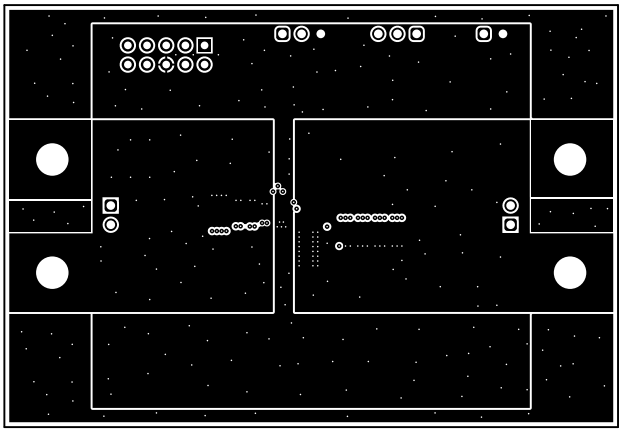


Figure 4-5. Signal Layer 2

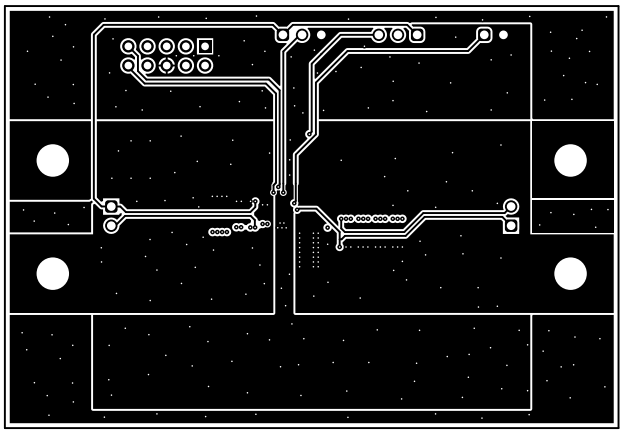


Figure 4-6. Signal Layer 3

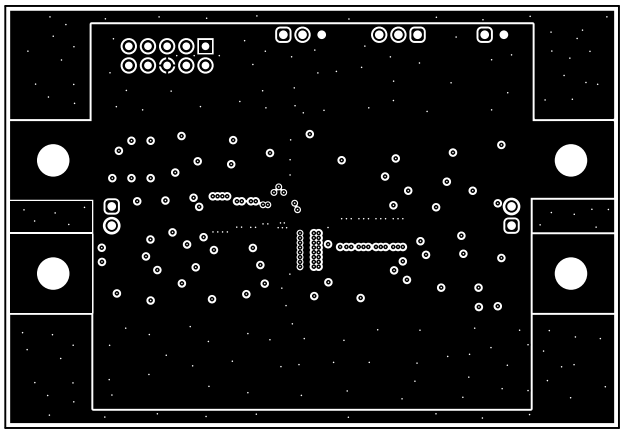


Figure 4-7. Signal Layer 4

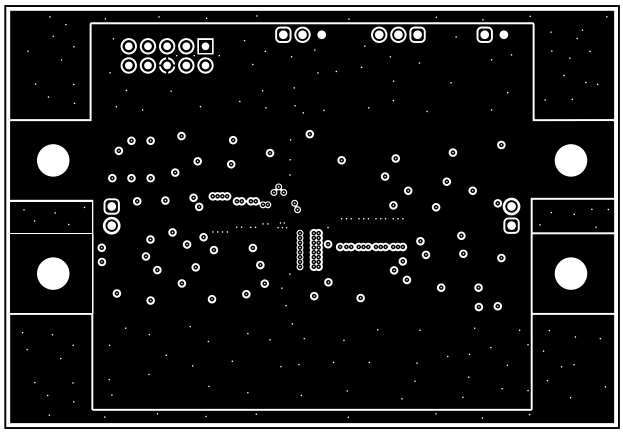


Figure 4-8. Signal Layer 5

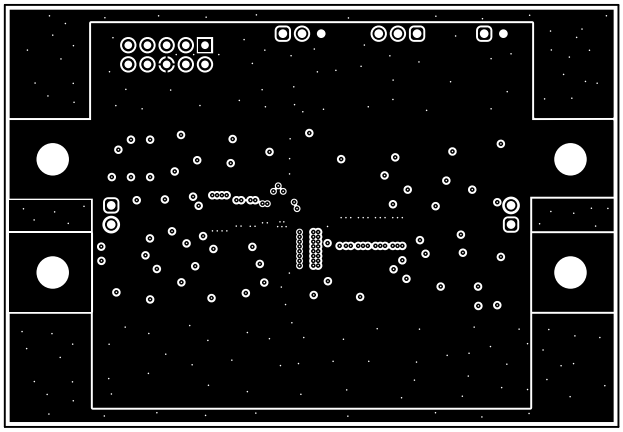


Figure 4-9. Signal Layer 6

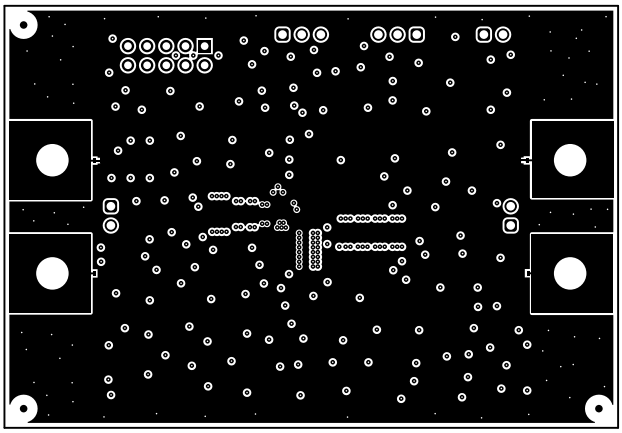


Figure 4-10. Bottom Layer

4.3 Bill of Materials

Table 4-1 lists the BOM for the TPS6286B10EVM-049.

Table 4-1. TPS6286B10EVM-049 BOM

Qty	Ref Des	Value	Description	Size	Part Number	MFR
1	C1	100uF	CAP, CERM, 100µF, 10V,+/- 20%, X5R, 1210	1210	GRM32ER61A107ME20K	MuRata
5	C2, C6, C7, C8, C9	22uF	CAP, CERM, 22uF, 10V, +/- 20%, X7R, 0805	0805	GRM21BZ71A226ME15L	MuRata
1	C3	10uF	CAP, CERM, 10uF, 10V, +/- 10%, X7R, 0805	0805	GCM21BR71A106KE22L	MuRata
2	C4, C5	100nF	Cap Ceramic 100nF 6.3V X7R ±10% SMD 0402 +125°C Paper T/R	0402	CL05B104KQ5NNNC	Samsung
1	C10	0.1uF	CAP, CERM, 0.1uF, 10V, +/- 10%, X7R, 0402	0402	GRM155R71A104KA01D	MuRata
4	C11, C12, C13, C14	0.01uF	CAP, CERM, 0.01µF, 50V,+/- 10%, X8R, AEC-Q200 Grade 0, 0402	0402	CGA2B3X8R1H103K050BB	TDK
1	L1	0.47uH	470nH Shielded Molded Inductor 16A 4.3mOhm Max Nonstandard	SMT2	XGL5020-471MEC	Coilcraft
1	R1	100k	RES, 100 k, 1%, 0.1 W, AEC-Q200 Grade 0, 0603	0603	CRCW0603100KFKEA	Vishay-Dale
2	R2, R3	2k	RES, 2.00 k, 1%, 0.063 W, AEC-Q200 Grade 0, 0402	0402	CRCW04022K00FKED	Vishay-Dale
1	U1		2.4V to 5.5V Input, 6A Synchronous Step-Down Converter in 2mm × 3mm QFN Package with I2C interface	VQFN-HR13	TPS6286B10VBMR	Texas Instruments

5 Additional Information

5.1 Trademarks

All trademarks are the property of their respective owners.

STANDARD TERMS FOR EVALUATION MODULES

1. *Delivery:* TI delivers TI evaluation boards, kits, or modules, including any accompanying demonstration software, components, and/or documentation which may be provided together or separately (collectively, an “EVM” or “EVMs”) to the User (“User”) in accordance with the terms set forth herein. User's acceptance of the EVM is expressly subject to the following terms.

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   1.2 EVMs are not intended for consumer or household use. EVMs may not be sold, sublicensed, leased, rented, loaned, assigned, or otherwise distributed for commercial purposes by Users, in whole or in part, or used in any finished product or production system.

2 *Limited Warranty and Related Remedies/Disclaimers*:

   2.1 These terms do not apply to Software. The warranty, if any, for Software is covered in the applicable Software License Agreement.

   2.2 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for a nonconforming EVM if (a) the nonconformity was caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI, (b) the nonconformity resulted from User's design, specifications or instructions for such EVMs or improper system design, or (c) User has not paid on time. Testing and other quality control techniques are used to the extent TI deems necessary. TI does not test all parameters of each EVM. User's claims against TI under this Section 2 are void if User fails to notify TI of any apparent defects in the EVMs within ten (10) business days after delivery, or of any hidden defects with ten (10) business days after the defect has been detected.

   2.3 TI's sole liability shall be at its option to repair or replace EVMs that fail to conform to the warranty set forth above, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

WARNING

Evaluation Kits are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems.

User shall operate the Evaluation Kit within TI’s recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI’s recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI’s instructions for electrical ratings of interface circuits such as input, output and electrical loads.

NOTE:

EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGREDATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.

3 *Regulatory Notices:*

3.1 *United States*

3.1.1 *Notice applicable to EVMs not FCC-Approved:*

FCC NOTICE: This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 *For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:*

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- *Reorient or relocate the receiving antenna.*
- *Increase the separation between the equipment and receiver.*
- *Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.*
- *Consult the dealer or an experienced radio/TV technician for help.*

3.2 *Canada*

3.2.1 *For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247*

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

3.3 *Japan*

3.3.1 *Notice for EVMs delivered in Japan:* Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。

https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-delivered-in-japan.html

3.3.2 *Notice for Users of EVMs Considered "Radio Frequency Products" in Japan:* EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

【無線電波を送信する製品の開発キットをお使いになる際の注意事項】 開発キットの中には技術基準適合証明を受けていないものがあります。 技術適合証明を受けていないもののご使用に際しては、電波法遵守のため、以下のいずれかの措置を取っていただく必要がありますのでご注意ください。

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2. 実験局の免許を取得後ご使用いただく。
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西新宿三井ビル

3.3.3 *Notice for EVMs for Power Line Communication:* Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_02.page

電力線搬送波通信についての開発キットをお使いになる際の注意事項については、次のところをご覧ください。https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-for-power-line-communication.html

3.4 *European Union*

3.4.1 *For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive)*:

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

4 *EVM Use Restrictions and Warnings:*

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

4.3 *Safety-Related Warnings and Restrictions:*

4.3.1 User shall operate the EVM within TI's recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.

4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User's handling and use of the EVM and, if applicable, User assumes all responsibility and liability for compliance in all respects with such laws and regulations. User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.

5. *Accuracy of Information:* To the extent TI provides information on the availability and function of EVMs, TI attempts to be as accurate as possible. However, TI does not warrant the accuracy of EVM descriptions, EVM availability or other information on its websites as accurate, complete, reliable, current, or error-free.

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