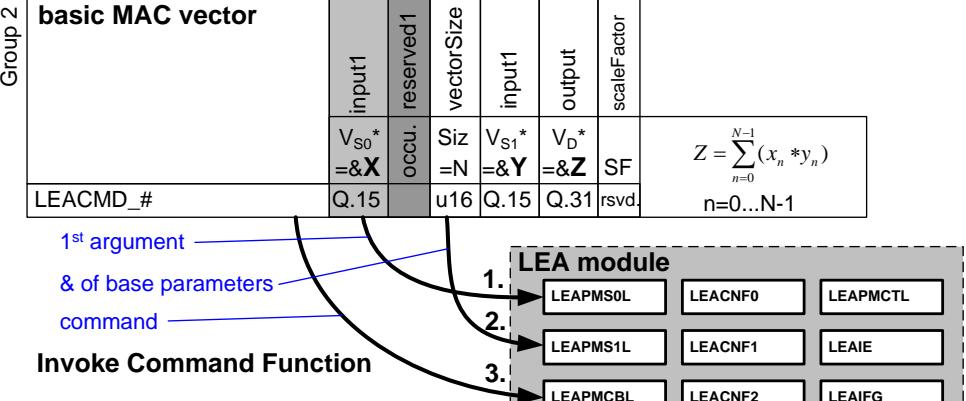
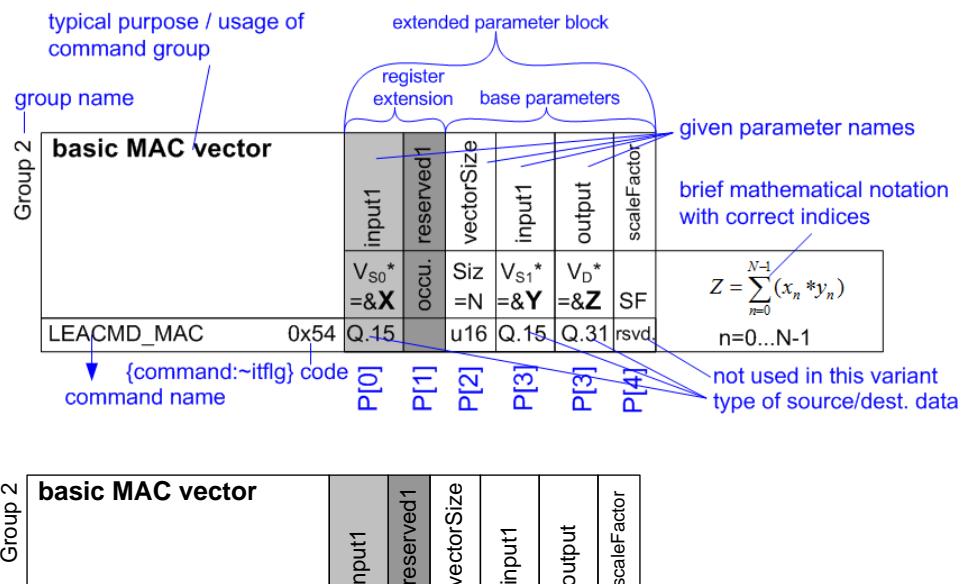
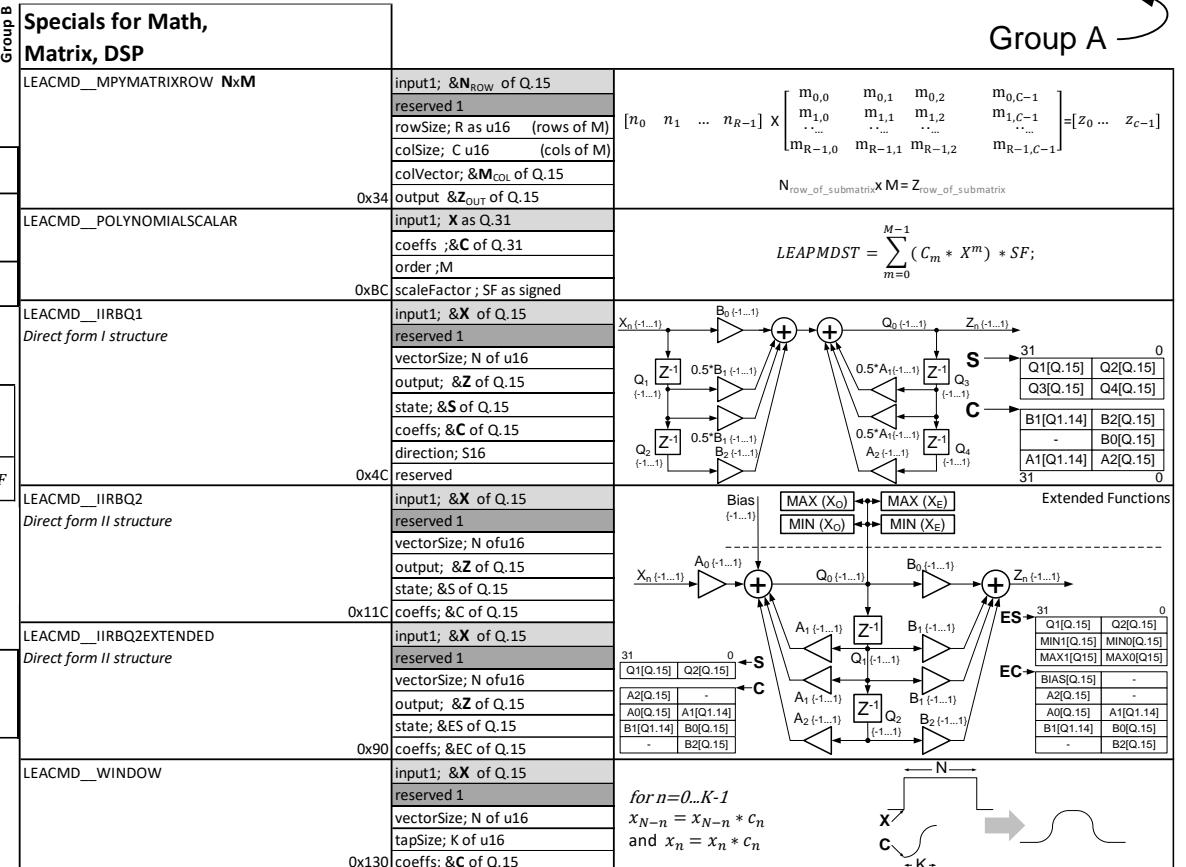


Low-Energy Accelerator (LEA) Common Parameter Blocks



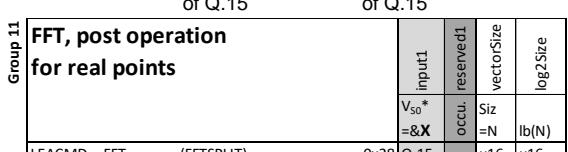
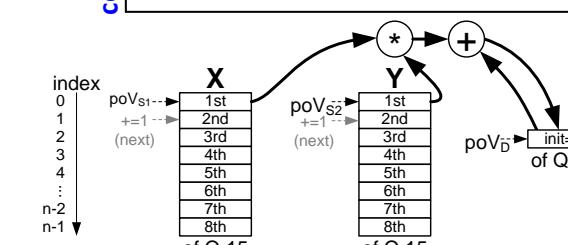
```
#include <msp430.h>

//macros to simplify LEA code (usually put in a header file)
#define LEA_EPRG(x) _Pragma(#x) //aux. def for NewLEA...
#define NewLEA_VAR(var) LEA_EPRG(RETAIN(var)) LEA_EPRG(DATA_SECTION(var,".leaRAM"))
#define Ladr(x) ((unsigned short)((unsigned long)(x) & 0xffff)>>2)
#define Q15(x) (x)<1.0? ((x)>-1.0? (x)*0x8000:0x8000):0x7FFF
#define LeaSP 0x3C00/4 //define Lea-Stack to top of LEA memory {32 bit oriented adr.}

NewLEA_VAR(Xi); NewLEA_VAR(Yi); NewLEA_VAR(Zo); NewLEA_VAR(P); //decl. variables in leaRAM

signed short Xi[]={Q15(-0.5), Q15(0.3), Q15(-0.4), Q15(0.5)}; //init with some input data
signed short Yi[]={Q15(-1) , Q15(-1) , Q15(-1) , Q15(-1)}; //init with some input data
signed long Zo[]={0x00000000}; //init space for result output
short P[]={0,0,4,0,0,0}; //here pre initialized parameters with N=4

void main(void) {
    WDTCTL = WDTPW | WDTHOLD; // Stop watchdog timer
    LEACNF0=0; LEACNF1=0; LEACNF2=LeaSP; //init LEA
    LEAPMCTL|=LEACMDEN; //enable LEA
    P[0]=Ladr(&Xi); //set Xi in parameters
    P[3]=Ladr(&Yi); //ditto with Yi using macro;
    P[4]=Ladr(&Zo); //address of Zo to last element of parameters
    LEAPMS0L=P[0]; //put 1st parameter directly in register ~PMS0L; here adr of X[]
    LEAPMS1L=Ladr(&P[2]); //put address of base parameter in ~PMS1L...
    LEAPMCBL=LEACMD__MAC + 0; //start/invoke command with ~ITFLGS=0 for simple execution
    S[0]=0; //put 0 in S[0] to enable LEA
}
```

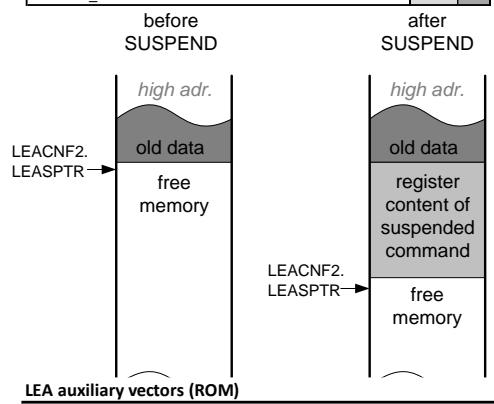


- Invoke a command**
 - 1. Declare a parameter block and variables using `NewLEA_VAR` macros;
 - 2. Initialize parameters according to the desired function (see this sheet)
 - 3. Invoke command by writing to LEAPMS0L, LEAPMS1L and LEAPMCBL with proper mode by setting `~ITFLGS` (see Invoke Methods)
 - 4. Pick up results

LEA notifies completion using flags, events, and interrupts

Low-Energy Accelerator (LEA) Common Parameter Blocks

Programming Structure		
	Input	vectorSize
V _{SP} *	=&L	N
LEACMD_MOVELONGLIST	0x80	u32
LEACMD_SUSPEND	0x00	
LEACMD_RESUME	0x08	



MOVELONGLIST Parameter Structure

```

L -> &SRC[0] {LeaAdr}
      &DST[0] {LeaAdr}
      &SRC[1] {LeaAdr}
      &DST[1] {LeaAdr}
      :
      &SRC[N-1] {LeaAdr}
      &DST[N-1] {LeaAdr}
  
```

N*2

Name	Value	Address range(Lea)	Width	Size
LEA_CONST_0	0x0	0xA000..0xA3FF	16B	2048
neutral element, dummy source		(dual)		
LEA_CONST_1	0x1	0xA400..0xA7FF	16B	2048
pos. epsilon		(dual)		
LEA_CONST_4	0x4000	0xA800..0xABFF	16B	2048
pos. amplitude margin 16B, + 0.5		(dual)		
LEA_CONST_7	0x7FFF	0xAC00..0xAFFF	16B	2048
neutral element, +1		(dual)		
LEA_CONST_8	0x8000	0xB000..0xB3FF	16B	2048
substitutions, complementary element, conversion of ADC/DAC formats, -1...		(dual)		
LEA_CONST_C	0xC000	0xB400..0xB7FF	16B	2048
neg. amplitude margin 16B, -.5		(dual)		
LEA_CONST_E	0xFFFFE	0xB800..0xBBFF	16B	2048
neg. double epsilon, inv. element		(dual)		
LEA_CONST_F	0xFFFFF	0xBC00..0xBF00	16B	2048
neg. epsilon		(dual)		
LEA_CONST_LONG_0	0x0	0xC000..0xC7FF	32B	2048
neutral element, dummy source		(dual)		
LEA_CONST_LONG_1	0x1	0xC800..0xCF00	32B	2048
pos. epsilon		(dual)		
LEA_CONST_LONG_4	0x4000:0000	0xD000..0xD7FF	32B	2048
pos. amplitude margin 32B, +.5		(dual)		
LEA_CONST_LONG_7	0x7FFF:FFFF	0xD800..0xDFFF	32B	2048
neutral element		(dual)		
LEA_CONST_LONG_8	0xE000..			
substitutions, complementary element, conversion of ADC/DAC formats, ...	0x8000:0000	0xE7FF	32B	2048
LEA_CONST_LONG_C	0xC000:0000	0xE800..0xEF00	32B	2048
neg. amplitude margin 32B, -.5		(dual)		
LEA_CONST_LONG_E	0xFFFFF:FFFF	0xF000..0xF7FF	32B	2048
neg. double epsilon, inv. element		(dual)		
LEA_CONST_LONG_F	0xFFFFF:FFFF	0xF800..0xFFFF	32B	2048
neg. epsilon		(dual)		

LEA common functions polynomial coefficients (ROM)

Name	function / purpose	Address(Lea)	Width	Size N
LEA_CONST_LONG_SQRT	coefficients for SQRT(X-1)	0x8000	32B	33
LEA_CONST_LONG_EXP	coefficients for EXP(X)	0x8021	32B	13
LEA_CONST_LONG_2COS	coefficients for COS(X^2)	0x802E	32B	7
LEA_CONST_LONG_SIN	coefficients for SIN(X)	0x8035	32B	12
LEA_CONST_LONG_COS	coefficients for COS(X)	0x8041	32B	13
LEA_CONST_LONG_INV	coefficients for 1/X	0x804E	32B	33
LEA_CONST_LONG_2SECH	coefficients for SECH(X^2)	0x806F	32B	25
LEA_CONST_LONG_CBRT	coefficients for CBRT(X) cubic root	0x8088	32B	33
LEA_CONST_SQRTW	coefficients for SQRT(X/P) wide range	0x80A1	16B	5
LEA_CONST_SQRT	coefficients for SQRT(X-1)	0x80A4	16B	13
LEA_CONST_SINW	coefficients for SIN(X/P)	0x80AB	16B	7
LEA_CONST_SIN	coefficients for SIN(X)	0x80AF	16B	8
LEA_CONST_COS	coefficients for COS(X)	0x80B3	16B	9
LEA_CONST_INV	coefficients for 1/X	0x80B8	16B	17
LEA_CONST_TAN	coefficient for TAN(X)	0x80C1	32B	12
LEA_CONST_LONG_ASIN	coefficient for ASIN(Nx)	0x80CD	32B	24
LEA_CONST_LONG_ATN1	coefficient for ATN(x) segment 1	0x80E5	32B	6
LEA_CONST_LONG_ATN2	coefficient for ATN(x) segment 2	0x80EB	32B	9
LEA_CONST_LONG_ATN3	coefficient for ATN(x) segment 3	0x80F4	32B	10
LEA_CONST_LONG_2EXP	coefficient for EXP(X^2)	0x80FE	32B	10
LEA_CONST_LONG_LOG	coefficient for LOG(X)	0x8108	32B	13
LEA_CONST_LONG_4EXP	coefficient for EXP(X) auxiliary	0x8115	32B	10
LEA_CONST_LONG_2SIN	coefficient for SIN(X^2)	0x811F	32B	4

Useful macros for Code Composer

```

#define Ladr(x) ((unsigned short)((unsigned long)(x) & 0xffff)>>2)
#define Q15(x) (x)<1.0? ((x)>-1? (x)*0x8000:0x8000) :0x7FFF //Mathematical correct
#define Q15(x) (((x)<1? ((x)>-1? (x)*0x8000:0x8000) :0x7FFF) //Ceiling rounded version
#define Q15c(x) (((x)<1? ((x)>-1? (x)*0x7FFF:(x)*0x7FFF) :((x)<=-1? 0x8000:(x)*0x8000)) //Floor rounded
#define Q31(x) (x)<1.0? ((x)>=1? 0x7FFF:(x)*0x8000) :0x7FFFFFF //Mathematical correct
#define Q31c(x) (((x)<1? ((x)>=1? 0x7FFF:(x)*0x8000) :0x7FFFFFF) //Ceiling rounded
#define Q31f(x) ((x)>=0? ((x)>=1? (x)*0x7FFF:(x)*0x7FFF) :((x)<=-1.0? 0x80000000:(x)*0x80000000)
#define IQ16_15(x) (x)<32768? ((x)>=-32768? (x)*0x8000:0x8000) :0x7FFFFFF //Math. correct
#define LEA_EPRG(x) _Pragma(#x) // auxiliary macro
// constructor for new LEA global variable without alignment
#define NewLEA_VAR(var) LEA_EPRG(RETAIN(var)) LEA_EPRG(DATA_SECTION(var,".leaRAM"))
// constructor for new LEA variable with alignment
#define NewLEA_VAR(var,align) LEA_EPRG(RETAIN(var)) LEA_EPRG(DATA_SECTION(var,".leaRAM"))
// constructor for new LEA scratch variable without alignment (API internal)
#define NewLEA_XVAR(var) LEA_EPRG(RETAIN(var)) LEA_EPRG(DATA_SECTION(var,".leascratch"))
// constructor for new global variable without alignment
#define NewLEA_dVAR(var) LEA_EPRG(RETAIN(var)) LEA_EPRG(DATA_SECTION(var,".data"))
// constructor for new FRAM variable without alignment
#define NewLEA_fVAR(var) LEA_EPRG(RETAIN(var)) LEA_EPRG(DATA_SECTION(var,".const"))

```

LEA Hardware Registers (abridged list)

LEACNF0	H 31/15	-TISEL	-TEN	-TRST	-MEMFLTS	-CFLTS	-TIMFLTS	-FREES	-DONE	16/0				
	L	-MEMFLTE	-TIMFLTE	-ILB	-ILPM	-LPR	-FLTHOLD	-SWRST						
LEACNF1	H 31/15	LEAWRSTAT	LEARDSTAT	-MEMFLTC	-CFLTC	-TIMFLTC	-FREEC	-DONEC		16/0				
	L	LEASAST	LEAPWST	LEAMODE					-BUSY					
LEACNF2	H 31/15									16/0				
	L								LEASPTR					
LEACMA	H 31/15									16/0				
	L								LEACMDP (high word)					
LEACMCTL	H 31/15								LEACMDP (low word)					
	L								LEAAP					
LEACMDSTAT	H 31/15								LEACROFF	-DEC	-INC	-MAE	16/0	
	L								LEACMD (working command)		-ITFLG			
LEAS0STAT	H 31/15								LEAS0VAL / LEAS1VAL (high word ; working register)				16/0	
	L								LEAS0VAL / LEAS1VAL (low word ; working register)					
LEADSTSTAT	H 31/15								LEADSTVAL / LEAPMDST (high word ; working result / result register)				16/0	
	L								LEADSTVAL / LEAPMDST (low word ; working buffer / result register)					
LEAPMDST	H 31/15								-TRG				16/0	
	L								-CMDEN					
LEAPMS0	H 31/15								LEAPMS0 / LEAPMS1 (high word ; source register)				16/0	
	L								LEAPMS0 / LEAPMS1 (low word ; source register)					
LEAPMS1	H 31/15								LEACTX (command context)				16/0	
	L								LEACMD (command buffer)		-ITFLG			
LEAPMCB	H 31/15													
	L													
LEAISET	H 31/15								-CMDIS	-SDIIS	-OORIS	-TIS	-OVLIS	16/0
	L													
LEAIE	H 31/15								-CMDIE	-SDIIIE	-OORIE	-TIE	-OVLIE	16/0
	L													
LEAIFG	H 31/15								-CMDIFG	-SDIIFG	-OORIFG	-TIFG	-OVLIFG	16/0
	L													
LEAIV	H 31/15													
	L													

Registers mainly for Programming Purposes Registers mainly for Debug and Diagnostics Purposes

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