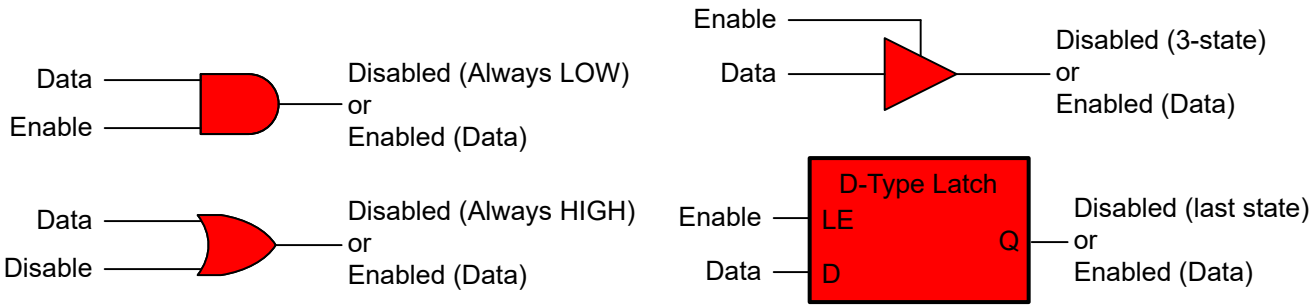
Product Overview

Enable or Disable a Digital Signal



See more about this use case in the *Logic Minute* video *Enable or Disable a Digital Signal*.

Design Considerations

- Refer to the table below to select the right function to meet system requirements
- Select the right family based on device parameters desired (for example, low power, strong drive, and high speed)
- *[FAQ] What is the default output of a latched device? (Flip-Flop, latch, register)*
- *[FAQ] What's the difference between logic output types (push-pull, open-drain, 3-state)?*
- *[FAQ] How do I size pull-up or pull-down resistors?*
- *[FAQ] What is a floating input or floating node?*
- Ask a question on our *Engineer-to-Engineer forum*

Recommended Parts

Part Number	AEC-Q100	Input Type	Behavior When Disabled	Features
SN74LVC1G08		Enable	LOW	• Wide operating supply range (1.65 V – 5.5 V) • High drive strength (32 mA) • Over-voltage tolerant inputs • Back-drive protection
SN74LVC1G08-Q1	✓	Enable	LOW	
SN74LVC1G32		Disable	HIGH	
SN74LVC1G32-Q1	✓	Disable	HIGH	
SN74LVC1G126		Enable	3-state	
SN74LVC1G126-Q1	✓	Enable	3-state	
SN74LVC1G373		Enable	Last known state	

For more devices, browse through the *online parametric tool* where you can sort by desired voltage, channel numbers, and other features.

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