



## ABSTRACT

The DLP® LightCrafter™ Single DLPC900 evaluation module (EVM) offers a reference design to enable faster development cycles for users of the Single DLPC900 Controller architecture in support of DMDs that use a single DLPC900 controller. This platform targets applications needing intelligent pattern management along with high resolution display.

## About This Guide

This guide explains the hardware and software features of the DLP LightCrafter Single DLPC900 EVM systems. The EVM architecture and connectors are described along with a quick start guide on how to operate the DLP LightCrafter Single DLPC900 EVM using a Graphical User Interface (GUI). Specific details for each DLP component can be found in related component documentation.

## Note

Power supply, optics, illumination source, and cables are sold separately.

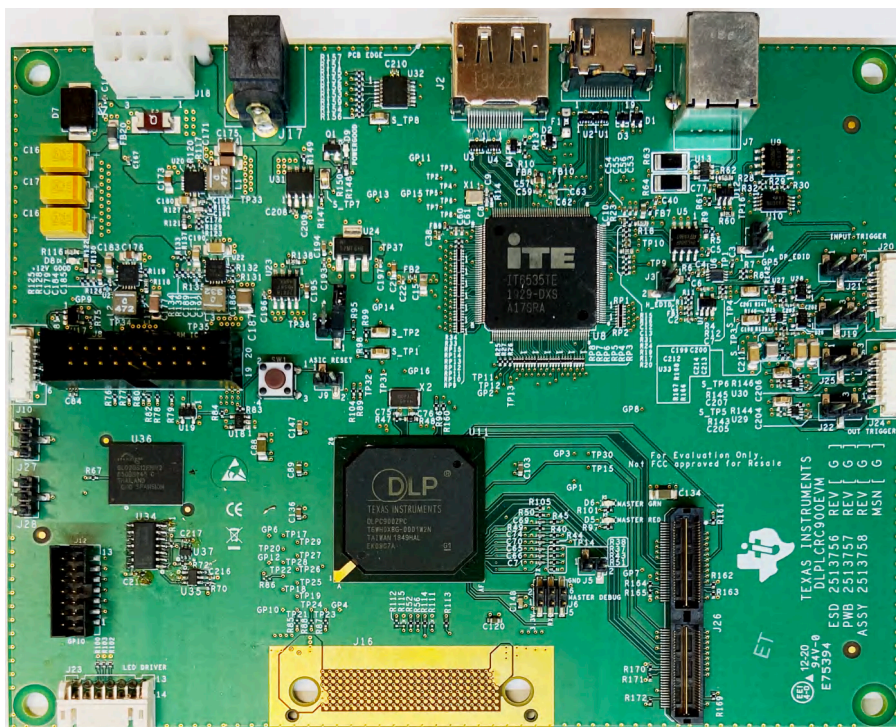


Figure 1-1. DLP LightCrafter Single DLPC900 Evaluation Module



This design incorporates HDMI® technology.

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## Trademarks

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## Related Documentation from Texas Instruments

Component data sheets, technical documents, design documents, and ordering information can be found at the following links:

[DLPC900 Digital Controller Product Folder](#)

[DLPC900 Programmer's Guide](#)

[LightCrafter Single DLPC900 EVM Product Folder](#)

[DLP6500FYE DMD Product Folder](#)

[DLP LightCrafter DLP6500FYE DMD EVM Product Folder](#)

[DLP5500 DMD Product Folder](#)

## If You Need Assistance

Refer to the [TI E2E DLP products forum](#).

# 1 DLP LightCrafter Single DLPC900 EVM Overview

## 1.1 Welcome

The DLP LightCrafter Single DLPC900 EVM, coupled to a supported Single Controller DMD EVM allows for easy evaluation of the DLP LightCrafter subsystem in bringing together high resolution display and advanced pattern control which is well suited for:

- Structured light applications
  - Factory automation and 3D machine vision
  - In-line automated optical 3D inspection
  - Robotic 3D vision
  - Offline 3D metrology
  - 3D scanners
  - 3D identification and biometrics
- 3D printing and additive manufacturing
- Medical and life sciences
- High speed imaging and display

## 1.2 DLP LightCrafter Single DLPC900 Evaluation Module (EVM) Hardware

The DLP LightCrafter Single DLPC900 EVM is one half of a complete DMD imaging electronics subsystem. The DLP LightCrafter Single DLPC900 EVM consists of the single DLPC900 board which includes a single DLPC900 Digital Controller, a digital video receiver, flash memory, power management circuits, and supporting digital logic.

Also needed to complete the imaging subsystem is a DLP LightCrafter Single Controller DMD EVM which is compatible with the LightCrafter Single DLPC900 EVM. A DMD EVM consists of a DMD, a DMD board (pcb) containing on-board DMD power circuits, DMD mounting hardware when necessary, and a flexible (pcb) cable for connections to the Single DLPC900 board.

Figure 1-1 outlines the major hardware components of a DLP LightCrafter Single DLPC900 System Hardware Components.

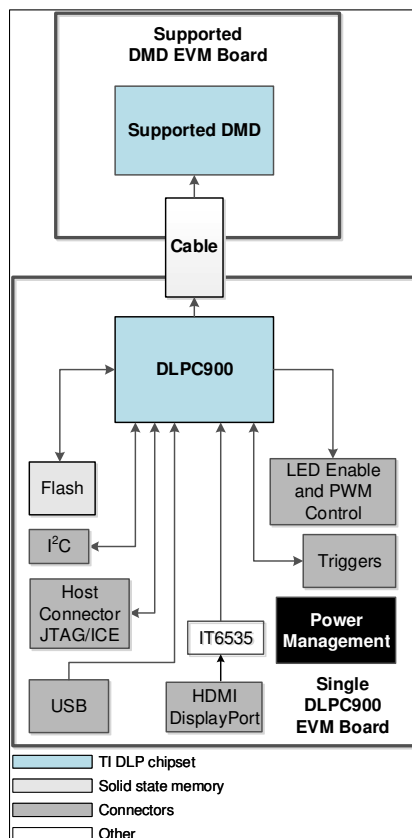
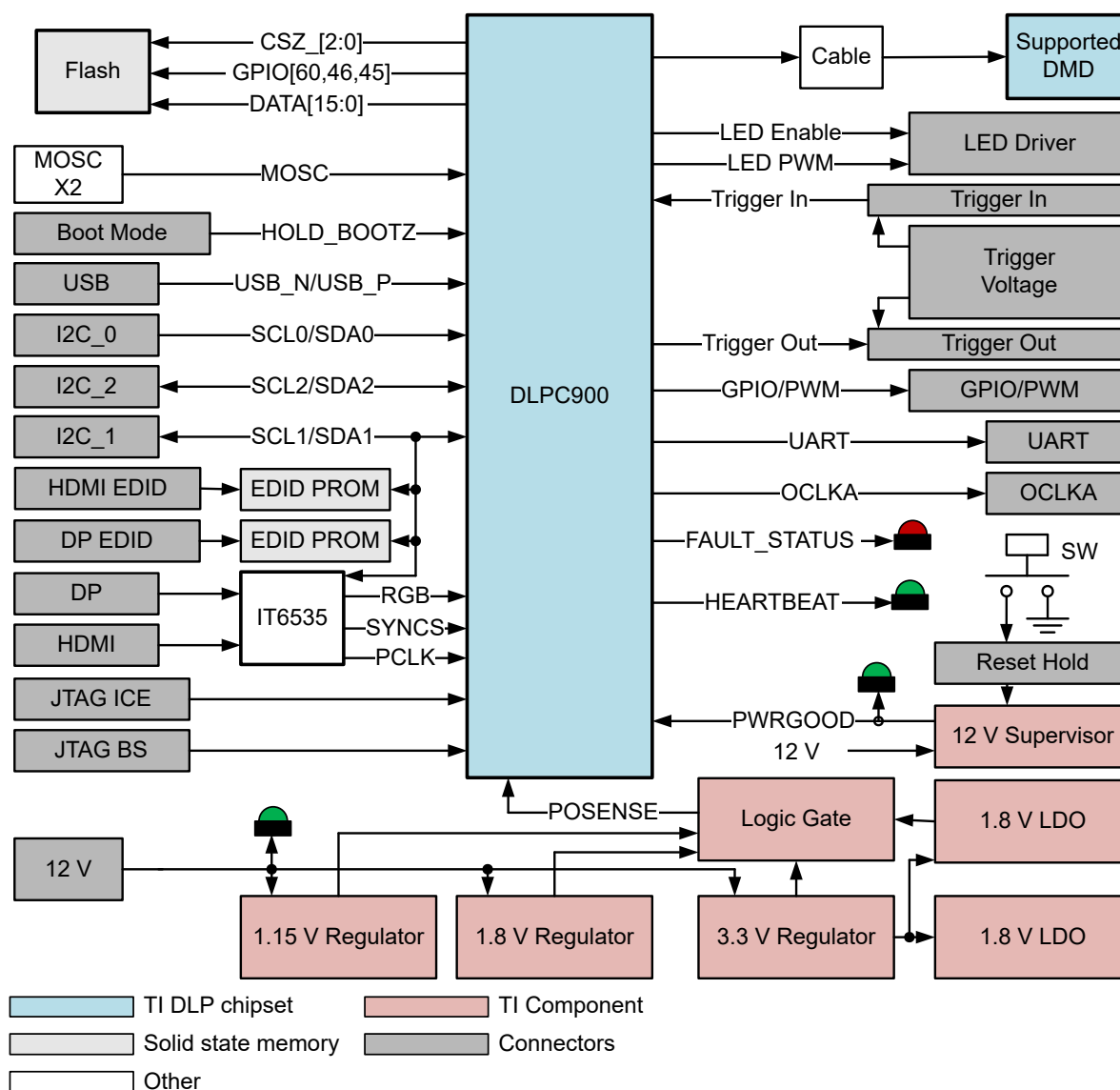


Figure 1-1. DLP LightCrafter Single DLPC900 Hardware Components

## 1.3 EVM Board

The DLP LightCrafter Single DLPC900 EVM contains the electronics capable of controlling supported Single controller DMDs. The EVMs offer several interface options, USB, I<sup>2</sup>C, trigger inputs and outputs, with video inputs through HDMI or Display Port connectors. Figure 1-2 shows the EVM block diagram of the DLP LightCrafter Single DLPC900 EVM.



**Figure 1-2. DLP LightCrafter Single DLPC900 EVM Block Diagram**

The DLP LightCrafter Single DLPC900 EVM major components are:

- A DLPC900 Digital DMD Controller
- Parallel FLASH memory for storing programming and pattern images for the DLPC900
- All necessary power supplies to support the DLP subsystem
- One IT6535 digital receiver for HDMI and Display Port Video Input



## 1.4 Other Items Needed for Operation

The DLP LightCrafter Single DLPC900 EVM is a flexible, ready-to-use evaluation module which, when coupled to one of the DLP LightCrafter DMD EVMs, enables the sending of customer created patterns from the DLPC900 Controllers to the attached DMD for display at the customer determined pattern rates. The DLP LightCrafter Single DLPC900 EVM and the DMD EVMs are offered for purchase separately so that customers can determine which elements to piece together for an application evaluation system.

The following items are not included with the EVM and need to be purchased separately if needed for customer evaluation:

- Power supply - see [Section 5.1 External Power Supply Requirements](#) for details
- USB cable: Type A to Type B USB cable
- HDMI® or DisplayPort™ cable connected to a graphics or video source (if using video input)
- Illumination module or source
- Illumination and projection optics

## 1.5 DLP LightCrafter Single DLPC900 Connections

Figure 1-3 depicts the switches and connectors with the respective locations.

### Note

Power supply (and cable), USB cable, and display cable are NOT included with the module.

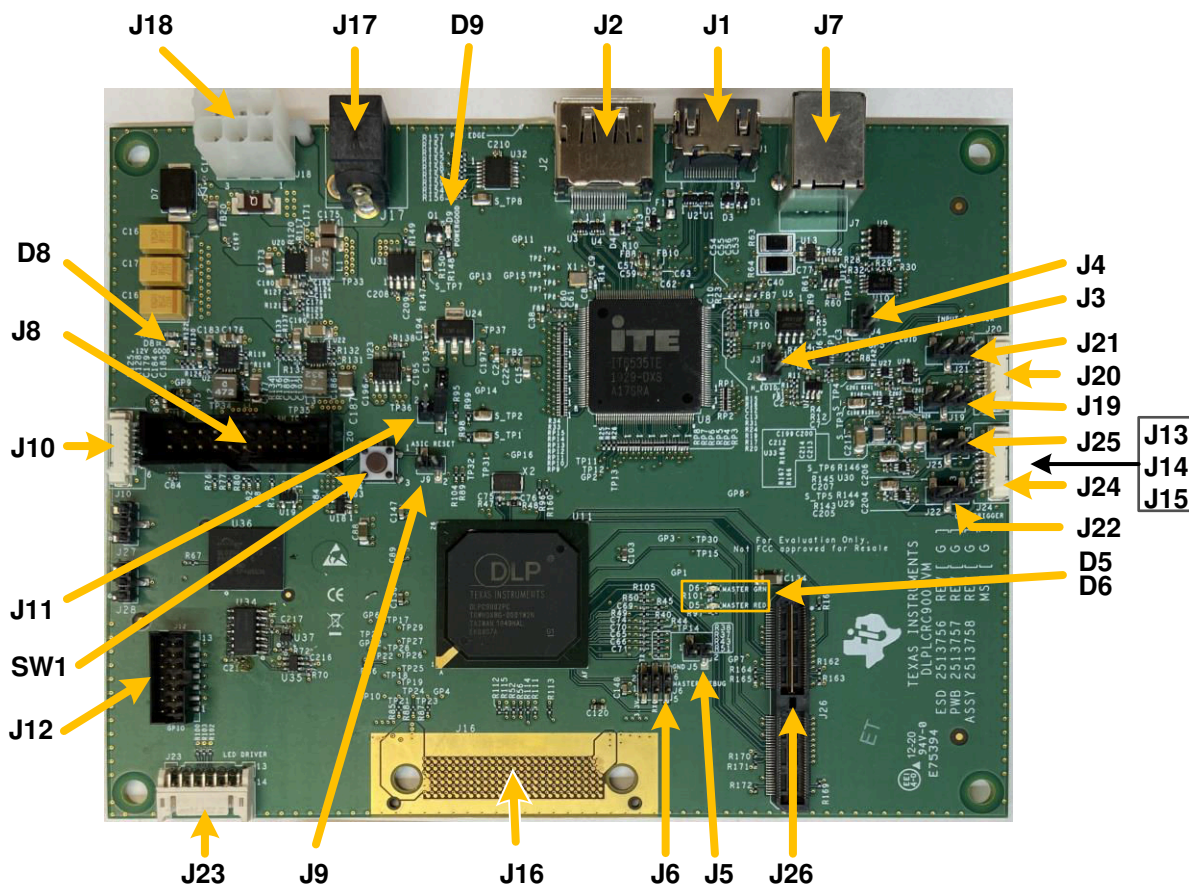


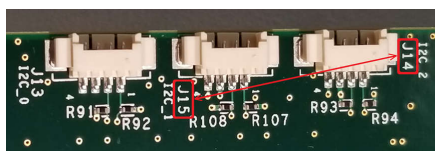
Figure 1-3. DLP LightCrafter Single DLPC900 EVM Connectors (Top View)

**Table 1-1. DLP LightCrafter Single DLPC900 EVM Connector Reference**

| Connector Reference | EVM Function                 | Description or Use                                                                                                                                                                                                                                                                                     |
|---------------------|------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| SW1                 | Hardware Reset Switch        | When pressed resets the DLPC900 controller. When released, the controller boots from reset.                                                                                                                                                                                                            |
| J1                  | HDMI Input                   | HDMI video input (primary).                                                                                                                                                                                                                                                                            |
| J2                  | DisplayPort Input            | DisplayPort video input (secondary).                                                                                                                                                                                                                                                                   |
| J3                  | EDID jumper (HDMI)           | When jumper is installed, the EDID prom for the HDMI interface can be updated.                                                                                                                                                                                                                         |
| J4                  | EDID jumper (DisplayPort)    | When jumper is installed, the EDID prom for the DisplayPort interface can be updated.                                                                                                                                                                                                                  |
| J5                  | OCLKA output                 | User configurable DLPC900 output clock for additional user on board logic. Pin 1 = OCLKA; Pin 2 = GND                                                                                                                                                                                                  |
| J6                  | DLPC900 debug UART           | DLPC900 UART interface for debug messages to a terminal <ul style="list-style-type: none"> <li>Bits per second: 115200</li> <li>Data bits: 8</li> <li>Parity: None</li> <li>Flow control: none</li> <li>Pin 1 = 3.3V</li> <li>Pin 2 = TX out</li> <li>Pin 6 = Ground</li> </ul> LVTTTL compatible only |
| J7                  | Host USB interface           | DLPC900 USB interface for Host communications with DLPC900                                                                                                                                                                                                                                             |
| J8                  | ARM RVI ICE debugger         | TI use only; Used for debugging DLPC900 ARM software code, requires the ARM RVI ICE debugger.                                                                                                                                                                                                          |
| J9                  | DLPC900 Reset jumper         | When jumper is installed, the controller is held in RESET.                                                                                                                                                                                                                                             |
| J10                 | JTAG Boundary Scan           | Used for programming the boot image into the flash memory when flash memory is blank or corrupted.                                                                                                                                                                                                     |
| J11                 | BOOTHold jumper              | When jumper is installed, forces the controller to remain in boot mode when power is applied.                                                                                                                                                                                                          |
| J12                 | GPIO inputs/outputs          | User configurable general purpose Inputs and/or outputs for customer use.                                                                                                                                                                                                                              |
| J13                 | Host I <sup>2</sup> C Port 0 | Host dedicated I <sup>2</sup> C interface port for communications to DLPC900. [Pin 1 = SCL, Pin 2 = SDA, Pin 3 = 3.3V, Pin 4 = GND]                                                                                                                                                                    |
| J14                 | Host I <sup>2</sup> C Port 1 | I <sup>2</sup> C interface port for programming EDID UARTs via I <sup>2</sup> C [ Pin 1 = SCL, Pin 2 = SDA, Pin 3 = 3.3V, Pin 4 = GND]                                                                                                                                                                 |
| J15                 | Host I <sup>2</sup> C Port 2 | I <sup>2</sup> C interface port 2. For TI internal use. [ Pin 1 = SCL, Pin 2 = SDA, Pin 3 = 3.3V, Pin 4 = GND]                                                                                                                                                                                         |

#### Note

On several board lots, the silk screen jumper labels *J14* and *J15* are swapped. Labels *I2C\_1* and *I2C\_2* on all boards are correct. The correct jumper labels are *I2C\_1* is *J14* and *I2C\_2* is *J15*.


**Figure 1-4. Correct J14 and J15 Labels**

**Figure 1-5. Incorrect J14 and J15 Labels**

**Table 1-1. DLP LightCrafter Single DLPC900 EVM Connector Reference (continued)**

| Connector Reference | EVM Function                                  | Description or Use                                                                                                                                                                                                                                                                                                                                                                    |
|---------------------|-----------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| J16                 | DMD flex connector (DLPC900)                  | Flex cable connection for data interface to DMD board and DMD.                                                                                                                                                                                                                                                                                                                        |
| J17                 | +12 VDC Power input                           | EVM power input. [Pin 1 = +12 VDC, Pin 2,3 = GND] See <a href="#">Section 5.1</a> .                                                                                                                                                                                                                                                                                                   |
| J18                 | +12 VDC Power input (Alternate)               | EVM power alternate input. [Pin 1,2,3 = GND, Pin 4,5,6 = +12 VDC] See <a href="#">Section 5.1</a> .                                                                                                                                                                                                                                                                                   |
| J19                 | Trigger Input 1 voltage level selectors.      | Set the voltage levels of the Trigger Input 1 signals. <ul style="list-style-type: none"> <li>Jump across pins 1 to 2 for 3.3V</li> <li>Jump across pins 2 to 3 for 1.8V</li> </ul>                                                                                                                                                                                                   |
| J20                 | External Input Triggers connector             | Trigger input 1 and 2 for triggering the DLPC900 with external input signals (from cameras, processors). See <a href="#">Section 4.1</a> .                                                                                                                                                                                                                                            |
| J21                 | Trigger Input 2 voltage level selectors.      | Set the voltage levels of the trigger input 2 signals. <ul style="list-style-type: none"> <li>Jump across pins 1 to 2 for 3.3V</li> <li>Jump across pins 2 to 3 for 1.8V</li> </ul>                                                                                                                                                                                                   |
| J22                 | Trigger Output 1 voltage level selectors      | Set the voltage levels of the trigger output 1 signals. <ul style="list-style-type: none"> <li>Jump across pins 1 to 2 for 3.3V</li> <li>Jump across pins 2 to 3 for 1.8V</li> </ul>                                                                                                                                                                                                  |
| J23                 | LED illuminator enables and PWM drive signals | <ul style="list-style-type: none"> <li>Pins 1, 2, and 3 are 12V.</li> <li>Pin 5 is Red LED Enable output.</li> <li>Pin 6 is Green LED Enable output.</li> <li>Pin 7 is Blue LED Enable output.</li> <li>Pin 8 is RED PWM output.</li> <li>Pin 9 is Green PWM output.</li> <li>Pin 10 is Blue PWM output.</li> <li>Pin 11 is 3.3V.</li> <li>Pins 12, 13, and 14 are ground.</li> </ul> |
| J24                 | Trigger Output connector                      | Trigger outputs 1 and 2 for triggering external devices. See <a href="#">Section 4.2</a> .                                                                                                                                                                                                                                                                                            |
| J25                 | Trigger Output 2 voltage level selectors      | Set the voltage levels of the Trigger Output 2 signals. <ul style="list-style-type: none"> <li>Jump across pins 1 to 2 for 3.3V</li> <li>Jump across pins 2 to 3 for 1.8V</li> </ul>                                                                                                                                                                                                  |
| J26                 | External Parallel Video Connector             | Used for direct connection to an external parallel video source.                                                                                                                                                                                                                                                                                                                      |
| D5                  | DLPC900 Red fault status LED                  | When lit indicates a fault has occurred in the DLPC900 controller.                                                                                                                                                                                                                                                                                                                    |
| D6                  | DLPC900 Green Heartbeat LED                   | When toggling indicates DLPC900 controller is operating. <div style="border: 1px solid black; padding: 5px; margin-top: 10px;"> <p style="text-align: center;"><b>Note</b></p> <p>The heartbeat operates even if no DMD is present.</p> </div>                                                                                                                                        |
| D8                  | 12V Power LED                                 | When lit indicates external 12V supply is on.                                                                                                                                                                                                                                                                                                                                         |
| D9                  | PWRGOOD LED                                   | When lit indicates power is within the expected limits.                                                                                                                                                                                                                                                                                                                               |

### 1.5.1 DLP LightCrafter Single DLPC900 LED Enable and PWM Outputs

The LED enables on J23 are low-current 3.3V outputs and are not to be used to drive LEDs directly. The enables are to be used as a control to enable a regulator that provides the necessary current to the LEDs. The PWM outputs are also 3.3V outputs and used for controlling the current to the LEDs, which controls the brightness.

#### Note

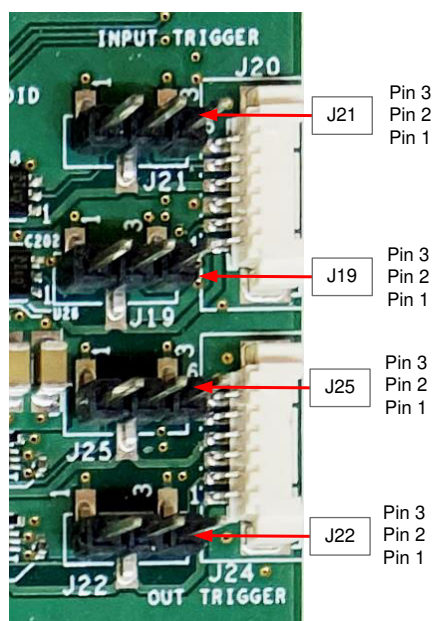
The 12V DC output on J23 is not to be used to power the LED drive circuitry since the output does not provide the necessary current when all three LED enables are simultaneous turned on. Doing so can cause a drop in the 12V supply to the EVM regulator circuitry and reset the controller.

### 1.5.2 DLP LightCrafter Single DLPC900 Trigger Input and Output Voltage Selectors

The trigger inputs on J20 are inputs from external devices to control the pattern sequence. While trigger input 2 is high, trigger input 1 advances the pattern sequence to the next pattern that is waiting for a trigger input. See [Section 3.6.4](#) for more information.

The trigger outputs on J24 are outputs to control external devices. While trigger output 1 frames the exposure time of the pattern, trigger output 2 pulses to mark the beginning of each pattern with a 19.4μs pulse.

Both input and output triggers have voltage level selectors. Use J19, J21, J22, and J25 to select the voltage as shown in [Section 4.1](#) and [Section 4.2](#). If no voltage is selected, then the onboard logic is not functional and any outgoing or incoming signals are not available.

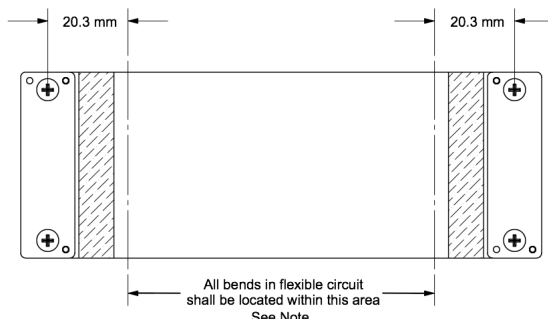


**Figure 1-6. DLP LightCrafter Single DLPC900 EVM Trigger Voltage Level Selectors**

## 1.6 DLP LightCrafter Single DLPC900 EVM Flex Cable

Electrical malfunctions can occur by stressing the flex cable connecting the DMD circuit board to the DLPC900 controller circuit board. Stressing the flex cable can be caused by:

- Bending the cable outside the area identified in [Figure 1-7](#) (within 20.3mm of connector plate centers).
- Repeatedly bending the flex cables where the bend radius is less than 25.4mm.
- A single bending of the flex cables where the bend radius is less than 6.35mm.



**Figure 1-7. Flex Cable Diagram**

### Note

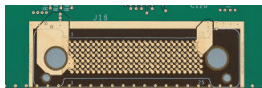
The minimum bend radius for forming flex cable (flexible) circuit is 6.35mm.

Use caution when bending the flex cable to not exceed bending guidelines explained above.

## 1.7 DLP LightCrafter Single DLPC900 EVM and DMD EVM Assembly

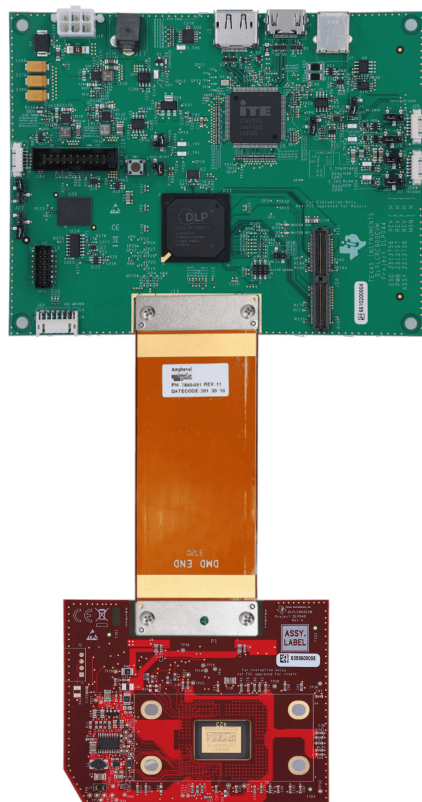
The DLP single DLPC900 EVM requires an appropriate DMD EVM, such as the DLPLCR65EVM.

- The flex cable provided with the DMD EVM has one end marked *DMD End* with key pins arranged diagonally that only fit one way onto the DMD board preventing attachment to the wrong side of the board. Place the insulated backer plate on the other side of the DMD board from the flex side with connection pins and carefully tighten the screws. Alternate tightening the screws from side to side until the screws are firm, but not overtightened (SCREW TORQUE REQUIREMENT: 6-8 in-lb [0.7-0.9 N·m]). [Figure 1-9](#) shows the flex cable DMD ends correctly assembled to an example DMD board.
- The other end of the flex cable attaches to the single DLPC900 controller board. The key pins are arranged laterally symmetric and are able to be inserted from either side of the controller board. To verify the cables are connected to the correct side of the single DLPC900 controller board, check that the connection flex pins are facing the pads shown in [Figure 1-8](#). Place the insulated backer plate on the other side of the controller board from the flex side with pins and carefully tighten the screws. Alternate tightening the screws from side to side until the screws are firm, but not overtightened (SCREW TORQUE REQUIREMENT: 6-8 IN\*LB [0.7-0.9 N\*m]). [Figure 1-9](#) shows the flex cable ends correctly assembled to the single DLPC900 controller board.



**Figure 1-8. DLPLCR900EVM Flex Cable Pad**





**Figure 1-9. Assembled DLPLC900EVM with DLPLCR65EVM**

## 2 Quick Start

This chapter offers a quick start guide to power-up the EVM, run the GUI control software, and create a simple three pattern sequence.

---

### Note

Be sure to install DLP LightCrafter GUI software version 5.0 and firmware 6.0 or newer. Operating the DLP LightCrafter Single DLPC900 EVM (DLPLCRC900EVM) with prior revisions of the GUI can render the EVM inoperable.

For DLP5500 DMD operation (DLPLCR55EVM) GUI version 5.2.x and firmware version 6.2.0 or newer is required.

---



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### Note

If the text in the GUI does not display correctly, then see [Section 3.2](#).

---

### 2.1 Power-up the DLP LightCrafter Single DLPC900 EVM

The DLP LightCrafter Single DLPC900 EVM is ready to use, out of the box, after assembling with a supported Single Controller DMD EVM. Steps 1 through 6 show how to power, display an image, and connect the EVM to a PC.

1. Connect a 12V DC power supply to the power supply connector J17 in [Figure 1-3](#).
  2. LEDs — D8 and D9 light up green to indicate configuration and power is within the expected limits.
  3. LEDs — D6 toggles on and off, indicating the DLPC900 controller is operating.
  4. After 5 seconds, the DLPC900 device starts sending a built-in rotating pattern sequence of images to the DMD for display on the DMD.
  5. Connect a USB cable from a PC to connector J7 on the DLP LightCrafter Single DLPC900 EVM, as seen in [Figure 1-3](#). The first time the cable is connected on a PC, the DLP LightCrafter Single DLPC900 EVM enumerates as a USB composite device with human-interface device (HID) class. No drivers are required since these are natively handled by the Windows operating system.
  6. The DLP LightCrafter Single DLPC900 EVM can be controlled with the GUI software and firmware versions that are available for download from [DLPC900REF-SW](#).
- 

### Note

The DLPC900 does not have a dedicated INIT\_DONE signal output to indicate that the completion of power-up initialization and is ready to accept commands. The user can configure one of the nine GPIOs available as an INIT\_DONE signal output simply by adding the GPIO configuration into a default batch file that is executed at power-up. A 10kΩ pull-down resistor must be connected to the GPIO to be used.

The following is an example of adding the configuration for GPIO\_08 to a batch file, where GPIO\_08 is configured as an output and the signal is set high. When this command is added to the top of the batch file, the GPIO output goes high in approximately 800ms from the time POSENSE goes high.

GPIO\_CONFIG: 0x8 0x3

---

## 2.2 Creating a Simple Pattern Sequence

Before creating a pattern sequence, make sure to follow the steps in [Section 2.1](#). Download and execute the latest [DLPC900REF-SW](#) Windows-Installer, and then start the DLP LightCrafter DLPC900 GUI application. The GUI detects the attached EVM, and the *Connected* radio-button lights up **Green** indicating the connection was successful. The GUI software includes sample images for the DLP LightCrafter Single DLPC900 EVM that are used in the examples in this guide. If the sample image files have not been unzipped during the installation process, then within the GUI install directory, unzip the image files to gain access to all the sample images. All images are 1-bit binary patterns and have the native resolution of the attached DMD.

Follow these steps to create a pattern sequence. See [Figure 3-7](#) to identify the buttons on the Menu bar.

1. Select *Pattern On-The-Fly Mode* from the *Operating Mode* group box, and click the *Pattern Design* button so that the panel is displayed as shown in [Figure 2-1](#).
2. Click the *Add Pattern* button (with the plus sign) in the *Menu* bar and browse for any three bitmap images from the sample of images. The default directory location is C:\Texas Instruments-DLP\DLPC900REF-SW-5.x\DLPC900REF-GUI\Images and Batch files\[DMD]\_Images. Make sure to select them from the correct image sub-folder for the EVM that is being used. All three images can be selected at the same time within the open file dialog window, but do not appear in the same order in the GUI as in the dialog window.

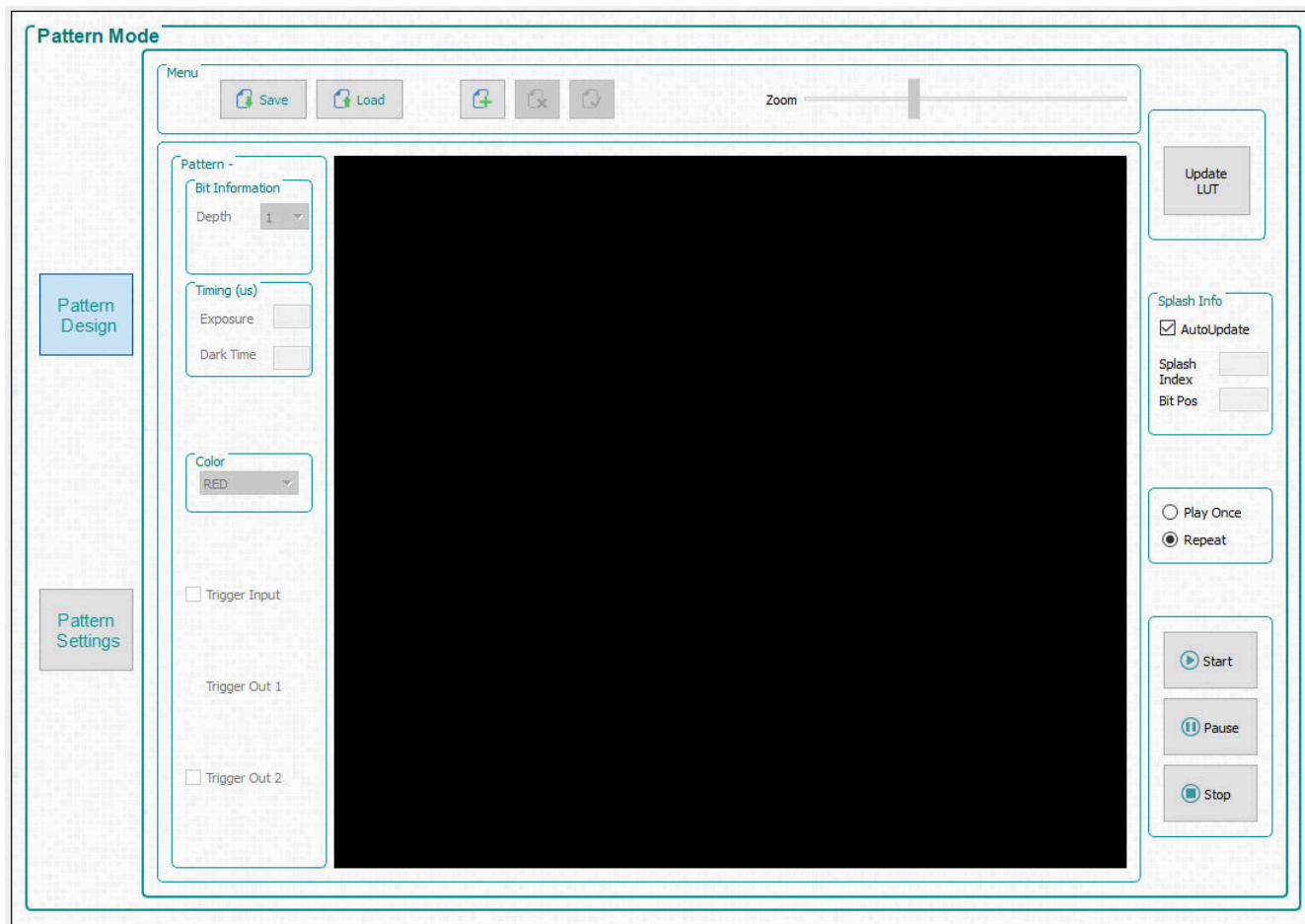
---

### Note

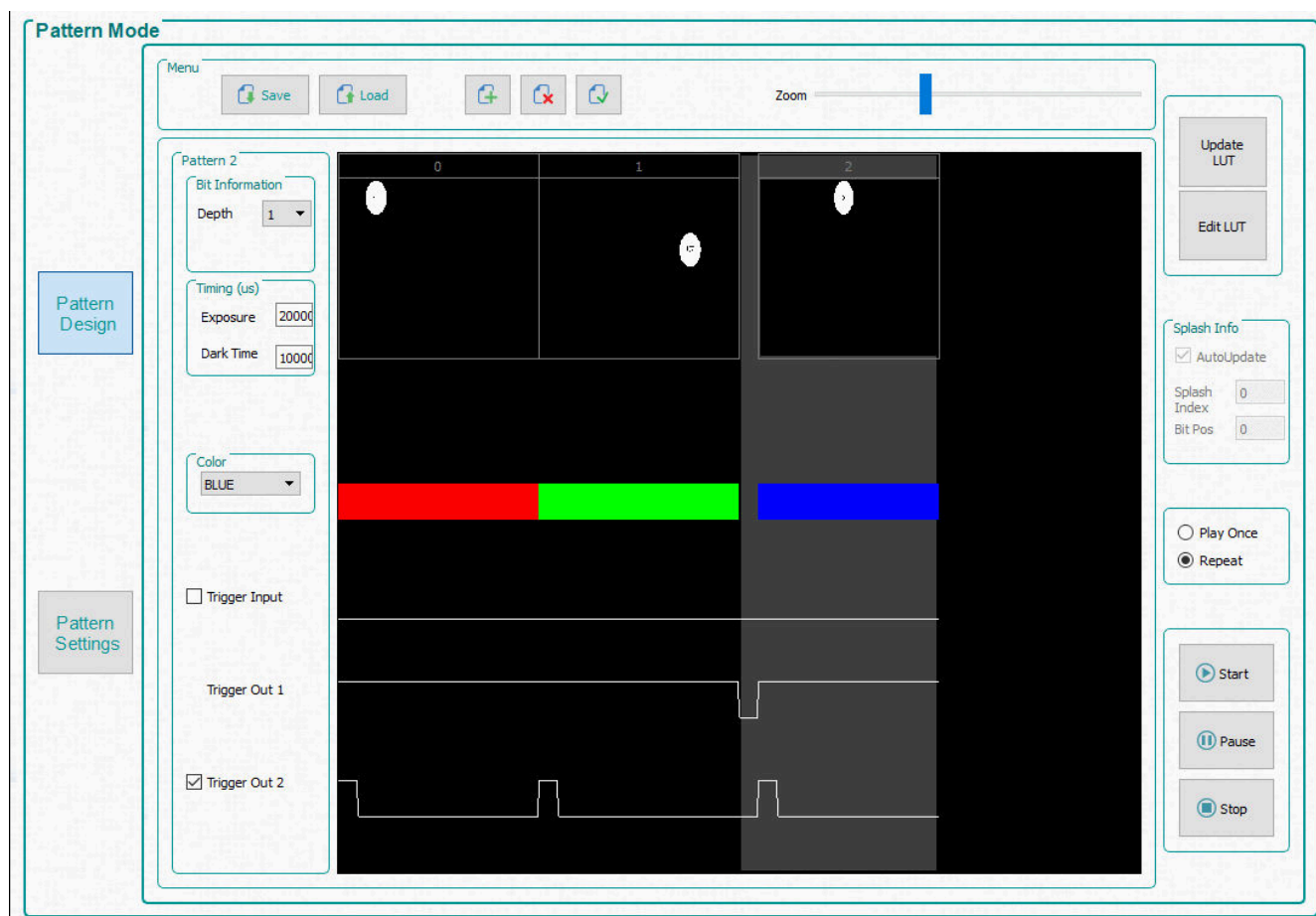
For the next three steps a selected pattern is indicated by a lighter gray selection area. See [Figure 2-2](#) which shows the third pattern (pattern 2) selected.

3. Select the first pattern (pattern 0) within the *Pattern Design* panel and choose a bit depth of 1. Set exposure to 100000 $\mu$ s, dark time to 50000 $\mu$ s, and select **Red** for the color.
4. Select the second pattern (pattern 1) and choose a bit depth of 1. Set exposure to 150000 $\mu$ s, dark time to 75000 $\mu$ s, and select **Green** for the color.
5. Select the third pattern (pattern 2) and choose a bit depth of 1. Set exposure to 200000 $\mu$ s, dark time to 100000 $\mu$ s, and select **Blue** for the color. [Figure 2-2](#) shows the pattern sequence.
6. Select the *Repeat* radio-button to continuously repeat the pattern sequence.
7. Click the *Update LUT* button to upload the pattern sequence definition including the three pattern images to the EVM.
8. Click the *Start* button to run the pattern sequence. The three selected patterns show for the timings entered in steps 3, 4, and 5 and repeat until Step 9 is completed.
9. Click the *Stop* button to stop the pattern sequence.





**Figure 2-1. Pattern Mode Panel**



**Figure 2-2. Simple Three Pattern Sequence**

## 3 Operating the DLP LightCrafter Single DLPC900 EVM

This chapter introduces the Windows software provided with the DLP LightCrafter Single DLPC900 EVM.

### 3.1 DLP LightCrafter Single DLPC900 Control Software

The [DLPC900REF-SW](#) bundle includes a GUI application to control the modules through the USB interface. QT is a cross-platform application and user-interface framework with open source and commercial licenses available. Install the QT-based GUI download and execute the latest [DLPC900REF-SW](#) Windows-Installer, and then start the DLP LightCrafter DLPC900 GUI application.

The following items are found in DLPC900REF-SW:

- DLPC900REF-GUI – The software bundle includes the executable files for the PC-based Graphical User Interface (GUI) for easy communication over USB with the DLPC900 controller. The GUI was developed using the QT Library and QT Creator. The GUI source code is provided so that the user can customize as required or use as a reference to create a DLPC900 controller software interface.
- DLPC900REF-JTAG – The JTAG Flash Programmer gives developers the ability to load the DLPC900 configuration firmware into flash memory attached to a DLPC900 controller. JTAG boundary scan programming allows developers to install the DLPC900 firmware in the absence of standard USB connectivity. For example, the JTAG Flash Programmer is used if the bootloader becomes corrupted and the board is rendered inoperable.
- DLPC900REF-EDID – EDID files which can be programmed to the DLPLC900EVM or DLPLC900DEVM board according to the DMD the DLPC900 is controlling.
- DLPC900-C900REF-FPGA – This binary file is used to configure the FPGA on the DLPLC900DEVM, to enable the display of 24-bit RGB data with dual DLPC900 controllers.

The DLP LightCrafter Single DLPC900 EVM supports two basic types of operation:

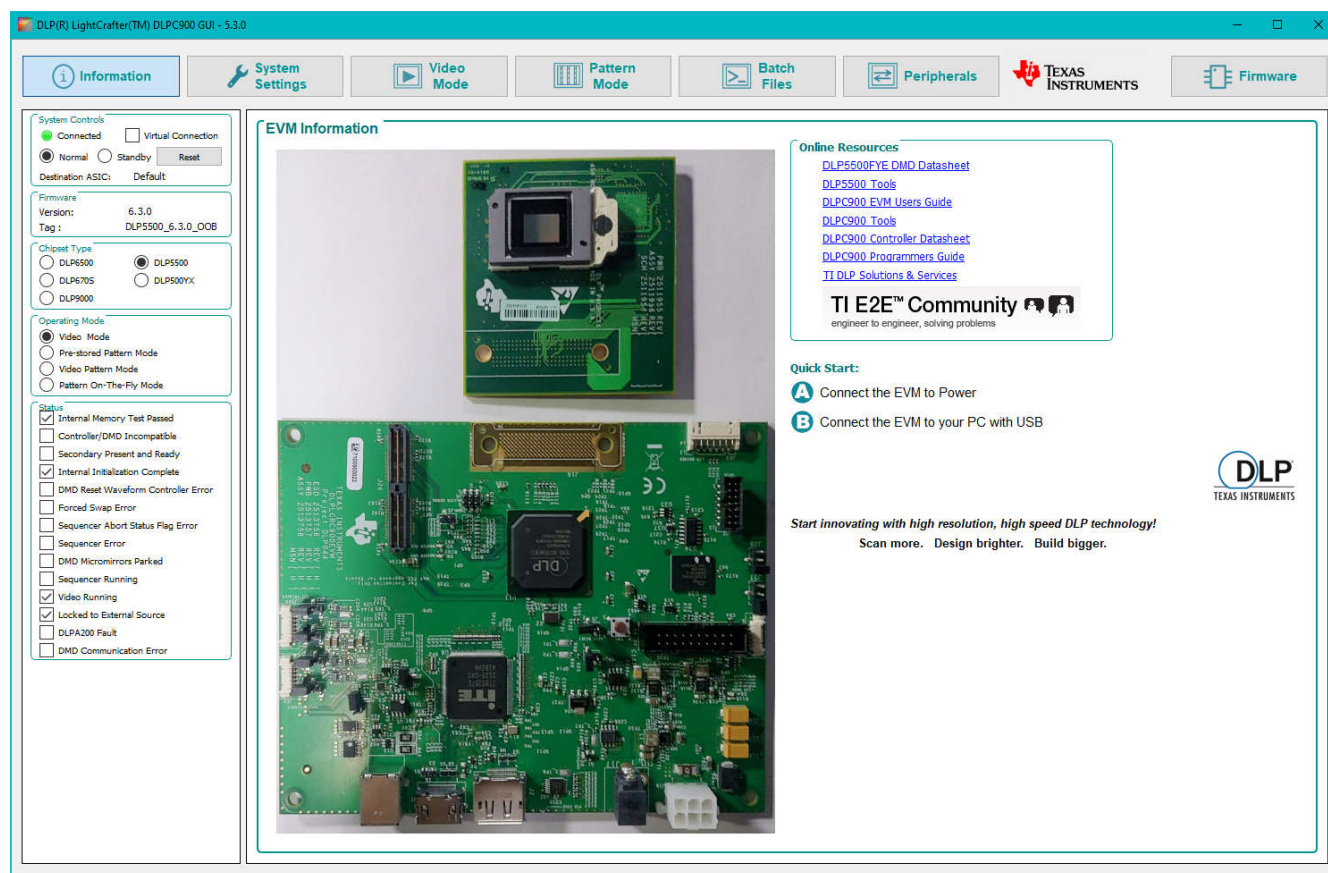
- Video Mode displays images from:
  - HDMI and DisplayPort inputs.
  - 24-bit RGB customer provided bitmaps stored in flash memory.
  - Internal test pattern generator with ten user-selectable patterns.
  - Solid Full-screen Curtain with user-selectable colors.
- Pattern Sequence displays images from:
  - Pre-stored Pattern Mode: 1-, 2-, 3-, 4-, 5-, 6-, 7-, and 8-bit user-defined bitmap images pre-stored in Flash memory.
  - Video Pattern Mode: 1-, 2-, 3-, 4-, 5-, 6-, 7-, and 8-bit user-defined bitmap images streamed through the DLPC900 24-bit RGB interface.
  - Pattern On-the-Fly Mode: 1-, 2-, 3-, 4-, 5-, 6-, 7-, and 8-bit user-defined bitmap images dynamically loaded into internal memory via USB or I<sup>2</sup>C interfaces.

## 3.2 PC Software

Upon execution of the DLP LightCrafter DLPC900 GUI application, the panel shown in [Figure 3-1](#) is displayed. The GUI panel contains the following three sections:

- System common controls and status on the left.
- System feature controls buttons on the top.
- Feature control panels in the center.

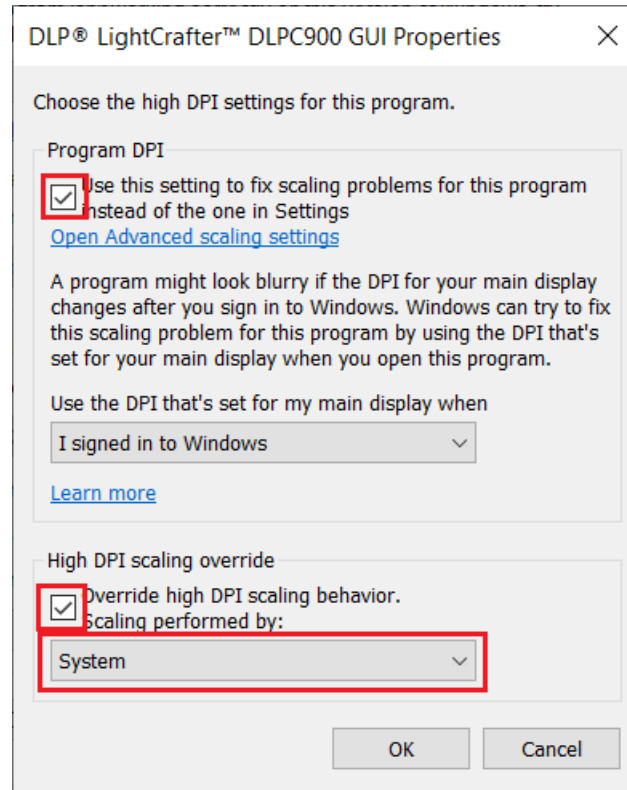
In any of the GUI sections, clicking a *Get* button reads the current settings of that particular subsection. Clicking the *Set* button programs the settings into the respective subsection. Note that some commands can require additional steps before the GUI display is updated.



**Figure 3-1. DLP LightCrafter DLPC900 GUI**

### Note

The GUI looks best when scaling is set to 100%. If the text in the GUI does not display correctly, then change the DPI settings. Right click on the executable and select *Properties*. On the *Compatibility* tab select the *Change high DPI settings* button. Change your settings as shown in Figure 3-2.



**Figure 3-2. High DPI Settings**

## 3.3 System Common Controls

The DLP LightCrafter Single DLPC900 EVM communicates with the DLPC900 using USB 1.1. The DLPC900 enumerates as a USB device with HID Support. The PC polls all the HID peripherals and once the PC detects the DLPC900, the *Connected* radio-button changes to **green**. If the USB cable is disconnected, the *Connected* radio-button changes to **red**. Once the *Connected* radio-button shows **green**, the firmware *Version* and the *Tag* name are displayed.

The *Virtual Connection* option is useful when there is no connection to the EVM. When the *Virtual Connection* is checked, all commands continue to operate as if there was a connection to the EVM except for firmware uploads, updating the Look Up Table (LUT), and get commands.

### 3.3.1 Operating Mode

Within the *System Controls* group box, the GUI updates the state of the EVM to indicate the EVM is in *Normal Mode* or *Standby Mode*. The user can command the EVM into *Standby Mode* if the EVM is not being used or the *Normal Mode* can be selected to command the EVM to wake up. The user can click the *Reset* button to command the EVM to perform a software reset.

#### Note

When in Standby Mode, the GUI suspends status updates until a return to operation or software reset is issued. If a hardware reset is asserted, the GUI must be restarted to recognize return to operation.

Standby mode requires a wait period of two minutes to run 50/50 (alternating pixels on/off at 50% duty cycle) and park the DMD. Three additional seconds are needed to complete all operations before issuing a Return to Normal Mode command.

Once Standby has been issued, the only command accepted by the DLPC900 controller board is Return to Normal Mode (0). **Any other command can result in unexpected behavior.** In particular, a Park/Unpark is not accepted until Normal mode is resumed. Using the isolated Park/Unpark command is no longer recommended except for debugging purposes.

The EVM can be commanded to enter one of the four operating modes. Within the *Operating Mode* group box there are four choices:

1. *Video Mode* – This mode is primarily used for display applications, and is not recommended for applications which require pixel and timing accuracy. In this mode, the user can choose from the four following video modes:
  - Parallel RGB interface
  - Internal test pattern generator with ten test pattern options
  - Pattern image display from flash memory
  - Solid Curtain with choice of color
2. *Pre-stored Pattern Mode* – In this mode, the user can create a pattern sequence using images stored in flash.
3. *Video Pattern Mode* – In this mode, the user can define a pattern sequence using pattern data or video which is streamed using a parallel RGB interface.
4. *Pattern On-The-Fly Mode* – In this mode, the user can create a pattern sequence using bitmap images which are loaded into the internal memory of the DLPC900 via the USB or I<sup>2</sup>C interfaces. This mode can be helpful to view a pattern sequence before storing the images in flash memory, as updating the flash to use *Pattern Mode* can be a timely process.

### 3.3.2 Connected DMD Type

The GUI queries the DLP LightCrafter Single DLPC900 EVM to determine the DMD type the firmware is programmed for and is expecting to be connected to the DLP LightCrafter Single DLPC900 EVM. The indicators in the *Chipset Type* group box are updated to show which DLPC900 compatible DMDs is connected.

### 3.3.3 EVM Information

The *Introduction Main Page* shows an image of the EVM currently connected and helpful links to online resources.



### 3.3.4 Status

The *Status* panel indicators show the current state of the DLPC900. When any of the boxes are checked, this indicates the stated text has occurred. Any boxes that state an error and remain checked must be corrected before continuing to operate the EVM.

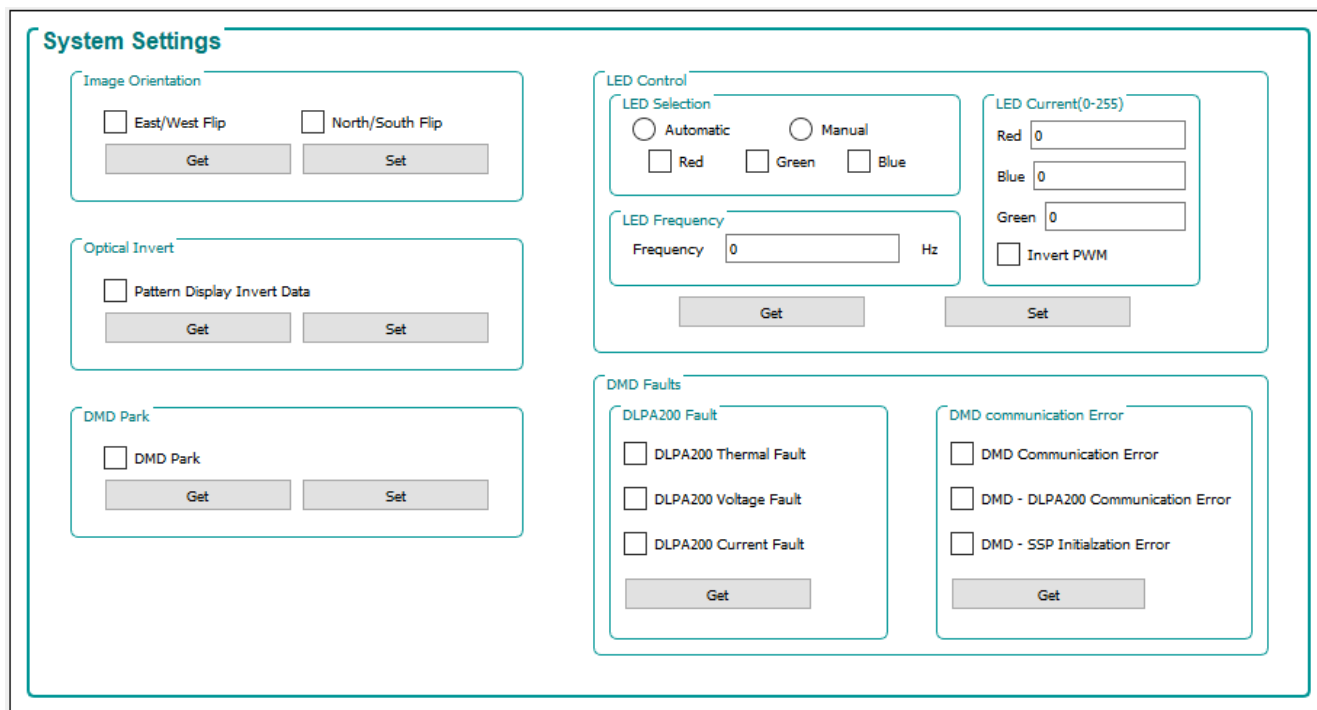
The following is a description of each status indicator:

- *Internal Memory Test Passed* – The DLPC900 performs an internal system memory test at power-up. The box is checked if the test passed.
- *Controller/DMD Incompatible* – The box is checked if either the controller or DMD are incompatible with the firmware including loading incorrect firmware or an issue with communication between the controller and DMD. Check for possible damage to the DMD SCP lines, or damage to the flex cable. Also verify that the DMD is sitting properly in the socket, and is not missing or damaged.
- *Secondary Present and Ready* – The box is checked when the GUI is connected to a DLP LightCrafter Dual DLPC900 EVM. If the GUI is connected to a DLP LightCrafter Single DLPC900 EVM and the box is checked then the DLPC900 is potentially malfunctioning or was incorrectly programmed with one of the Dual DLPC900 firmware files.
- *Internal Initialization Complete* – The box is checked when all power-up initialization routines have completed and passed.
- *DMD Reset Waveform Controller Error* – The box is checked whenever the DMD Reset Controller has found multiple overlapping Vbias or reset operations accessing the same DMD block of micromirrors.
- *Forced Buffer Swap* – The box is checked whenever a forced buffer swap occurs. This error can occur if the DLP LightCrafter Single DLPC900 EVM is set to Video Mode and the vertical back-porch timing is too small. The error can also occur if the DLP LightCrafter Single DLPC900 EVM is set to Video Pattern Mode where the patterns are from the video port and the pattern sequence timings do not match the video port VSYNC - specifically the cumulative exposure times are greater than the frame time. Forced buffer swaps can also occur during any of the pattern modes in preparing a pattern sequence and the timing of a status read. There is a possibility that the user needs to perform additional reads of the status to get a correct state of this indicator. If the box continues to be checked, then check for an error condition in the pattern sequence.
- *Sequence Abort Status Flag* – The box is checked whenever a sequence abort has occurred. Sequence abort can occur during any pattern mode in preparing a pattern sequence and the timing of reading the status. Additional status reads can be needed to get a correct state of this indicator. If the box continues to be checked, then check for an error condition in the pattern sequence. Also check that the cumulative exposure time is not greater than the frame time.
- *Sequence Error* – The box is checked whenever a sequence error has occurred. Sequence errors can occur during any of the pattern modes in preparing a pattern sequence and the timing of reading the status. The user can need to perform additional reads of the status to get a correct state of this indicator. If the box continues to be checked, then check for an error condition in the pattern sequence.
- *DMD Micromirrors Parked* – The box is checked whenever the DMD micromirrors are parked.
- *Sequencer Running* – The box is checked whenever the sequencer is running.
- *Video Running* – The box is checked whenever video is running.
- *Locked to External Source* – The box is checked to indicate that the EVM has *locked* to an external parallel RGB video source. If the EVM loses the locked signal at any time, the box becomes unchecked. A locked source must be present to switch to Video Pattern Mode.
- *DLPA200 Fault* – The box is checked whenever a [DLPA200](#) fault has occurred. The box is active when a DLP5500 DMD is connected.
- *DMD Communication error* – The box is checked if the controller is unable communicate with the attached DMD.



### 3.4 System Settings

Click the *System Settings* button at the top of the GUI to display the *System Settings* panel shown in [Figure 3-3](#).



**Figure 3-3. System Settings Panel**

Within the *System Settings* panel, the user can control the orientation of the image, the LEDs, and the optical inversion.

- **Image Orientation** – Depending on the orientation of the projected image, the image can require to be flipped on the short or long axis. The *East/West Flip* setting causes the image to be flipped on the long axis. The *North/South Flip* setting causes the image to be flipped on the short axis. This setting must be set before any Pattern LUT updates are sent to the DLPC900 and the pattern sequence must be in the stopped state.
  - The East/West setting causes the image to be flipped on the long axis. This setting must be set before any Pattern LUT updates are sent to the DLPC900 and the pattern sequence must be in the stopped state.
  - The North/South setting causes the image to be flipped on the short axis. This setting must be set before any Pattern LUT updates are sent to the DLPC900 and the pattern sequence must be in the stopped state.
- **LED Controls** – The LED controls allow the user to manually control the state of the LEDs or allow the internal DLPC900 sequencer to control.

#### Note

If the LEDs are disabled in manual control, then the LEDs must be enabled before switching to automatic control (internal DLPC900 sequencer).

The LED current regulates the brightness of the LEDs. Setting the Invert PWM causes the LED currents to have an opposite effect on the LEDs as the current is changed. When manipulating the LED frequency and current, TI recommends to think of the led current value from 0-255 (0.39% per step) as the duty cycle where 255 equals approximately 100% (99.61%) Internally the software then calculates the PWM value depending on the set frequency and duty cycle. Setting the Invert PWM changes the polarity of the PWM signal which causes the LED currents to have an opposite effect on the LEDs as the current is changed.

#### Note

DLP LightCrafter Single DLPC900 EVMs do not come with LEDs or optical engines of any kind.

- **Pattern Display Invert Data** – This setting allows the user to invert the data to the DMD. The setting must be set before any Pattern LUT updates are sent to the DLPC900 and the pattern sequence must be in the stopped state. This setting is not applicable to *Video Mode*.
- **DMD Park** – This setting allows the user to park and un-park the DMD. Using the isolated Park/Un-park command is no longer recommended except for debugging purposes. If the Display Mode is set to pre-stored pattern mode, or Video Pattern mode or Pattern On-The-Fly mode, then the pattern sequence must be stopped prior to execution of this command.
- **DMD Faults:**
  - **DLPA200 Fault** - Only applies to the DLP5500 DMD. The user can read the status of the DLPA200, displayed as a checked box for any of the following: thermal fault, voltage fault, and current fault.
  - **DMD Communication Error** - Get the status result of the controller query of the DMD type (typically only queried during initialization).

### 3.5 Video Mode

Click the *Video Mode* button at the top of the GUI to display the *Video Mode* panel. Within this panel, there are two tabs:

The Source Settings Tab is shown in [Figure 3-4](#).

The Display Settings Tab is shown in [Figure 3-5](#).

**Video Mode**

**Source Settings**

**Input Configuration**

Source Select: Parallel RGB (dropdown), 24 BITS (dropdown)

Get Set

**Input Port Data Swap**

Port 1 (dropdown), ABC->ABC (dropdown)

Get Set

**Pixel Data Format**

RGB 444 (dropdown)

Get Set

**IT6535 Receiver**

☐ Power Down ☐ HDMI ☐ Display Port

Get Set

**Display Settings**

**Parallel Port Configuration**

Total Pixels Per Line (input field)

Total Lines Per Frame (input field)

Active Pixels Per Line (input field)

Active Lines Per Frame (input field)

Active Area First Pixel (input field)

Active Area First Line (input field)

Bottom Field First Line (input field)

Pixel Clock (KHz) (input field)

Get

**Port Config Controls**

Pixel Mode: Single Pixel (dropdown)

Data Port: Port 1 (dropdown) Pixel Clock: Clock 1 (dropdown)

Data Enable: Port 1 (dropdown) VSync & HSync: Port 1 (dropdown)

Get Set

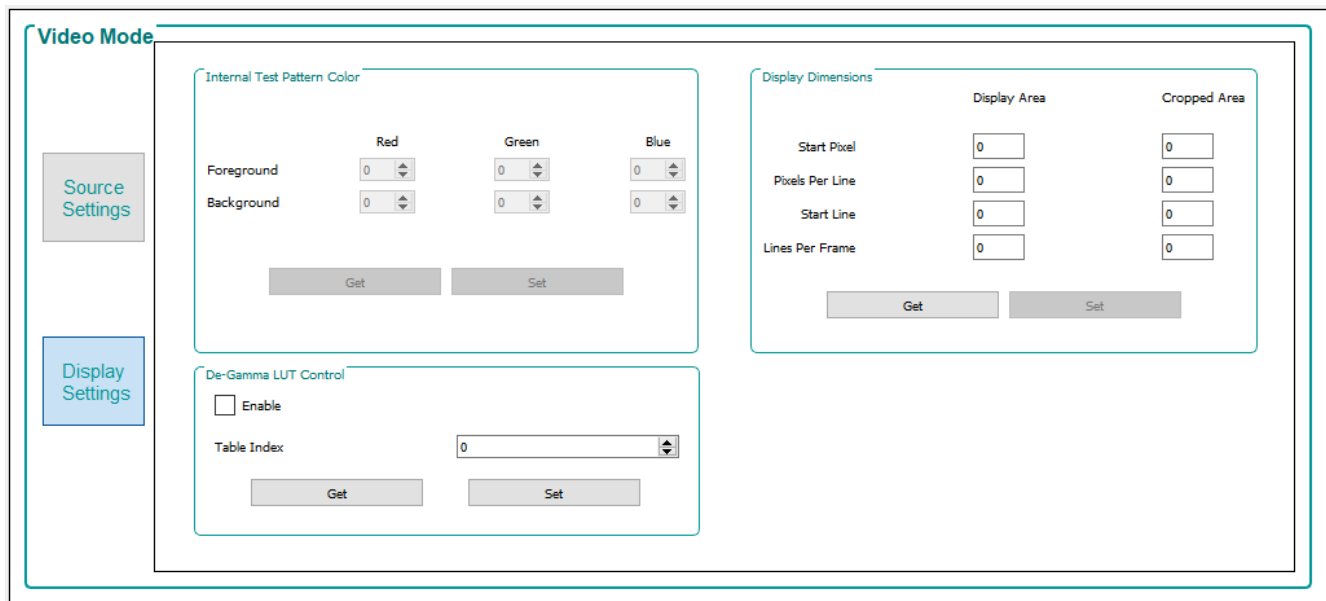
**Figure 3-4. Video Mode Panel - Source Settings Tab**

1. **Input Configuration.**
  - **Source Select** – Allows the user to select :
    - a. Parallel RGB interface
    - b. Internal test pattern generator
    - c. Pattern images from flash memory
    - d. Solid Curtain– which displays a constant, solid color image (including black or white)
  - **Input Port Data Swap** – Depending on the routing of the parallel RGB data lines, users need to swap the order of the color channels. The DLP LightCrafter Single DLPC900 EVM requires an RGB → GRB setting. The user selects which port to apply the settings.
  - **Pixel Data Format** – Allows the user to select the video format of the input source.
2. **Port Config Controls** – Depending on which input signals were chosen during board design, users need to select the appropriate signals so that the DLPC900 can properly detect the incoming video source. If incorrect settings are chosen, then the curtain can be displayed or the image can be incorrect. **Pixel Mode** can also be set to Single Pixel or Dual Pixel. Dual Pixel allows for higher data rates by loading two pixels per clock instead of one pixel per clock.

#### Note

Only Pixel Clock 1 is connected on the DLPLC900EVM. Selecting Pixel Clock 2 or 3 on the EVM results in erratic behavior.

3. **IT6535 Receiver** – This control allows selection between the HDMI or the DisplayPort input connectors of the IT6535 digital receiver. The digital receiver can also be powered-down, which tri-states all the output signals of the IT6535 to allow another device to share the input ports and syncs of the DLPC900.
4. **Parallel Port Configuration** - Allows readback of the current parallel port configuration parameters determined by the EDID or user Display Dimensions. Values are read only from the primary controller.



**Figure 3-5. Video Mode Panel - Display Settings Tab**

1. **Internal Test Pattern Color** – When the internal test pattern is selected as the input source, the foreground and background colors can be changed.
2. **Display Dimensions** – Allows the user to scale or crop the image of the incoming video source.
3. **De-Gamma LUT Control** – De-Gamma controls:
  - Enable/Disable - Command enables or disables De-Gamma
  - Sets the level of De-gamma - See the [Gamma Configuration and Enable](#) section of the [DLPC900 Programmer's Guide](#) for further information

### 3.5.1 Video Support

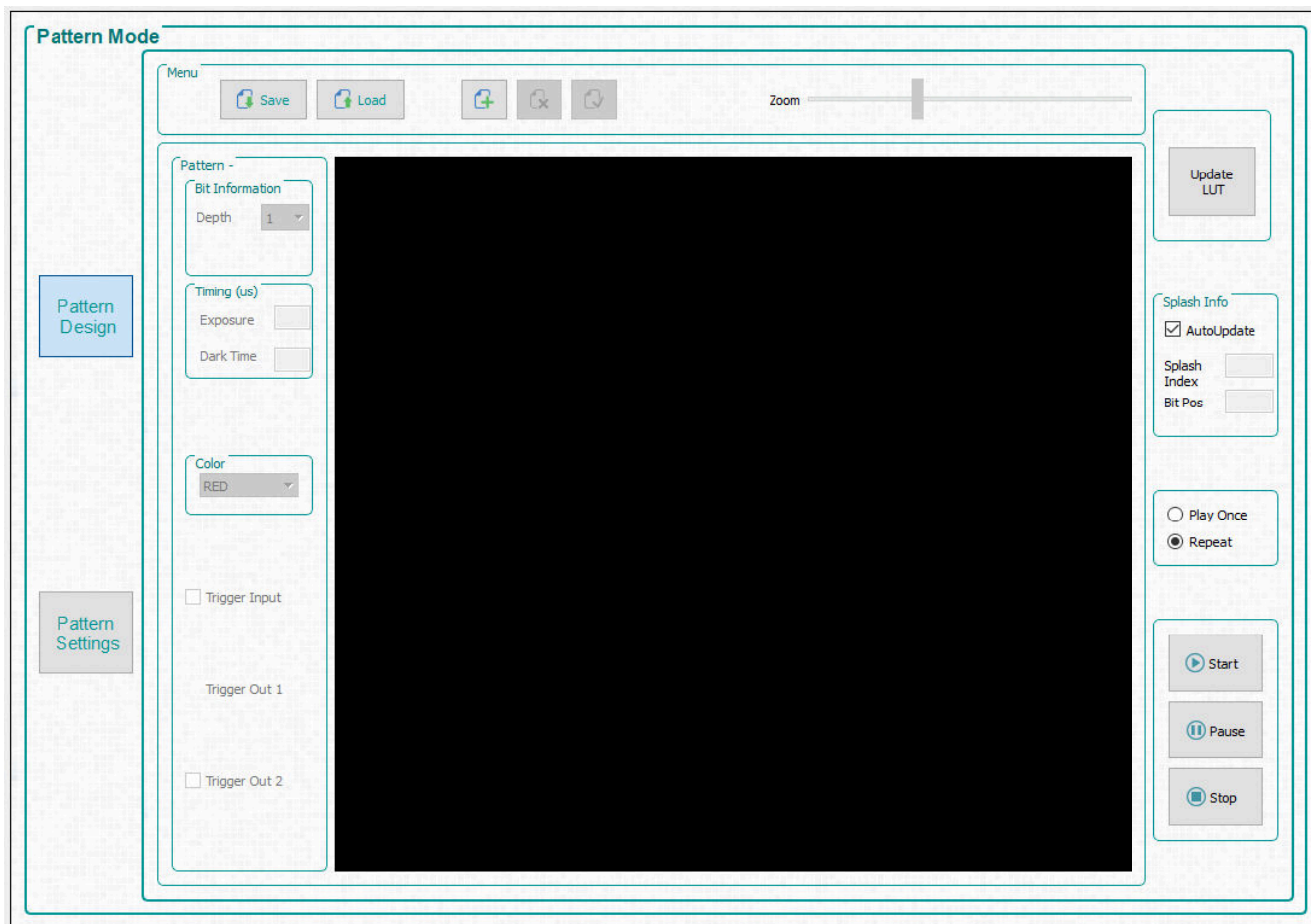
Please refer to the DMD data sheet of the DMD connected to the DLP LightCrafter Single DLPC900 EVM for supported video frame rates.

### 3.6 Pattern Modes

Click the *Pattern Mode* button at the top of the GUI to display the *Pattern Mode* panel as shown in [Figure 3-6](#).

The *Pattern Mode* panel allows the user to create a pattern sequence. First the user must choose the operating mode by selecting which of the three pattern modes to use. The three pattern modes are:

1. **Pre-Stored Pattern Mode.** In this mode the patterns are pre-stored in flash memory and loaded into the internal memory of the DLPC900 by the firmware. The pattern sequences and images must first be defined before starting the pattern sequence.
2. **Video Pattern Mode.** In this mode, the images are streamed from the incoming video source. The pattern sequences must be defined first before starting the pattern sequence.
3. **Pattern On-The-Fly Mode.** In this mode the patterns are uploaded to the internal memory of the DLPC900 via the USB or the I<sup>2</sup>C interface. The pattern sequences must be defined first before starting the pattern sequence. Using USB is preferred for the higher upload speed.

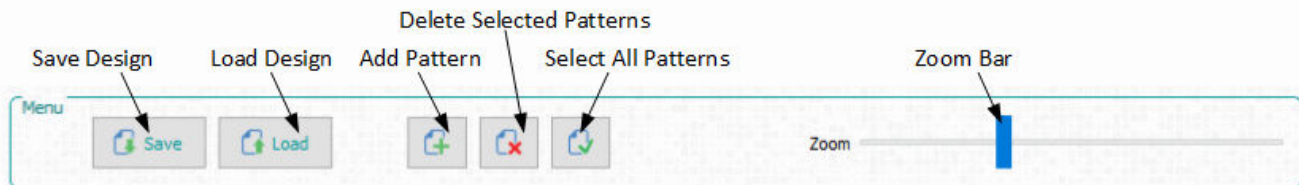


**Figure 3-6. Pattern Mode Design Panel**

### 3.6.1 Menu Bar

The *Menu* bar has six controls as shown in [Figure 3-7](#).

- The *Save* button allows the current pattern design to be saved to a file.
- The *Load* button allows a saved design to be loaded into the Design Panel. This control also allows images to be loaded from a text file that contains a list of the bitmaps in a desired order.
- The *Add* pattern button adds a single pattern or multiple patterns to be loaded into the Design Panel.
- The *Zoom* bar allows the Design Panel to show more of the patterns within the panel.
- The *Delete* pattern button deletes a selection of patterns (a single pattern or multiple patterns) from the Design Panel.
- The *Select* button selects all the patterns from within the Design Panel.

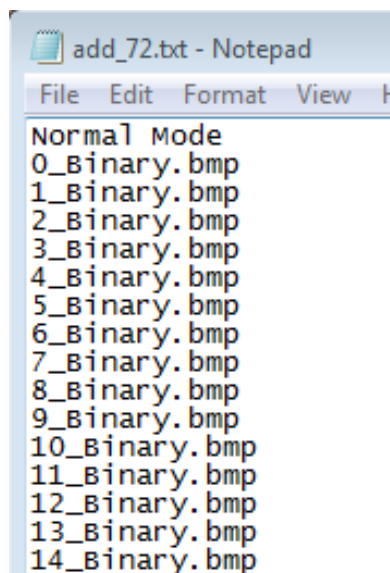


**Figure 3-7. Pattern Mode Menu Bar**

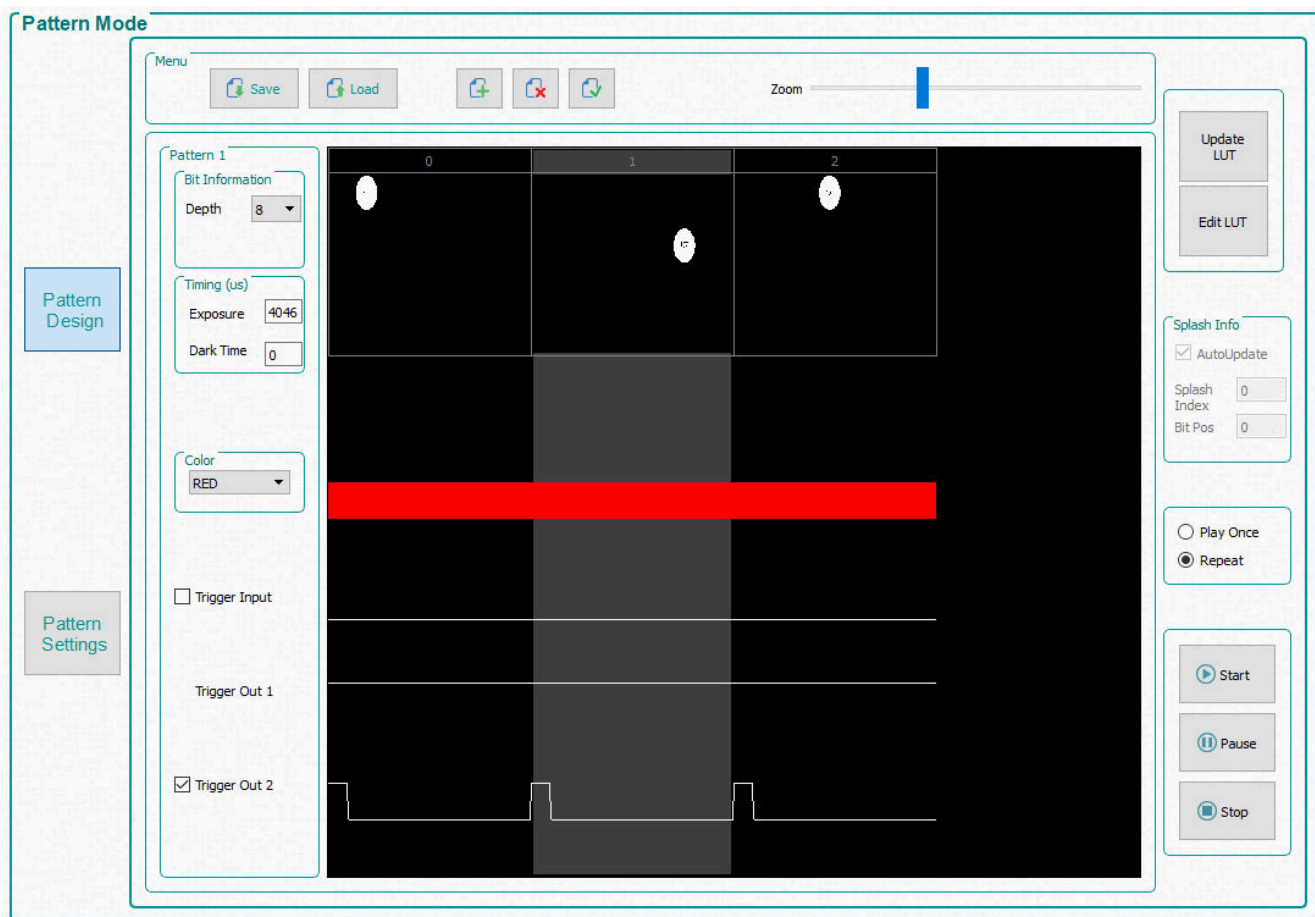
### 3.6.2 Creating a Pattern Sequence in Pattern On-The-Fly Mode

*Pattern On-The-Fly Mode* is the easiest and quickest way to create a pattern sequence. Follow these steps to create a simple pattern sequence using three images

1. Make sure the EVM is powered up and operating correctly, and the *Connected* radio-button is lit **green**.
2. Select *Pattern On-The-Fly Mode* from the *Operating Mode* group box, and then click the *Pattern Design* button so that the large black panel is displayed as shown in [Figure 3-6](#).
3. Click the *Add Pattern* button in the *Menu* bar and browse for any three bitmap images from the sample of images directory. Be sure to select them from the correct image folder for the EVM that is being used. All three images can be selected at the same time within the open file dialog window. When selecting multiple files at once, the operating system do not load the images in the same order as the images were selected. To add images in a predefined order, use the *Load* button. This option loads the images from a text file that contains the file names of each bitmap in the desired order. Alternatively, you can add the images one at a time using the *Add Pattern* button to verify order. [Figure 3-8](#) shows an example of loading images from a file. The bitmap images must be located in the same directory as the text file. See [Figure 3-9](#) after the images have been added.



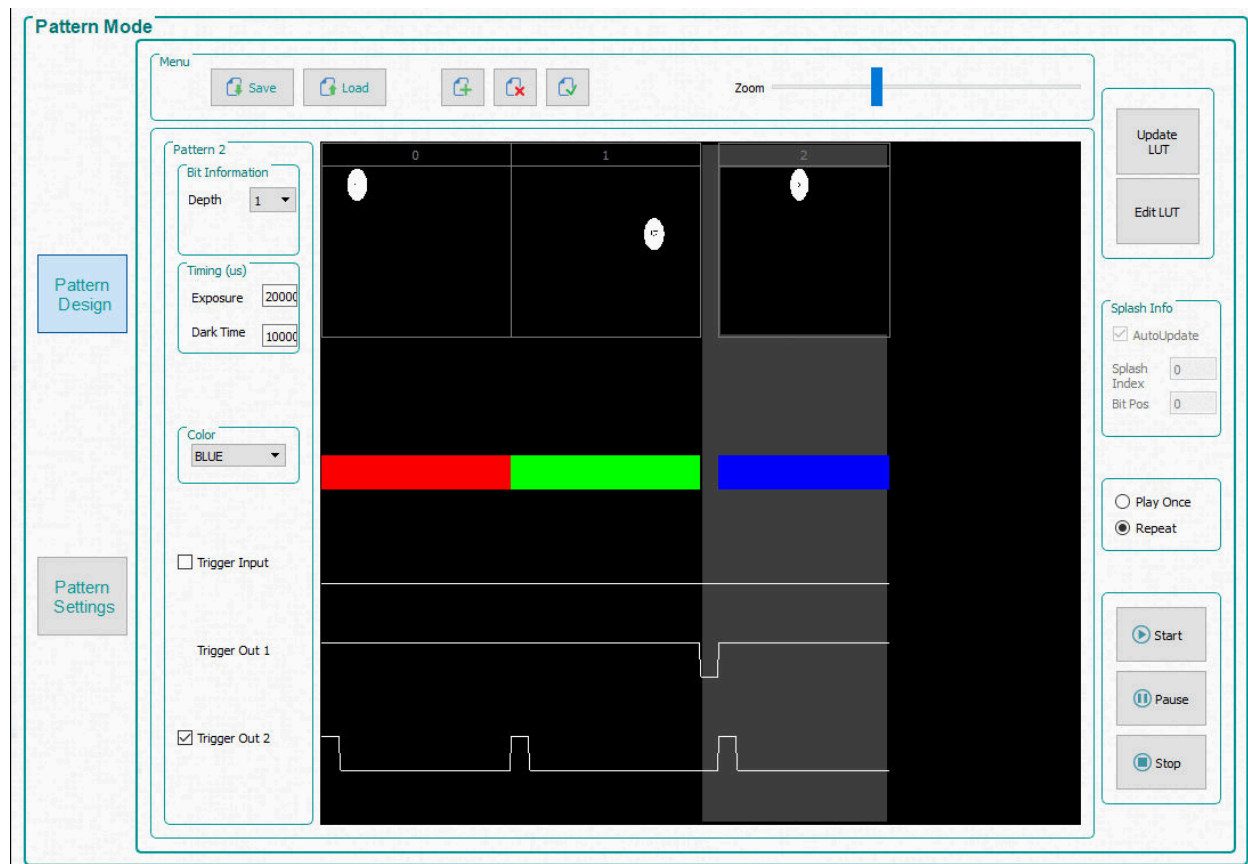
**Figure 3-8. Add From List**



**Figure 3-9. Pattern Sequence**

4. Each pattern in the display can now be selected individually or a multiple selection can be done. To select multiple patterns in a series, use *Shift+Click*. To select multiple patterns that are not in a series, use *Ctrl+Click*. An alternate method for selecting all images is to use the *Select All Patterns* button on the *Menu* bar.
5. Select the first pattern and choose a bit depth of 1. Set exposure to 100000 $\mu$ s and set dark time to 50000 $\mu$ s. The dark time is the time from the end of one pattern to the start of the next pattern. Select **Red** for the color.
6. Select the second pattern and choose a bit depth of 1. Set exposure to 150000 $\mu$ s and set dark time to 75000 $\mu$ s, and select **Green** as the color.
7. Select the third pattern and choose a bit depth of 1. Set exposure to 200000 $\mu$ s and set dark time to 100000 $\mu$ s, and select **Blue** as the color. Figure 3-10 shows the pattern sequence. The *Zoom* bar can be used to zoom in and out which is useful when many patterns are loaded into the panel.





**Figure 3-10. Three Pattern Sequence**

8. Select the *Repeat* radio-button to continuously repeat the pattern sequence, or, if only one time is desired, then select the *Play Once* radio-button.
9. Click the *Update LUT* button to upload the pattern sequence definition, including the three pattern images, to the DLPC900. Notice the green status bar indicating that the updated LUT is uploading to the EVM. This animation does not occur if the user only edits pattern settings rather than uploading actual new patterns. If there are any errors in the pattern sequence, the GUI displays an error message.
10. Once the upload is complete, click the *Start* button. The display repeatedly displays the Red, Green, and Blue dots.
11. To pause the pattern sequence, click the *Pause* button. To continue from where the pattern sequence paused, click the *Start* button.
12. To stop the pattern sequence, click the *Stop* button. To restart the pattern sequence click the *Start* button. The pattern sequence starts from the beginning whenever the pattern sequence is stopped using the *Stop* button.

Another method of creating a pattern sequence in *Pattern On-The-Fly Mode* is using a batch file that contains all the necessary commands and the compressed images.

Follow these steps to execute a pattern sequence using a batch file.

1. Stop the pattern sequence if currently running.
2. Click the *Batch Files* button at the top of the GUI.
3. Check the box to *Enable Command Logging*.
4. Click the *Clear All* button to clear any content in the panel.
5. Click the *Load Batch File* button and select the **onthe-fly.txt** file located in the sample images directory. Be sure to select the correct file for the EVM being used.
6. Click the *Execute All* button. Wait for the batch file to execute.
7. Click the *Pattern Mode* button at the top of the GUI.
8. Click the *Start* button.



9. Click the *Stop* button to end this example.

### 3.6.3 Creating a Pattern Sequence in Pre-Stored Pattern Mode

Creating a Pre-Stored Pattern sequence is very similar to *Pattern On-The-Fly Mode*. The difference is that the patterns are pre-stored in flash memory.

If images are not present in the flash memory, then proceed to [Section 3.9.1](#) to add the images to the firmware and upload them to the EVM, then return here for steps on creating the pattern sequence.

To start creating a Pre-Stored Pattern sequence, follow the steps in [Section 3.6.2](#) with the only change in step 2. In step 2, select the *Pre-Stored Pattern* radio-button and then proceed with step 3.

### 3.6.4 Reordering a Pattern Sequence using the Edit LUT Feature

The *Edit LUT* feature is only available in *Pre-Stored Pattern Mode* and *Pattern On-The-Fly Mode* when using GUI 4.0 or later in conjunction with Firmware 5.0 or later. This feature allows the user to manipulate the pattern display sequence without having to reload or change any data in the DLPC900 memory. The *Edit LUT* button appears on the right hand side of the *Pattern Mode* tab after adding patterns as described in [Section 3.6.2](#).

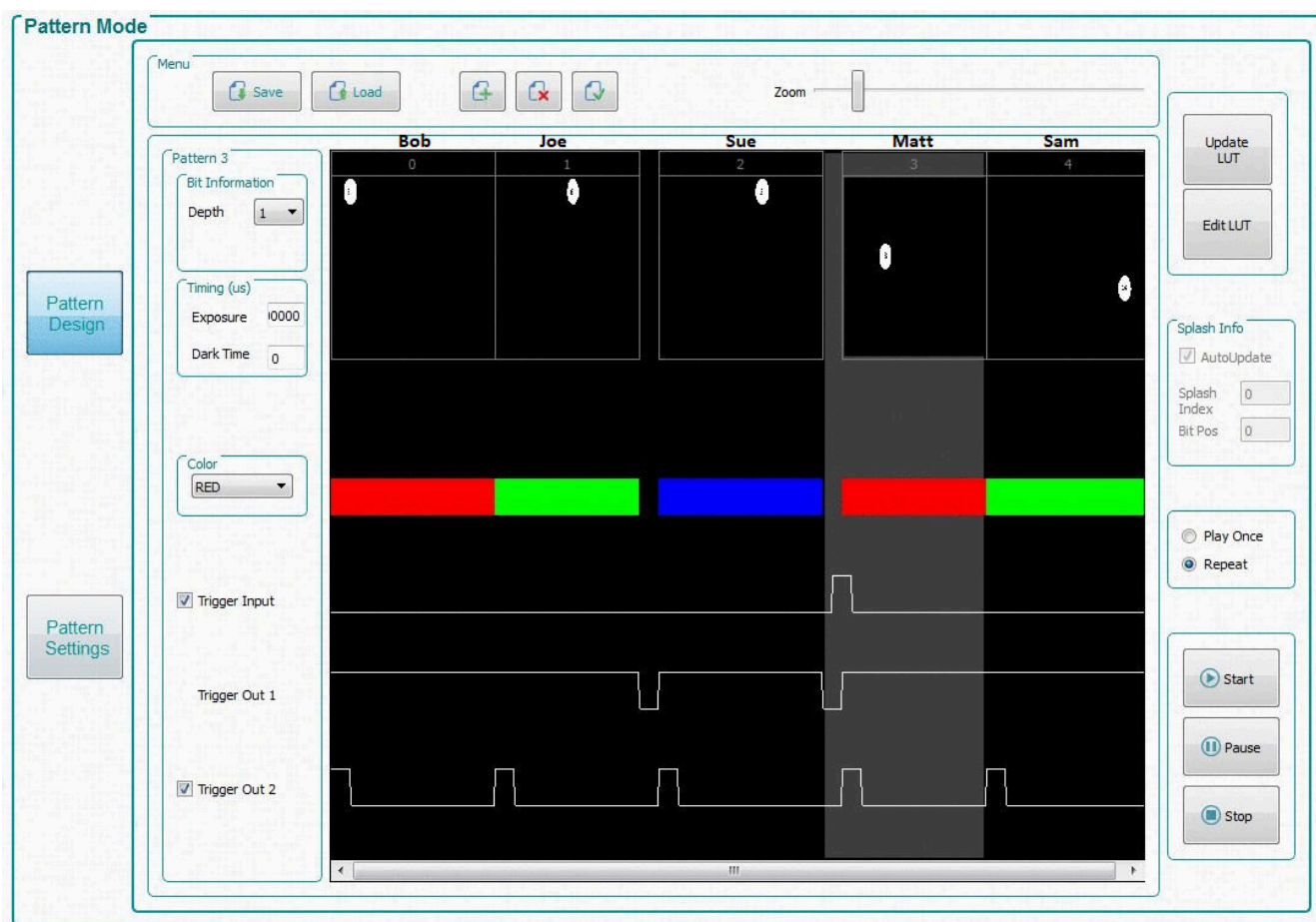


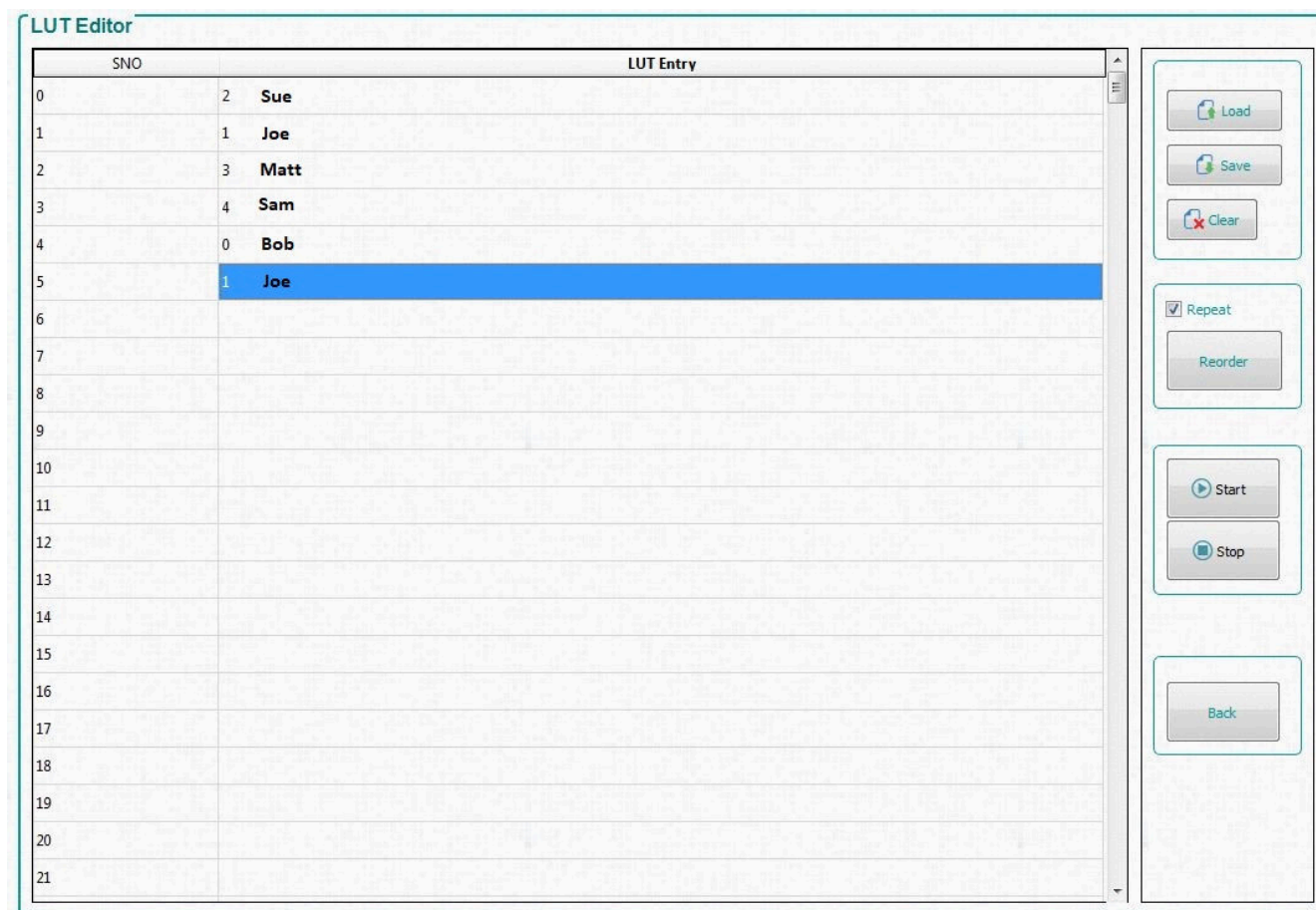
Figure 3-11. Pattern Design Example

#### Note

The names Bob, Joe, Sue, Matt, and Sam in [Figure 3-11](#) and [Figure 3-12](#) do not actually appear in the GUI but are for explanation purposes used in [Figure 3-13](#). In the GUI only numbers are used.

The *LUT Editor* panel is shown in [Figure 3-12](#) where the column labeled *SNO* (for example, sequence number) represents the pattern display slot numbers according to the order set (after clicking *Update LUT*) in the *Pattern Design* tab as shown in [Figure 3-11](#). The user can then reorder those patterns by either manually entering the

pattern number of choice into a row in the *LUT entry* column or loading a text file beginning with the word *Reorder* followed by the desired numerical pattern slots as shown in [Figure 3-13](#).



| SNO | LUT Entry |
|-----|-----------|
| 0   | 2 Sue     |
| 1   | 1 Joe     |
| 2   | 3 Matt    |
| 3   | 4 Sam     |
| 4   | 0 Bob     |
| 5   | 1 Joe     |
| 6   |           |
| 7   |           |
| 8   |           |
| 9   |           |
| 10  |           |
| 11  |           |
| 12  |           |
| 13  |           |
| 14  |           |
| 15  |           |
| 16  |           |
| 17  |           |
| 18  |           |
| 19  |           |
| 20  |           |
| 21  |           |

**Figure 3-12. LUT Editor Panel**

The following steps detail an example using the *Edit LUT* feature with five patterns in *Pattern On-The-Fly-Mode*:

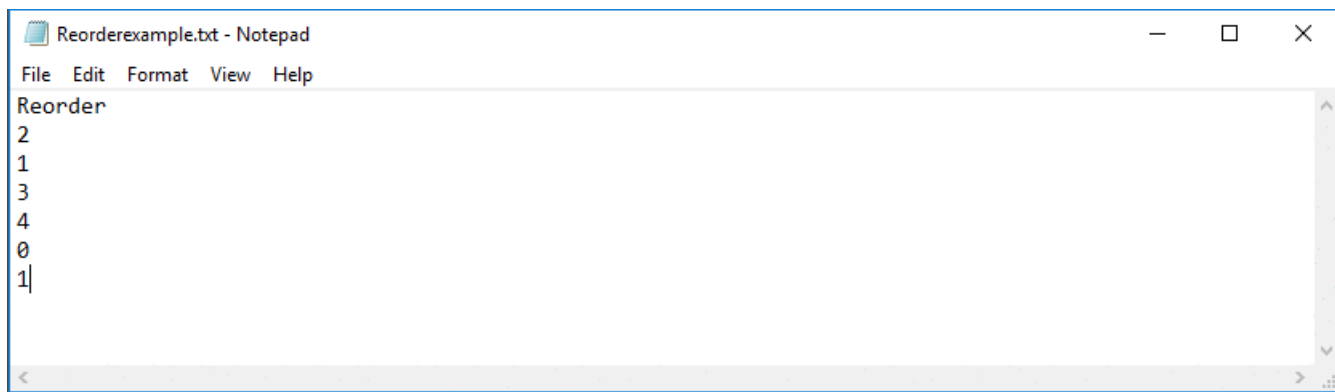
1. By following the steps similar to [Section 3.6.2](#), load five patterns in *Pattern On-The-Fly-Mode* as shown in [Figure 3-11](#).
2. Set the exposure time to 1000000μs for ease of viewing.
3. Set an input trigger on Pattern 3 by checking the *Trigger Input* box.
4. Set all other user desired pattern property settings.
5. Click the *Update LUT* button and wait for all patterns to be uploaded to the EVM.
6. Click the *Start* button to view the pattern display order.
7. Click the *Edit LUT* button to open the *LUT Editor* panel as shown in [Figure 3-12](#). This panel initially opens with an empty *LUT Entry* column.
8. To reorder the patterns, enter a 2 in *LUT Entry* row 1, and subsequently a 1, 3, 4, 0, and 1 as shown in [Figure 3-12](#).
9. Click the *Stop* button to stop the pattern sequence even if not currently running.
10. Check the *Repeat* box for display repetition of the new pattern sequence and then click the *Reorder* button.
11. Click the *Start* button to view the new pattern display order.
12. Click the *Save* button to save the new pattern sequence as a text file for later use. Name the file **Reorderexample.txt** and save in a directory of choice.
13. Browse to wherever the text file was saved, and open the file in a text editor program as shown in [Figure 3-13](#).
14. Notice at the top of the text file is the word *Reorder* which is required to successfully load a reorder file in the GUI. If desired, swap any of the pattern numbers to create a new pattern sequence and save the file.
15. Click the *Load* button and browse for **Reorderexample.txt**.

16. Click the *Start* button to view the new pattern display order.
17. Click the *Stop* button to stop the pattern sequence.
18. Click the *Back* button to return to the *Pattern Mode* tab.

### Note

When using the GUI interface, each pattern carries several items of information corresponding to the slot that was inserted in the original LUT order. Therefore, when reordering a pattern to a different slot in the LUT, the LUT retains the original assigned bit information, timing (exposure and dark time), LED color, and trigger information and not the information that is located in the reassigned slot in the LUT. For example, if pattern 3 contains a trigger input to begin the user's execution in [Figure 3-11](#) and is reordered to slot 2 as shown in [Figure 3-12](#), then the trigger input is now located at slot 3 in the LUT rather than slot 0.

In *Pre-Stored Pattern Mode*, reordering the LUT does not change the pattern locations as the LUT does in *Pattern On-The-Fly Mode*. Only the requested bit information, timing, color, and trigger information are reordered based on the user's updated LUT. However, users can manipulate the pattern information after reordering the sequence by manually editing the batch file commands uploaded to the EVM rather than using the GUI.



**Figure 3-13. Reorder Example**

TI recommends to understand the *Edit LUT* feature by giving names to the patterns, as shown above the patterns in [Figure 3-11](#), and treating them as the batting order in a baseball game making easier to picture the distinction between the slot order numbers in the *SNO* column (the new batting order) and the patterns that are inserted into the *LUT Entry* column (the batters). For example, Bob retains his batting statistics independent of his batting position (for example, bit information, timing, color, trigger), so if he originally bats first (slot 0 in [Figure 3-11](#)) but is moved to fifth in the new batting order (reorder LUT entry 4 in [Figure 3-12](#)) he retains all of his properties. The only difference is that now he does not bat (for example, is not displayed) until after the four batters (patterns) preceding him (Sue, Joe, Matt, and Sam). Notice also that in our example Joe bats twice in the new batting order (SNO 2 and 5). There is only one Joe, who bats twice in the new batting order. In the same way, a pattern in the original pattern list can be displayed in many positions in the reordered display LUT, without adding or removing any patterns in DLPC900 memory. Therefore, in terms of the system memory, the LUT is only changed when using the *Update LUT* feature but never when using the *Edit LUT* (reorder) feature in the [Figure 3-11](#).

#### 3.6.4.1 Special Considerations for Input Triggers when using the Edit LUT Feature

Input triggers have some special considerations when using the new *Edit LUT* feature. As previously noted, Pattern 3 (*Matt*) has an input trigger associated. When a trigger is added to a pattern, the pattern preceding, Pattern 2 (*Sue*) in this case, has an all-black pattern loaded at the very end, so that while the sequence is waiting for the trigger, the sequence does not have a pattern displaying during the wait. This attribute follows Pattern 2 during a reorder. In our example the first entry (Pattern 2) in the new display order has a 105µs dark pattern at the end. If Pattern 0 (*Bob*) is placed in front of Pattern 3 (*Matt* - the triggered pattern) then the last

bit-pattern of Pattern 0 (*Bob*) continues to display in our new display order while waiting for the trigger in Pattern 3 (*Matt*).

The unwanted trigger behavior can be compensated for by one the following methods:

- **Method 1:** Consider Pattern 2 and 3 ("Sue" and "Matt") as a set that must always be moved together in your new display order.
  - Method 1 is the simplest, but the tradeoff is that these two patterns are not separable without the undesired trigger behavior described above.
- **Method 2:** Add a 1-bit all-black pattern with the shortest duration allowed for most DMDs preceding the trigger pattern. See Minimum Exposure in Any Pattern Mode in the [DLPC900 Programmer's Guide](#) for details. Now consider this pattern and the triggered pattern following as a set that must always be moved together.
  - Method 2 is more versatile, but the pattern set must always contain a triggered pattern.
- **Method 3:** Add a 1-bit all-black pattern with the shortest duration allowed followed by a triggered 1-bit all-black pattern, and remove the trigger from the actual pattern of interest. These two patterns are now a versatile trigger set that can be used repeatedly wherever a trigger is desired. Note that since the triggered pattern is black, even if your exposure integration begins with the triggered black pattern there is no light contributing to your exposure.
  - Method 3 is the most versatile since this standalone trigger set only has to be placed in the Pattern Index set (on the Pattern Mode page) once but can be used in many positions when defining a new display order.

### 3.6.5 Creating a Pattern Sequence in Video Pattern Mode

Creating a *Video Pattern Mode* sequence is also similar to *Pattern On-The-Fly Mode*. The difference is that the patterns are streamed from the input video source. The user must create the video images and then input them through the RGB parallel interface. The input video source vertical sync is used as the triggering mechanism. The input video source must be stable and locked and continuously being applied, otherwise the firmware detects that the source was lost and a solid curtain is displayed. While the video source is stable and locked, the status box for *Locked to External Source* remains checked.

Follow these steps to create a *Video Pattern Mode* sequence.

1. Make sure the EVM is powered up and operating correctly, and the *Connected* radio-button is lit **green**.
2. Select *Video Mode* from the *Operating Mode* group box, and then select the *HDMI* radio-button from the *IT6535 Receiver* group box and finally click the *Set* button. (Select the *DisplayPort* radio-button if using this input connector). The *Locked to External Source* status box is checked after a few seconds.
3. Verify the video source is displayed. If no video source is displayed, then make sure the PC or host is supplying the correct resolution and frame rate. See [Video Support](#) for video support.
4. Select *Video Pattern Mode* from the *Operating Mode* group box, and then click the *Pattern Design* button so that the large black panel is displayed as shown in [Figure 3-6](#).
5. Click the *Add Pattern* button on the *Menu* bar and add 3 patterns. (Hover over buttons in the *Menu* bar to see the tool tip).
6. Click the *Select All Patterns* button to select all images.
7. Set exposure time to 1215 $\mu$ s and dark time to 0 $\mu$ s.
8. Select the first image and select a bit depth of 2, start bit G0, and color Green.
9. Select the second image and select a bit depth of 4, start bit R0, and color Red.
10. Select the third image and select a bit depth of 3, start bit B0, and color Blue
11. Click the *Select All Patterns* button.
12. Remove the check from the *Frame Change* box.
13. Select pattern 0 (first pattern) only and check the *Frame Change* box.
14. Click the *Update LUT* button.
15. Click the *Start* button to run the sequence. The system displays a green 2-bit sequence based on G0 - G1 for 1215 $\mu$ s, a red 4-bit sequence based on R0 - R3 for 1215 $\mu$ s, and a blue 3-bit sequence based on B0 - B2 for 1215 $\mu$ s with the remainder of the video frame time dark. This sequence repeats every frame with the new video data for that frame.
16. Click the *Stop* button to end this example.

See [Figure 3-14](#) after all settings have been applied.

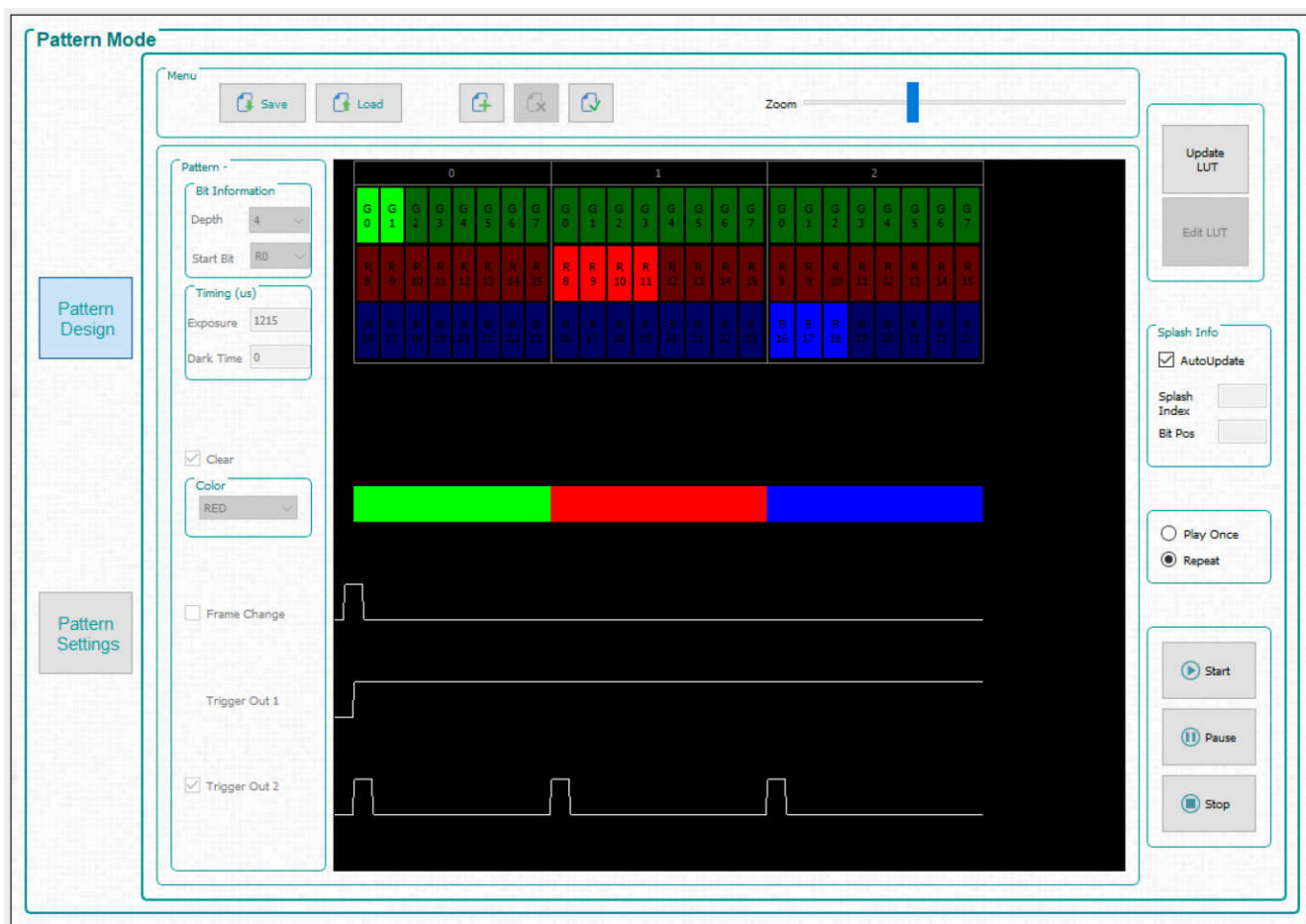


Figure 3-14. Video Pattern Mode



### 3.6.6 Creating a Pattern Sequence With DMD Block Load

Creating pattern sequences with DMD Block Load achieves higher pattern rates by using a subset of the DMD blocks. See DMD Block Load command in the DLPC900 Software Programmer's Guide ([DLPU018](#)) for a description of this command.

Follow these steps to create a *Pattern On-The-Fly* pattern sequence with only three DMD active blocks.

1. Make sure the EVM is powered up and operating correctly, and the *Connected* radio-button is lit **green**.
2. Select *Pattern On-The-Fly Mode* from the *Operating Mode* group box, and then select the *Pattern Design* button so that the large black panel is displayed as shown in [Figure 3-6](#).
3. Select the *Load* button from the menu bar and select the **Block\_Load.txt** file located in the sample images directory. Be sure to select the correct file for the EVM being used.
4. The pattern design looks similar to [Figure 3-15](#).
5. Select the *Pattern Settings* button.
6. Within the *DMD Active Blocks*, select 6 for the *Start Block* and select 8 for the *End Block*, and then Select *Set*.
7. Select the *Pattern Design* button.
8. Select the *Update LUT* button.
9. Select the *Start* button to run the sequence. The system only loads data in blocks 6, 7, and 8 regardless of the thumbnail image shown in the Pattern Design window and displays each pattern for 180,000  $\mu$ s.
10. Select the *Stop* button to end this example.

Notice that pattern 0 is displayed the same way as pattern 2 even though pattern 0 contains image data for all blocks showing that only blocks 6, 7, and 8 are active while all other blocks are in the off state.

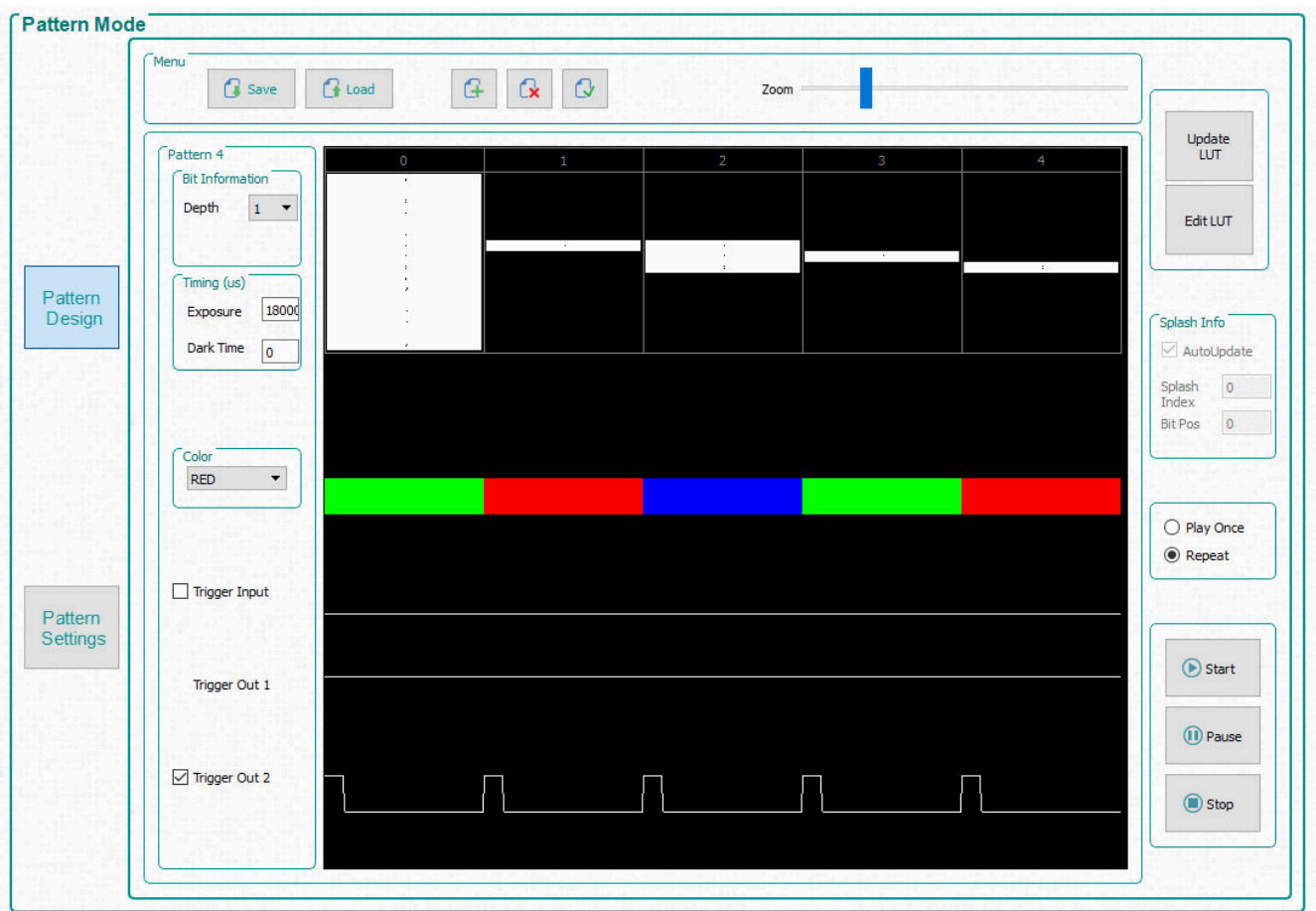
---

#### Note

The performance of the mirrors in the blocks that are not active are affected by prolonged use of being in the off state. Enable *DMD Idle Mode* as often as possible to optimize the mirrors. This mode provides a 50/50 duty cycle across the entire DMD mirror array, where the mirrors are continuously flipped between the on and off states. See *DMD Idle Mode* command in the DLPC900 Software Programmer's Guide ([DLPU018](#)) for a description of this command. The *DMD Idle Mode* command is located in [Section 3.6.7](#) within the *Pattern Mode* panel.

---





**Figure 3-15. DMD Block Load Pattern Sequence**

### 3.6.7 Pattern Settings

To configure the output or input triggers click the *Pattern Settings* button as shown in [Figure 3-16](#). Within this panel, the user can select the output delays of Trigger 1 and 2 and the input delay for Trigger In 1 and 2. The output delays are from the start of the pattern on the DMD.

Trigger In 1 and 2 can be delayed to have a later affect from when the external device performed the trigger. All signals can be inverted.

The LED controls can also be delayed. These delays are from the start of the pattern on the DMD. Checking the invert box on the LED control signals causes the signals to be inverted so that rising edges become falling edges and falling edges become rising edges.

Minimum LED Pulse Width control allows the system to have much higher pattern speeds. Input the minimum LED Pulse Width specific to your illumination system to achieve higher pattern speeds through illumination modulation. Illumination modulation occurs with 8-bit patterns at rates faster than 1031Hz, by modulating the light source to shorten the shortest bits, therefore increasing the maximum pattern speed. The LED enable signals from the DLPC900 can be used to control the illumination modulation of the LEDs.

The *DMD Active Blocks* allows a subset of DMD blocks to be activated, while the mirrors in the non-selected blocks are set to the off state. See [Section 3.6.6](#) for an example of using this mode. When operating with a subset of DMD blocks, enable the *DMD Idle Mode* as often as possible. For example, whenever the system is idle, between exposures if the application allows, or when the exposure pattern sequence is stopped. To enable the *DMD Idle Mode*, the pattern sequence must be stopped. To restart the pattern sequence, the *DMD Idle Mode* must be disabled.

---

#### Note

To resume operation after disabling *DMD Idle Mode* issue an *Update LUT* command.

---

The *DMD Idle Mode* provides a 50/50 duty cycle across the entire DMD mirror array, where the mirrors are flipped between the on and off states to optimize the performance of the mirrors.

The *Image Compression* option allows for control of the compression type that occurs to the pattern images.

- The Uncompressed option does not attempt to compress the images.
- The Run Length Encoding (RLE) option is a lossless compression method that compresses the pattern data using horizontal pixel similarities.
- The Enhanced RLE option is a lossless compression method that compresses the pattern data using vertical and horizontal pixel similarities.
- The Auto option is capable of doing all three compression types and selects the method that takes up the smallest amount of data.

More information on these compression types can be found in the [DLPC900 Programmer's Guide](#).

Pattern Mode

DMD Active Blocks

Start Block: 0

End Block: 15

Get

Set

Minimum LED Pulse Width

Pulse Width (in ns): 0

Get

Set

DMD Idle Mode (50/50 Sequence)

Enable

Disable

Image compression

Auto

Trigger Control

Trigger Out 1 (us)

Rising Edge Delay: 0

Falling Edge Delay: 0

☐ Invert

Get

Set

Trigger Out 2 (us)

Rising Edge Delay: 0

Falling Edge Delay: 0

☐ Invert

Get

Set

Trigger In 1 (us)

Delay (us): 0

☐ Invert

Get

Set

Trigger In 2

☐ Invert

Get

Set

LED Delay Controls (us)

Red LED Rising Edge Delay: 0

Green LED Rising Edge Delay: 0

Blue LED Rising Edge Delay: 0

Red LED Falling Edge Delay: 0

Green LED Falling Edge Delay: 0

Blue LED Falling Edge Delay: 0

☐ Invert

Get

Set

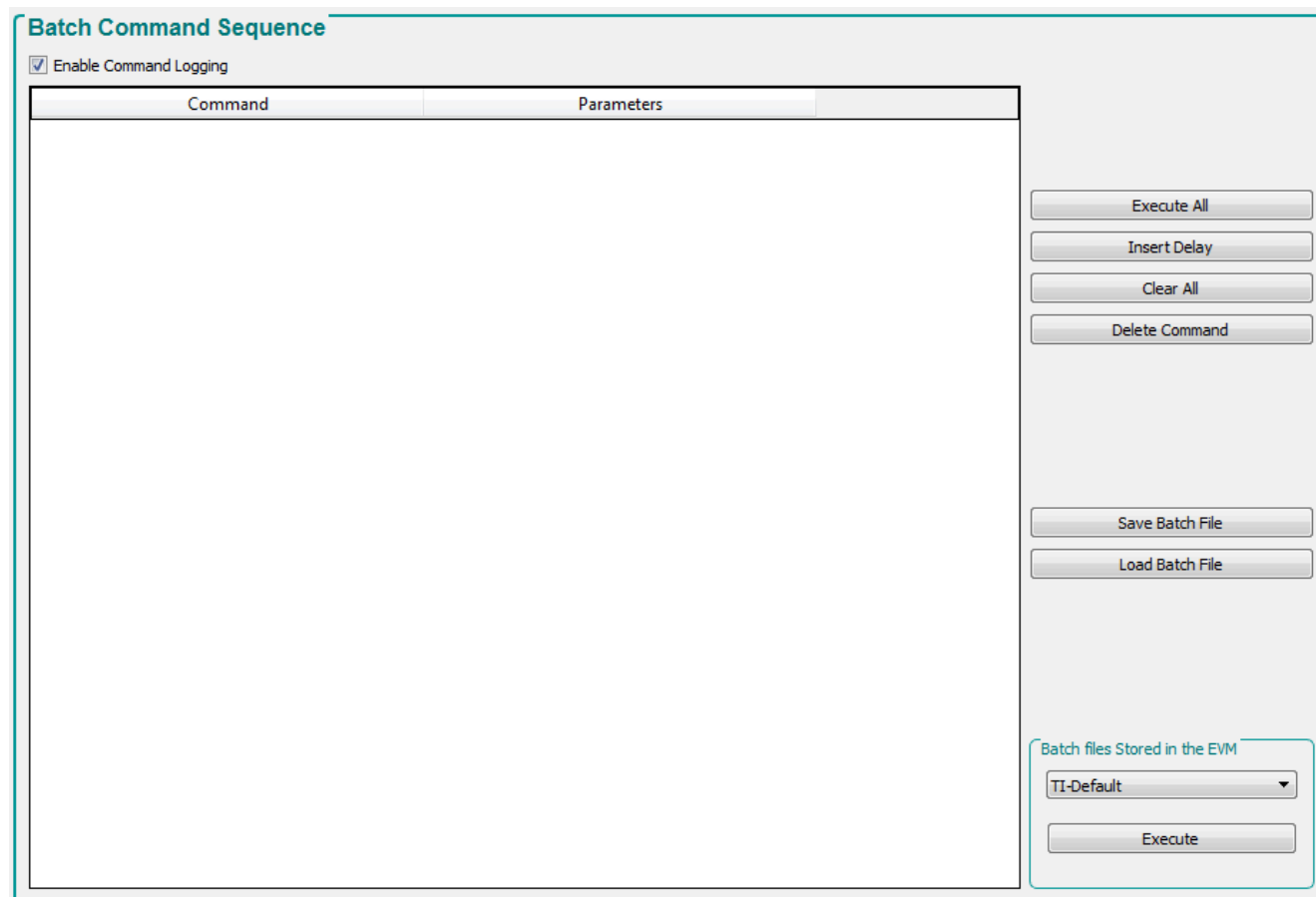
Pattern Design

Pattern Settings

**Figure 3-16. Pattern Settings Panel**

## 3.7 Batch Files

Click the *Batch Files* button at the top of the GUI to display the *Batch Command Sequence* panel as shown in [Figure 3-17](#). If the *Enable Command Logging* box is checked, then the panel displays all the commands the user clicks on the GUI. When clicking the *Save Batch File* button, the content that is in the panel is saved to a text file containing the command descriptor followed by the command data. This batch file can then be reloaded at a later time using the *Load Batch File* button and executed or can be added to the firmware to be executed using the *Batch files Stored in the EVM* group box. This group box lists all the batch files currently in the firmware.



**Figure 3-17. Batch Files Panel**

### 3.7.1 Execute Batch File

The DLPC900 firmware has the option to store batch files within the firmware. These batch files contain commands that are executed to perform some sequence of settings without the user or host performing these commands. If the firmware contains batch files, then the GUI queries the batch file names and list them in the *Batch Command Sequence* panel within the *Batch Files Stored in the EVM* group box. The user can then choose a batch file from the list to execute.

The user can also designate a default batch file to be executed during the DLPC900 power-up sequence when the power is applied. For example, if the LEDs are to remain off after power has been applied to the EVM, a batch file can be created with the commands to turn the LEDs off, and then update the firmware with this batch file as the default. The updated firmware can then be uploaded to the EVM. The next time the power is applied to the EVM, the LEDs are disabled from turning on. See the [DLPC900 Programmer's Guide](#) for the batch file commands in Appendix B for a complete list of commands that can be used within a batch file.

### Note

If an *IDX* error is received during execution of a batch file, then this indicates that execution failed at the line number that follows *IDX* in the error message. *POWER\_CONTROL* (Power Mode) commands often result in an *IDX* error since the EVM can no longer communicate with the GUI because the power state has been changed.

## 3.7.2 Creating and Saving Batch Files

There are two methods to create and save batch files:

- Use the GUI
- Use a text editor

### 3.7.2.1 Creating and Saving a Batch File Using the GUI

This example contains the following set of commands:

1. Set the short axis image orientation.
2. Set the input channel swap to ABC->CBA.
3. Set the LED currents.

Follow these steps to add the commands and save to a text file

1. Power-up the EVM and verify that the *Connected* radio button is lit *green*.
2. Click the *Video Mode* button at the top of the GUI .
3. Select the *Parallel RGB* under *Source Select*.
4. Select *Video Mode* from the *Operating Mode* group box, and then select the *HDMI* radio-button from the *IT6535 Receiver* group box and finally click the *Set* button. (Select the *DisplayPort* radio-button if using this input connector).
5. Verify the EVM is displaying the input source correctly. If no video source is displayed, then make sure the PC or host is supplying the correct resolution and frame rate. See [Section 3.5.1](#) for video support.
6. Click the *Batch Files* button at the top of the GUI.
7. Check the *Enable Command Logging* box to enable logging.
8. Click the *Clear All* button to clear the contents of the list box.
9. Click the *System Settings* button at the top of the GUI.
10. Click the *Get* button within the *Image Orientation* group box.
11. Check the *North/South* checkbox and click the *Set* button within the group box. The image orientation of the displayed image is flipped on the short axis.
12. Click the *Video Mode* button at the top of the GUI.
13. Select ABC->CBA from the drop-down list box within the *Input Port Data Swap* group box.
14. Click the *Set* button within the box. The colors of the image are adjusted for the setting.
15. Click the *System Settings* button at the top of the GUI.
16. Click the *Get* button within the *LED Controls* group box.
17. Enter 100 for each of the LEDs within the *LED Current* group box and click the *Set* button. The LED brightness can change after setting the LED current.
18. Click the *Batch Files* button at the top of the GUI.
19. Within the list box, all the commands that were applied on previous steps are listed.
20. Click the *Save Batch File* button to save as text file.

[Figure 3-18](#) shows the contents of the list box. Notice that there are more than the three mentioned commands in the list box because when clicking the *Set* button for some commands in the GUI, multiple commands are combined. For example, when clicking the *Set* button for image orientation, both long and short axis commands are included.

Within the Batch File panel, each command can also be deleted or a delay can be inserted between commands.

### Batch Command Sequence

☒ Enable Command Logging

|   | Command      | Parameters     |
|---|--------------|----------------|
| 1 | FLIP_LONG    | 0x1            |
| 2 | FLIP_SHORT   | 0x1            |
| 3 | CHANNEL_SWAP | 0xa            |
| 4 | LED_ENABLE   | 0xf            |
| 5 | PWM_INVERT   | 0x0            |
| 6 | LED_CURRENT  | 0x64 0x64 0x64 |

Execute All  
Insert Delay  
Clear All  
Delete Command

Save Batch File  
Load Batch File

Batch files Stored in the EVM  
TI-Default  
Execute

**Figure 3-18. Batch File Example**

### 3.7.2.2 Creating a Batch File Using a Text Editor

Use a text editor to add the same three commands in the previous example. Refer to Appendix B in the DLPC900 Programmer's Guide for the list of command descriptors supported by the DLPC900.

When adding a command descriptor to the file, a colon must be between the descriptor and the parameters. Use a space between the colon and each parameter and use '0x' for each byte in the parameter list. Remember that the parameters are least significant byte first. The lines below show what is to be in the text file.

```
FLIP_SHORT:           0x01
CHANNEL_SWAP:         0x0A
LED_CURRENT:          0x64 0x64 0x64
```

Once all three commands are added, save the file as a text file and use a name to describe what the batch file does.

### 3.7.3 Loading a Batch File

Once users have saved a batch file from either the GUI or a text editor, users can load the batch file into the GUI and execute the commands.

Use the button labeled *Load Batch File* (shown in [Figure 3-18](#)) on the right midway down, and navigate to and select a batch file. Once loaded, the commands can be executed or added to the firmware (see [Section 3.7.4](#)).



### 3.7.4 Adding a Batch File to the Firmware

A batch file can be added to the firmware to be used as the default batch file to be executed during the power-up sequence of the DLPC900. Or a batch file can be added to be executed at any time after the power-up sequence has completed to perform some action. Some special considerations must be taken if adding pattern images and a batch file to the firmware image. See NOTE at end of [Section 3.9.1.2](#) for a description of these considerations. Follow these steps to add the example batch file from the previous section to the firmware.

#### CAUTION

The user must exercise caution when selecting the correct firmware file. Install DLP LightCrafter GUI version 5.x or later and select FW version 6.x or higher. Operating or updating the firmware on the DLPC900 LightCrafter GUI version 4.x (or earlier) renders the EVM inoperable. Note: The DLP5500 DMD is only supported by GUI version 5.2.x and FW version 6.2.0.

1. Power-up the EVM and verify the EVM is operating as expected.
2. Start the GUI and verify the connection has been established with the EVM.
3. Click the *Batch File* button at the top of the GUI.
4. Check the *Enable Command Logging* box to enable logging.
5. Click the *Clear All* button to clear the contents.
6. Click the *Load Batch File* button and browse for the text file that was saved in [Section 3.7.2](#).
7. The panel displays the commands as shown in the file.
8. Click the *Firmware* button at the top of the GUI.
9. Click the *Browse* button and select the firmware files for the EVM being used.

#### Note

When updating the firmware for the DLPC900 Single Controller EVM, there is only one firmware image file (\*.img).

10. Within the *Batch File and Patterns* group box, check the *Add Batch File* box.
11. In the *Batch File Name* edit box, enter a name for the batch file. Up to 16 characters are allowed without spaces and special characters.

#### Note

If the batch file is to be a default batch file to be executed during the power-up sequence, then continue to step 12, otherwise go to step 13.

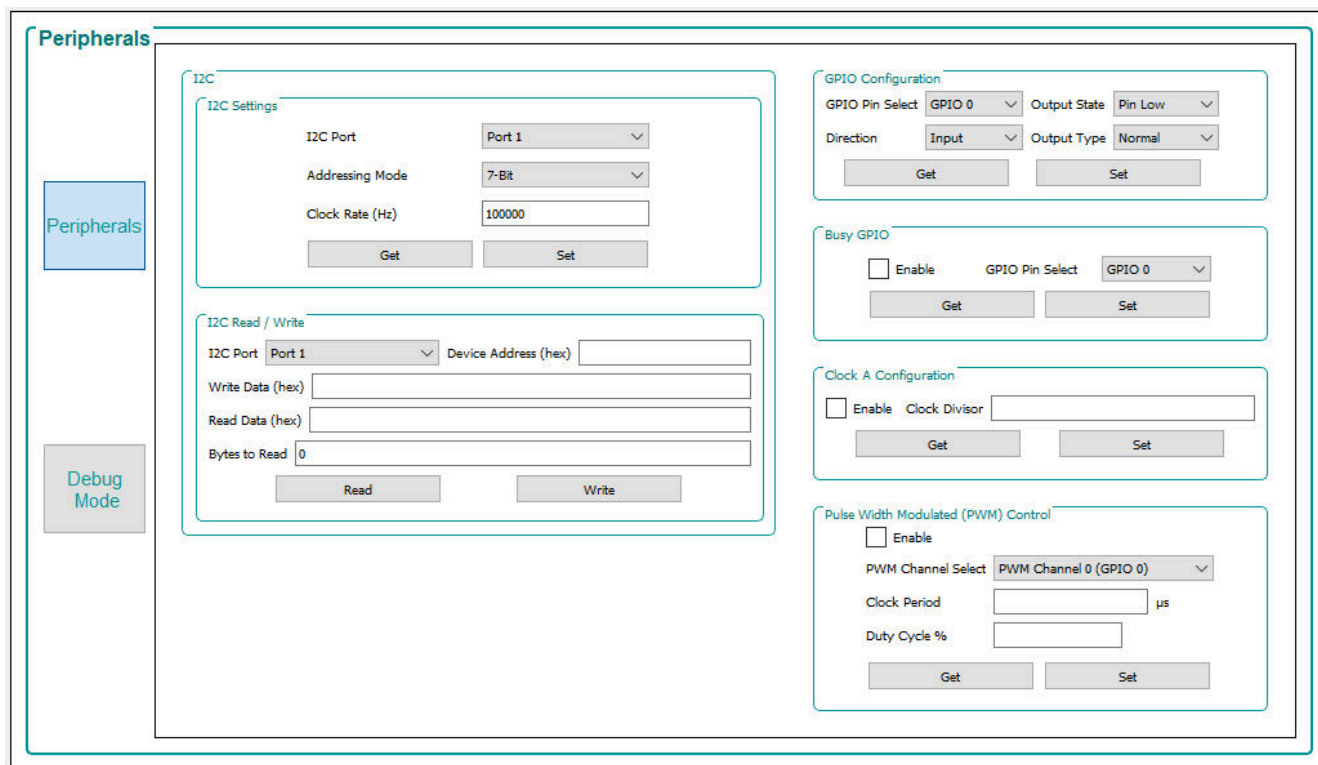
12. Within the *Batch File and Patterns* group box, check the box next to *Set as Default Batch File*.
13. Click the *Update Firmware* button. The GUI displays a pop-up box providing the name of the updated firmware file.
14. Click the *Browse* button and locate the firmware file from step 13.
15. Click the *Upload* button. The GUI performs the necessary steps to update the firmware in the EVM.

#### Note

The name of the firmware file created by the GUI includes a timestamp, verifying that new firmware does not overwrite any previously created firmware files.

### 3.8 Peripherals

Click the *Peripherals* button at the top of the GUI to display the *Peripherals* panel as shown in [Peripherals Panel](#). Beginning with GUI 5.3 Peripherals is divided into the [Peripherals Tab](#) and the [Debug Tab](#).



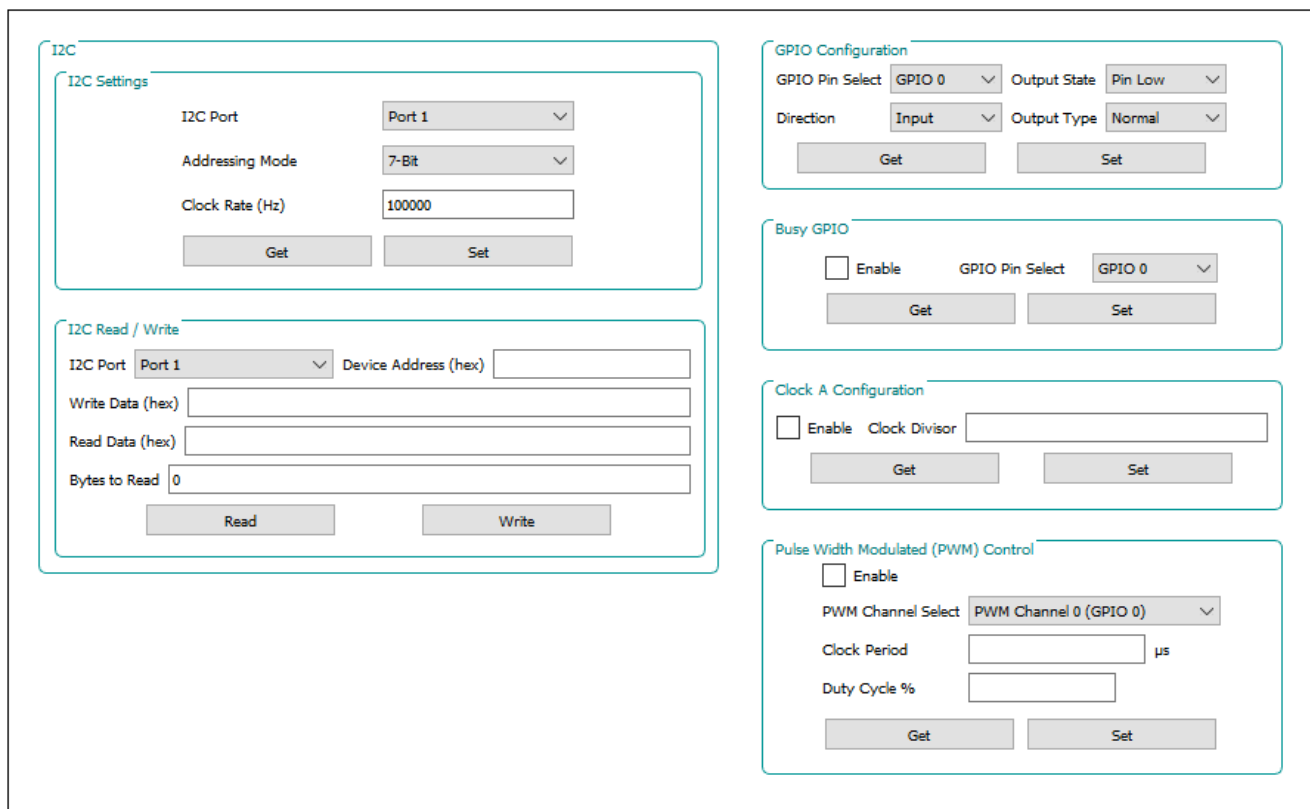
The screenshot displays the 'Peripherals' panel of the GUI. On the left, there is a vertical sidebar with two buttons: 'Peripherals' (highlighted in blue) and 'Debug Mode'. The main area is divided into several sections:

- I2C Section:**
  - I2C Settings:** Includes dropdowns for 'I2C Port' (set to 'Port 1'), 'Addressing Mode' (set to '7-Bit'), and a text field for 'Clock Rate (Hz)' (set to '100000'). Below these are 'Get' and 'Set' buttons.
  - I2C Read / Write:** Includes dropdowns for 'I2C Port' (set to 'Port 1') and 'Device Address (hex)'. It has text fields for 'Write Data (hex)', 'Read Data (hex)', and 'Bytes to Read' (set to '0'). Below these are 'Read' and 'Write' buttons.
- GPIO Configuration:** Includes dropdowns for 'GPIO Pin Select' (set to 'GPIO 0'), 'Output State' (set to 'Pin Low'), 'Direction' (set to 'Input'), and 'Output Type' (set to 'Normal'). Below these are 'Get' and 'Set' buttons.
- Busy GPIO:** Includes an 'Enable' checkbox (unchecked) and a 'GPIO Pin Select' dropdown (set to 'GPIO 0'). Below are 'Get' and 'Set' buttons.
- Clock A Configuration:** Includes an 'Enable' checkbox (unchecked) and a 'Clock Divisor' text field. Below are 'Get' and 'Set' buttons.
- Pulse Width Modulated (PWM) Control:** Includes an 'Enable' checkbox (unchecked), a 'PWM Channel Select' dropdown (set to 'PWM Channel 0 (GPIO 0)'), a 'Clock Period' text field with a 'µs' unit, and a 'Duty Cycle %' text field. Below are 'Get' and 'Set' buttons.

**Figure 3-19. Peripherals Panel**

### 3.8.1 Peripherals Tab

Click the *Peripherals Tab* button at the side of the Peripherals Panel to display the [Figure 3-20](#).



The figure shows a software interface for configuring peripherals. It is divided into several sections:

- I2C Settings:** Includes dropdowns for I2C Port (Port 1), Addressing Mode (7-Bit), and a text field for Clock Rate (Hz) (100000). There are 'Get' and 'Set' buttons.
- I2C Read / Write:** Includes dropdowns for I2C Port (Port 1) and Device Address (hex). It has text fields for Write Data (hex), Read Data (hex), and Bytes to Read (0). There are 'Read' and 'Write' buttons.
- GPIO Configuration:** Includes dropdowns for GPIO Pin Select (GPIO 0), Output State (Pin Low), Direction (Input), and Output Type (Normal). There are 'Get' and 'Set' buttons.
- Busy GPIO:** Includes an 'Enable' checkbox and a dropdown for GPIO Pin Select (GPIO 0). There are 'Get' and 'Set' buttons.
- Clock A Configuration:** Includes an 'Enable' checkbox and a text field for Clock Divisor. There are 'Get' and 'Set' buttons.
- Pulse Width Modulated (PWM) Control:** Includes an 'Enable' checkbox, a dropdown for PWM Channel Select (PWM Channel 0 (GPIO 0)), and text fields for Clock Period and Duty Cycle %. There are 'Get' and 'Set' buttons.

**Figure 3-20. Peripherals Tab**

- The I<sup>2</sup>C group box allows external I<sup>2</sup>C device to be controlled using one of the DLPC900 I<sup>2</sup>C interfaces. For example, if an LED driver requires I<sup>2</sup>C communications to enable the LEDs, then a command can be sent to the LED driver using this interface. When using the I<sup>2</sup>C interfaces, first configure the I<sup>2</sup>C port using the commands within the *I<sup>2</sup>C Settings* and then perform data transfers using the commands within the *I<sup>2</sup>C Read/Write*.
- The GPIO Configuration group box allows the user to configure any of the nine available GPIOs. These GPIOs can be configured as inputs or outputs.
- The Clock A Configuration group box allows the user to enable and control the output frequency of OCLKA. The clock can be used as a source to some external logic.
- The Pulse Width Modulate Control group box allows a user to configure any of the four available PWM outputs.

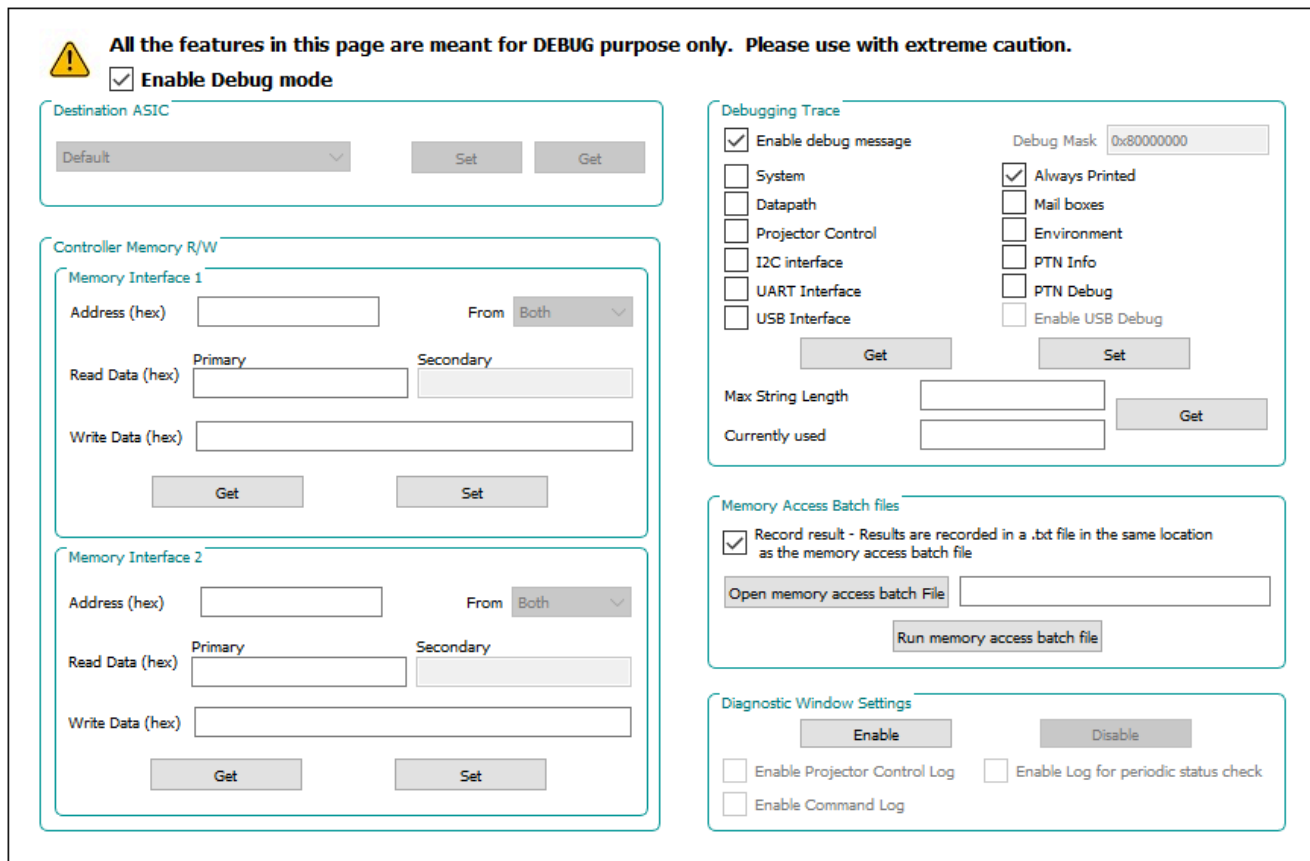
#### Note

**GPIO\_PWM\_00 – GPIO\_PWM\_03 are shared GPIO/PWM outputs.**

### 3.8.2 Debug Tab

GUI 5.3 Added debug functions shown in [Figure 3-21](#).

Click the *Debug Tab* button at the side of the Peripherals Panel to display [Figure 3-21](#).



**All the features in this page are meant for DEBUG purpose only. Please use with extreme caution.**

☒ **Enable Debug mode**

**Destination ASIC**

Default

**Controller Memory R/W**

**Memory Interface 1**

Address (hex)  From **Both**

Read Data (hex) Primary  Secondary

Write Data (hex)

**Memory Interface 2**

Address (hex)  From **Both**

Read Data (hex) Primary  Secondary

Write Data (hex)

**Debugging Trace**

☒ Enable debug message

☐ System ☒ Always Printed

☐ Datapath ☐ Mail boxes

☐ Projector Control ☐ Environment

☐ I2C interface ☐ PTN Info

☐ UART Interface ☐ PTN Debug

☐ USB Interface ☐ Enable USB Debug

Max String Length

Currently used

**Memory Access Batch files**

☒ Record result - Results are recorded in a .txt file in the same location as the memory access batch file

Open memory access batch File

**Diagnostic Window Settings**

☐ Enable Projector Control Log ☐ Enable Log for periodic status check

☐ Enable Command Log

**Figure 3-21. Debug Tab**

Selecting the *Enable Debug mode* checkbox enables the debug functions shown in on the Debug Tab.

- The *Destination ASIC* does not apply to single controller systems and the options remain grayed out.
- The *Controller Memory Read/Write* box allows Reading from and Writing to memory address on the controller.

The *From* box is grayed out for single controller systems since there is only the default (primary) controller.

**CAUTION**

**Read from or write only to defined addresses. Reading from or writing to undefined addresses results in unpredictable behavior!**

There are two virtual interfaces to assist in debugging. For example Reading from or Writing to some addresses can result in changes in other memory locations. The result of writing to a memory using Interface 1 can be tracked in Interface 2.

The Secondary - Read Data box is grayed out for single controller systems.

**The following are valid memory address ranges:**

- 0x00000000 - 0x00003FFF
- 0x11000000 - 0x11003FFF
- 0x11005000 - 0x1100AFFF
- 0x1100F000 - 0x110137FF

- 0x11014000 - 0x110177FF
- 0x11018000 - 0x110187FF
- 0x11019000 - 0x110197FF
- 0x11020000 - 0x11023A40
- 0x11024000 - 0x11025FFF
- 0x20000000 - 0x27FFFFFF
- 0xA7000000 - 0xA7FFFFFF
- 0xF7FEC000 - 0xFAFFFFFF
- 0xFFFF0000 - 0xFFFFFFFF
- The *Debugging Trace* box allows the user to enable various debug areas to report detailed debug information to the [DLPC900 UART](#) beyond the always printed debug messages output during regular operation. Select the *Enable debug message* checkbox to allow the following individual masks to be enabled or disabled separately:

#### **Debug Masks - detailed descriptions:**

- *System information* - Enable printing of debug messages regarding system functions like system initialization, memory allocation, batch command execution.
- *Datapath information* - Enable printing of debug messages regarding datapath like datapath initialization to process video input, ITE, HDMI, DP information, and System state transition.
- *Projector Control Information* - Enable printing of debug messages regarding projector control like initialization of USB and I2C command port, command message handler, and command processing.
- *I<sup>2</sup>C handler* - Enable printing of debug messages regarding I2C communication interface.
- *UART handler* - Enable printing of debug messages regarding UART communication interface.
- *USB handler* - Enable printing of debug messages regarding USB communication interface.
- *Always Printed* (on by default) - Enable printing of debug messages regarding system, power-up, illumination, memory initialization, state transitions, application layer command processing.
- *Mailbox message* - Enable printing of debug messages regarding application mailbox interface.
- *Environment information* - Not used
- *PTN Information* - Enable printing of Pattern Info debug messages regarding loading of splash images to FMT memory or loading a BMP pattern.
- *PTN debug* - Enable printing of Pattern Info debug messages regarding sequence database, selected sequence information, encode and decode of sequences, pattern parameter setup, etc.

When debug messages are enabled, pressing *Get* retrieves the currently enabled masks. After selecting the desired masks pressing *Set* enables the selected masks. The Debug Mask text box shows, in hex, the overall mask value. The value can be used in the [Firmware](#) Power up Defaults section to build a firmware with the desired masks enabled at power up.

#### **Note**

Deselecting *Enable debug message* disables all debug output except Always Printed items

Max String Length - reads the maximum allocated string size for the debug message

Currently Used - reads the string size currently used by the debug message

- The *Memory Access Batch Files* box enables special batch files that can be provided by TI during advanced debugging.

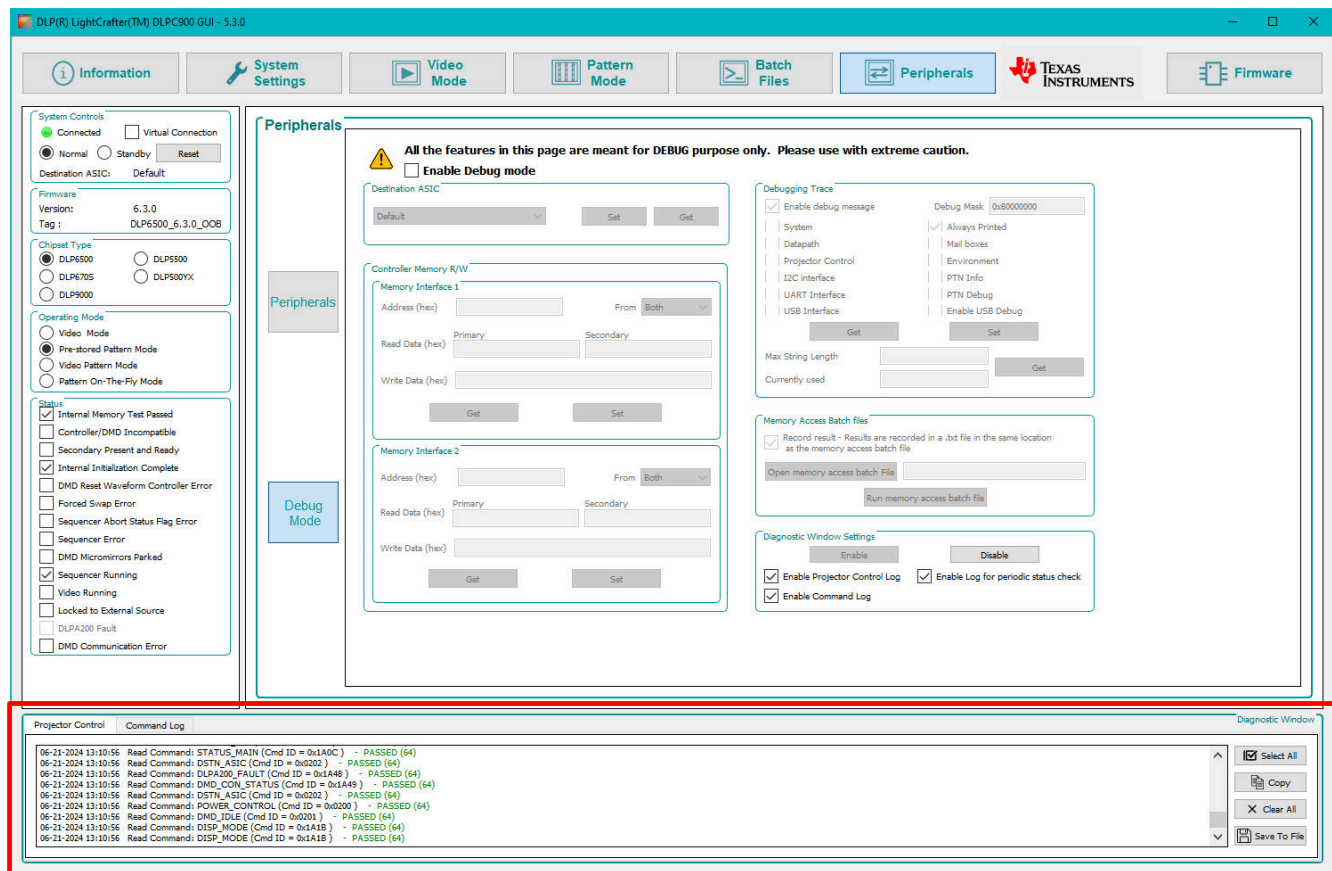
Select the *Record result* check box to record the result of running a provided batch file. Results are recorded in the same directory that the batch file resides.

Use *Open memory access batch File* button to navigate to a memory access batch file provided.

Use *Run memory access batch file* button to run the batch file selected by the *Open memory access batch File*.

- Diagnostic Window Settings

Select the *Enable* or *Disable* button to show or hide diagnostic log windows. Enabling this option shows a command log window under the main GUI window as shown in [Figure 3-22](#).



**Figure 3-22. Diagnostic Window**

These logs are available:

- Select the *Enable Projector Control Log* tab. Records every command sent to the DLPC900 and the status of transaction. Records the system initialization and status commands the first time the DLPC900 is powered up.
- Select the *Enable Command Log* tab. Records information such as command name, command ID, sent and received message packet, status of transaction.
- Select the *Enable Periodic Status Log* to show the periodic status polling commands in the Projector Control tab. Records the DLPC900 main, HW, system status query commands sent by the GUI. This occurs at 120 second intervals. Enabling this records these commands each time the commands are sent. This causes slow response from the GUI.

For the Projector Control log and Command log the following options are available:

- Select all - selects all of the contents of the selected log tab to be copied.
- Copy - copies to the clipboard the currently selected items in the selected log tab. Drag click to select a subset of the log to copy.
- Clear all - clears all of the current entries in the selected log tab.
- Save to File - saves the current selected log tab entries to a file.



### 3.9 Firmware

Click the *Firmware* button to display the Firmware panel as shown in [Figure 3-23](#). In this panel a user can update the firmware with patterns, batch files, and set start-up conditions.

Before uploading firmware, verify the EVM is powered-up, operating as expected and connection has been established with the GUI. To update the firmware on the EVM click the *Browse* button and locate the firmware file for the EVM type. Then click the *Upload* button.

Beginning with GUI 5.3 the user can choose to either:

- Do a Partial Image Download (default) which compares the cached copy of the firmware and only loads the changed sectors.
- Do a Complete Image Download. Use this option if you suspect that the firmware has become corrupted or are loading the same firmware to a different EVM.

By default the Bootloader Area is skipped, but unchecking the Skip Bootloader Area box causes the Bootloader to be reloaded. Use this option with caution.

Within the *Power up Defaults* group box, a user can select the default power-up state by checking the *Startup State* check box and then selecting the state from the drop-down list box.

If the IT6535 digital receiver is present on the hardware, then the user can enable the use of this receiver by checking the *HDMI/DP Receiver* check-box.

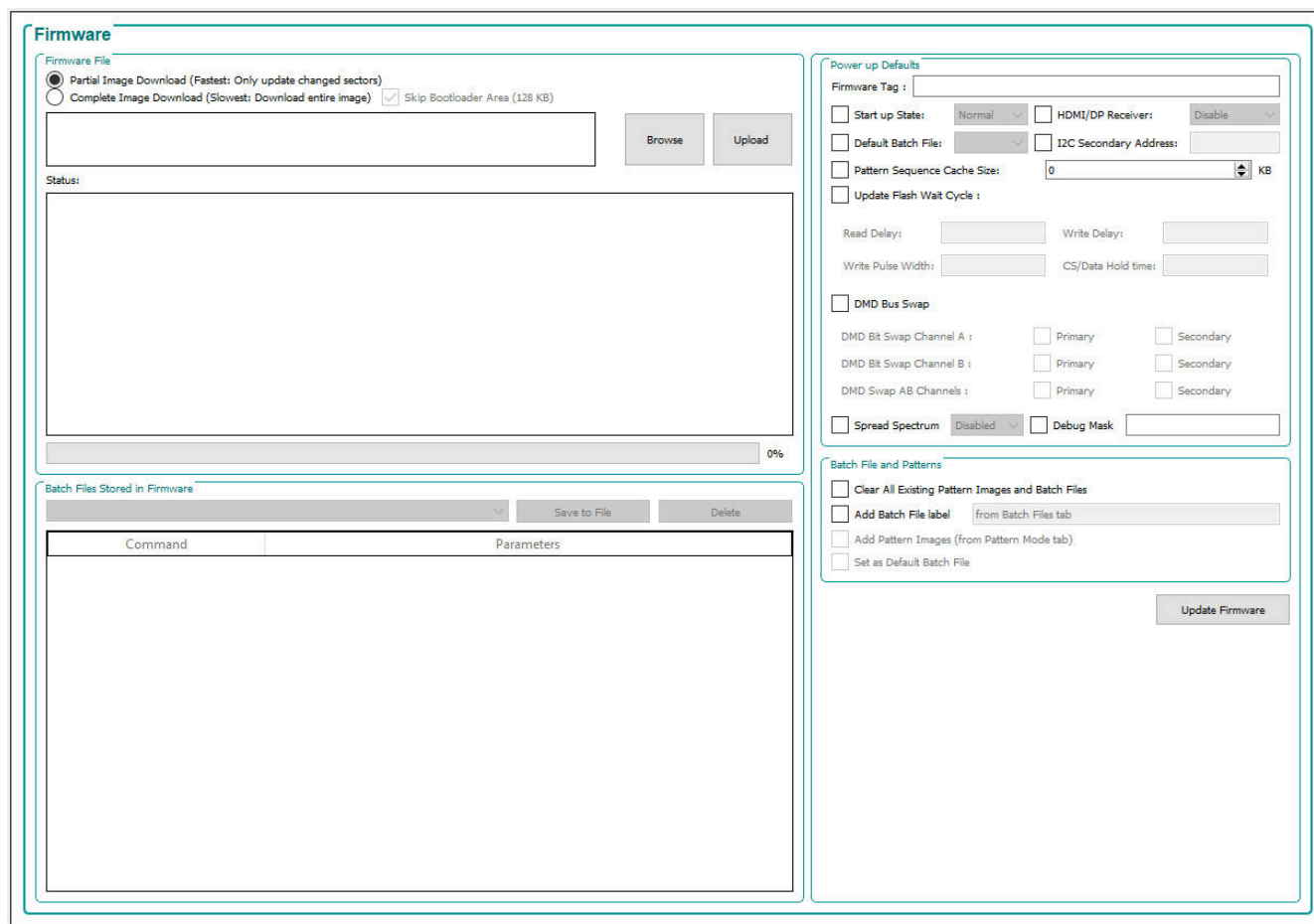
A *Debug Mask* check box was added in GUI 5.3. Check this box to have the debug mask specified in the entry box executed on power up. Copy the string from the Debug Mask shown in the *Debugging Trace* box of the *Debug Mode* tab in [Section 3.8.2](#).

If a default batch file is needed, then see the Batch File section in this guide.

The DLPC900 can also be controlled over the I<sup>2</sup>C interface on I<sup>2</sup>C port 0. The default DLPC900 secondary address is 0x34. This address is programmable using the I<sup>2</sup>C secondary address within the *Power up Defaults* group box.

The GUI permits the user to perform a bus swap on the DMD by checking the *DMD Bus Swap* box and selecting between Channels A and B. This feature is a firmware configuration option that only takes effect after uploading the firmware onto the EVM. However, when using the DLP LightCrafter Dual DLPC900 EVM the user can perform a bus swap on both the primary and the secondary by selecting each check box per desired channel.

The GUI also permits the user to enable Spread Spectrum Clock Generator Support. The user can select from .5%, .75%, or 1% in the drop down menu. This option uses internally controlled clock spreading to help minimize EMI due to high-speed signals. More information on the spread spectrum feature and DMD bus swapping can be found in the [DLPC900 data sheet](#).



**Firmware**

**Firmware File**

☒ Partial Image Download (Fastest: Only update changed sectors)  
☐ Complete Image Download (Slowest: Download entire image) ☒ Skip Bootloader Area (128 KB)

Browse Upload

Status:

0%

**Batch Files Stored in Firmware**

Save to File Delete

| Command | Parameters |
|---------|------------|
|         |            |

**Power-up Defaults**

Firmware Tag:

☐ Start up State: Normal ☐ HDMI/DP Receiver: Disable  
☐ Default Batch File:  ☐ I2C Secondary Address:   
☐ Pattern Sequence Cache Size: 0 KB  
☐ Update Flash Wait Cycle:

Read Delay:  Write Delay:   
 Write Pulse Width:  CS/Data Hold time:

☐ DMD Bus Swap  
 DMD Bit Swap Channel A: ☐ Primary ☐ Secondary  
 DMD Bit Swap Channel B: ☐ Primary ☐ Secondary  
 DMD Swap AB Channels: ☐ Primary ☐ Secondary

☐ Spread Spectrum Disabled ☐ Debug Mask

**Batch File and Patterns**

☐ Clear All Existing Pattern Images and Batch Files  
☐ Add Batch File label from Batch Files tab  
☐ Add Pattern Images (from Pattern Mode tab)  
☐ Set as Default Batch File

Update Firmware

**Figure 3-23. Firmware Panel**

### 3.9.1 Adding or Removing Patterns from the Firmware

For most efficient storage and compression of images, the GUI packs the images into groups of 24-bit RGB bitmap images. If there are 1-bit black and white images, 8-bit gray scale images, or any other image bit-depth (up to 24-bit images), then the images are combined to create a composite image. For example, a composite image composed of 24 1-bit images, three 8-bit images, or a combination of images of various bit-depths which add up to a 24-bit composite image.

#### Note

An image cannot span two 24-bit composite images. For example three 7-bit images can fit in a composite 24-bit image, but a fourth 7-bit image must go into a separate 24-bit image. Only a 3-bit or smaller image can be put into the remaining bits of the first composite 24-bit image.

The GUI then compresses each 24-bit image using the Enhanced Run-Length Encoding described in the *DLPC900 Programmer's Guide*. These compressed images are then decompressed when loaded into the DLPC900 internal memory when operating in *Pre-Stored Pattern Mode* or *Pattern On-The-Fly Mode*.

Based on the DMD connected, the EVM is capable of holding up to 400 1-bit binary/50 8-bit gray scale compressed patterns in **Flash Memory**, or 960 1-bit binary/120 8-bit gray scale patterns. Depending on the compression ratio, more than 400/960 patterns can be stored to have multiple pattern sets for multiple pattern sequences. These patterns are then loaded when the operating mode is set to *Pre-Stored Pattern Mode*. The remainder of this topic applies only to *Pre-Stored Pattern Mode*.

The DLPC900 Single Controller LightCrafter EVMs are pre-loaded with a pattern sequence that is displayed when power is applied to the EVMs. Since the GUI does not recognize the images that are stored in flash

memory, TI recommends to delete all images from flash before storing new ones. When adding images to the *Pattern Design* panel, the GUI always begins with image index Zero. However, the GUI saves the images to the firmware by appending them to the end of the last image that is in the firmware if any.

For example, if there are already two 24-bit images stored in the firmware. These two images have index values of 0 and 1. If then a pattern sequence is created in the *Pattern Design* panel, where the GUI packs all the images into four 24-bit images, and then saved to the firmware, the four images are appended to the firmware and have index values of 2, 3, 4, and 5. When the firmware is uploaded to the EVM and the pattern sequence is started, the sequence of images is 0, 1, 2, 3 rather than 2, 3, 4, 5.

First begin by examining the images that are currently in the firmware.

1. Make sure the EVM is powered up and operating correctly, verifying the *Connected* radio-button is lit green.
2. Display the pre-stored pattern sequence.
3. Select *Video Mode* from the *Operating Mode* group box.
4. Click the *Source Select* drop-down list box, and select *Images From Flash*.
5. Below the *Source Select* drop-down list box, select 0.
6. Click the *Set* button.
7. Continue to index through all the values until a new image is not displayed indicating that the end of the stored images has been reached.

The index value reached (plus one) represents the number of images that are stored in flash.

Return to the video source by selecting the *Parallel RGB* interface from the *Source Select* drop-down list box and click the *Set* button. If there is a video source connected to the EVM, the source is displayed, otherwise a solid curtain is displayed.

### 3.9.1.1 Deleting Images

When deleting images from flash, the flash device is erased and then updated with the firmware that contains the new images. In the event the flash is accidentally corrupted, follow the instructions under JTAG Flash Programming (see [Section 3.11](#)) to reload the original firmware. To avoid accidental erasure or corruption of the flash device, TI recommend to operate in *Pattern On-The-Fly Mode*.

#### CAUTION

The user must exercise caution when selecting the correct firmware file. Make sure the firmware file is version 3.0 or later. Using version 2.0 or earlier renders the EVM inoperable. Version 6.0 or later is recommended.

Follow these steps to begin deleting images from the firmware file:

1. Make sure the EVM is powered up and operating correctly and the *Connected* radio-button is lit green.
2. Display the pre-stored pattern sequence.
3. Click the *Firmware* button on the top of the GUI.
4. Click the *Browse* button.
5. Select an appropriate firmware file or the original firmware file that comes with the EVM.
6. Add a tag name for this updated firmware file by adding a name in the *Firmware Tag* box.
7. Check the box next to *Clear All Existing Patterns Images and Batch Files*.
8. Click the *Update Firmware* button.
9. The GUI displays a pop-up box providing the name of the updated firmware file.
10. Click the *Browse* button and select the firmware file from step 8.

The updated firmware file has no images. TI recommends to rename the updated firmware file with a specific name so that the file can be reused as a firmware file that has no images.

### 3.9.1.2 Adding Images

The bitmap images to be added to the firmware file must have the native resolution of the DMD that is being used. The GUI returns an error if the GUI encounters an incorrect image size. Make sure any images added to the firmware are of the correct resolution.

1. Continuing from the previous set of steps, click the *Browse* button and select the firmware file in step 8 or the renamed version.
2. Select *Pre-Stored Pattern Mode* from the *Operating Mode* group box. The *Pattern Mode* panel is displayed.
3. In the menu bar click the *Load* button and select *add\_72.txt*. The file is typically stored in the folder named *C:\Texas Instruments-DLP\DLPC900REF-SW-x.x\DLPC900REF-GUI\Images and Batch files\*, then *LCR[DMD]\_Images\* folder depending on which DLPC900 DMD EVM is being used. Be sure to select the correct file for the DMD EVM being used. This file adds 72 1-bit images to the *Pattern Design* panel. See [Figure 3-10](#).
4. Click the *Select All Patterns* button from *Menu* bar to select all the images.
5. Under *Bit Information* select bit depth 1 for all images.
6. Enter 120000µs for the exposure time for all images.
7. Enter 10000µs for the dark time for all images.
8. Select color **Red** for all images.
9. Click the *Firmware* button at the top of the GUI.
10. Under *Batch File* check the box next to *Add Patterns Images* and *Clear Existing Patterns*. These patterns are from the *Pattern Design* panel. See [Figure 3-10](#).
11. Under power up defaults update the firmware tag (ex: dlp6500\_6.0.0\_72images)
12. Click the *Update Firmware* button.
13. The GUI displays a pop-up box providing the name of the updated firmware file.
14. Click the *Browse* button and select the firmware file from step 12.
15. Click the *Upload* button and allow firmware to be uploaded to the flash memory.

---

#### Note

When a firmware file is uploaded, a cached version of that file is created on the PC in the original folder location in a subdirectory named *FlashLoaderCache*. Each time a new firmware is uploaded, the GUI compares to the cache file on the PC and only reprograms the new sectors. The cache file is then updated to represent the latest firmware image. Caching leads to significant download speed improvements if the new firmware and the previous firmware are similar.

---

#### Note

If a full reprogramming of the flash is desired, then delete the *FlashLoaderCache* folder or select a firmware image from a different file location that does not have a cached firmware. Programming the firmware to a different board requires deleting the *FlashLoaderCache* subdirectory under the directory where the firmware resides.

16. Once the EVM resets and displays a solid curtain, click the *Pre-Stored Pattern Mode* from the *Operating Mode* group box.
17. The *Pattern Design* panel (See [Figure 3-10](#)) is displayed which includes the pattern images that were added into the panel in step 3 with all the settings from steps 5-8. If the *Pattern Design* panel is not displayed, then click the *Pattern Mode* button at the top of the GUI. Press *Stop* if pattern sequence is currently playing.
18. Click the *Update LUT* button.
19. Click the *Start* button.
20. All 72 1-bit images are displayed with the settings that were entered in steps 5-8.

Adding many images to the firmware is possible without deleting all the images that are currently in the firmware, and also selecting which images and bit positions to choose for different pattern sequences. Doing this requires editing the image index and the bit position in the *Pattern LUT Definition* that is sent to the EVM, and then using the *Batch File* method to upload the *Pattern LUT Definition* and *Configuration* settings to the EVM.

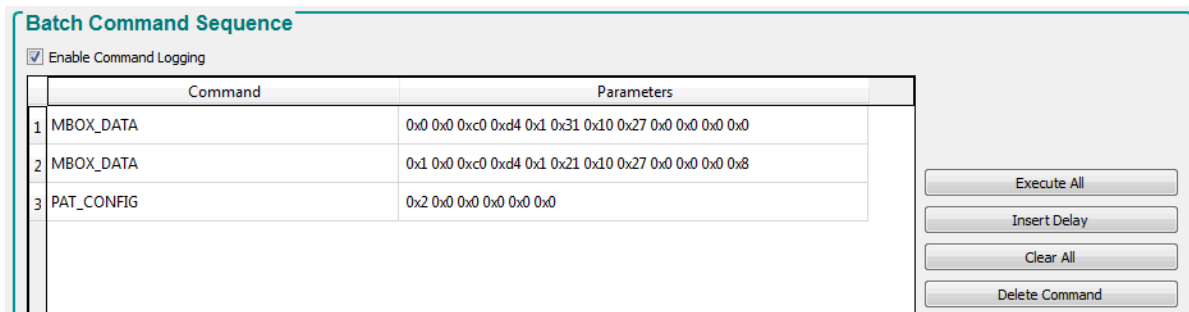
The following example shows how to choose different images and bit positions from the 72 1-bit images that are stored in the firmware.

Follow these steps to edit the image index and bit position of the images to be used for the pattern sequence.

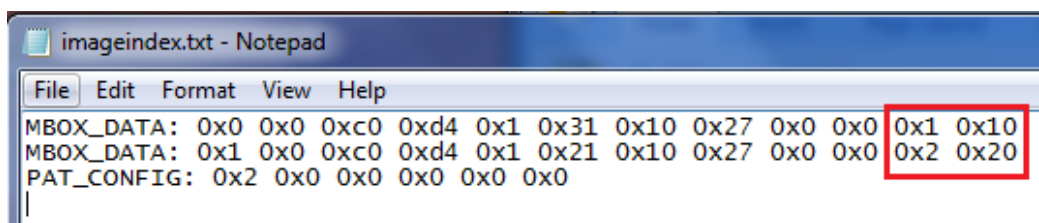
1. Stop the pattern sequence currently running.
2. Delete all images except for the first two in the *Pattern Design* panel. See [Figure 3-10](#).
3. Select the first image and set the color to Yellow.
4. Select the second image and set the color to Green.
5. Click the *Batch File* button at the top of the GUI.
6. Check the *Enable Command Logging* box, and then click the *Clear All* button.
7. Click the *Pattern Mode* button at the top of the GUI.
8. Click the *Update LUT* button.
9. Click the *Batch File* button at the top of the GUI.
10. [Figure 3-24](#) shows the *Pattern LUT Definition and Configuration* of the pattern sequence.
11. Click the *Save Batch File* button to save the contents to a file.
12. Click the *Pattern Mode* button at the top of the GUI.
13. Click the *Start* button and take note of the images that are displayed, and then Click the *Stop* button.
14. Click the *Batch File* button at the top of the GUI.
15. Click the *Clear All* button and uncheck the *Enable Command Logging* box.
16. Use Notepad or a similar editor to open and edit the batch file saved in step 11. The file is typically stored in the folder named *C:\Texas Instruments-DLP\DLPC900REF-SW-5.2.0\DLPC900REF-GUI\Images and Batch files\*, then *LCR[DMD]\_Images\* folder depending on which DLPC900 DMD EVM is being used. Change the image index and bit position as seen in [Figure 3-25](#). See the DLPC900 Programmer's Guide ([DLPU018](#)) for details of the Pattern LUT Definition and Configuration commands.
17. [Figure 3-25](#) shows the image index and the bit position of the images for the two patterns, where the values have been changed compared to the values in [Figure 3-24](#). Click the *Save* to save the new batch file.
18. In the *Batch File* tab click the *Clear All* button, and then click the *Load Batch File* button and load the file from step 17.
19. Click the *Execute All* button.
20. Click the *Pattern Mode* button at the top of the GUI.
21. Click the *Start* button.

Notice that two different images or bit positions are displayed compared to the images in step 13.

Therefore, many different images can be added to the firmware and knowing the image index and the bit positions of each image, the user can create different *Pattern LUT Definition and Configuration* batch files that select different images and bit positions from the images stored in the firmware.



**Figure 3-24. Pattern LUT Definition and Configuration**



**Figure 3-25. Updating the Index and Bit Position**

### 3.9.1.3 Adding Both Images and Batch Files

This section covers how to add pattern images and a batch file to the firmware. These steps allow the user to add patterns to the flash and have them automatically uploaded to the DLPC900 upon power up. The GUI is not connected when starting this process.

1. Make sure the EVM is powered up and operating correctly, and the Connected radio-button is lit green.
2. Navigate to the Batch File page select Clear All and then check the Enable Command Log box.
3. In the operating mode box on the left of the GUI select *Pattern on-the-fly Mode*.
4. Navigate to the pattern mode tab and add images.
5. Adjust exposure settings and other pattern configurations to desired values.
6. Click the update LUT button.
7. Click the start button if desired for the pattern sequence to be running upon start-up.
8. Navigate to the Firmware Page and browse to select a firmware to be modified.
9. Check all four boxes in the Batch File and Patterns Box on the bottom right of the firmware page.
10. Type in a name for the *Firmware Tag* and *Batch File* label.
11. Press the *Update Firmware* button.

These steps generate a new firmware image containing the pattern images and batch file located in the pattern mode tab and the batch file tab respectively. This firmware file is ready to be uploaded to the system when connected. If the user desires to load a previously created batch file, then make sure that the batch file begins with a DISP\_MODE : 0x03 command and ends with a PAT\_START\_STOP : 0x02 command to have the pre-stored patterns run upon start-up.

---

#### Note

An alternate method is to build a firmware adding only the images. Then add the batch file that updates the LUT and runs the images.

---

### 3.9.2 Reprogram Firmware for a Different Supported DMD

To reprogram the firmware to support a different DMD, perform the following:

1. With the board powered off, place the boot hold jumper onto the EVM.
2. Power up the EVM. Upon power up, the default DMD in the GUI is the DLP6500.
3. Select the new target device from the GUI menu.
4. In the Firmware tab, click the Browse button. Search for the firmware in the Texas Instruments-DLP\ directory that matches the target DMD. For the single controller EVM this is either the DLPLCR65EVM or the DLPLCR55EVM. Choose either the OOB image or a custom firmware that matches the target device.
5. Click the Upload button and wait for the process to complete.
6. Power down the EVM and remove the boot hold jumper.
7. Attach the new hardware and power on the EVM.



### 3.10 Flash Device Parameters

For EVM use with several different flash memory parts, the user can edit the FlashDeviceParameters.txt file to match the flash memory part that has been installed with the EVM. This file is located in the DLPC900REF-SW-x.x.x\DLPC900REF-GUI\Flash directory of the [DLPC900REF-SW](#) bundle. An example with the Infineon S70GL02GS part is shown in [Figure 3-26](#) where the user comments out all lines EXCEPT for the top line which includes the chosen Infineon flash part. More detailed information can be found in the comments at the top of the text file.

```
Version, 4
// Flash Device Information
// DLPC900 information updated 3/1/2023
//
// The GUI reads mfg ID and device ID in sequential order in this file.
// The device used in the hardware MUST be first in the list in the case that IDs are identical with another part.
//
// M29DW128G and JS28F128M parts are no longer available to purchase.
//
// Field Descriptions:
// Mfg - Manufacturer Name
// MfgID - Manufacturer ID
// LMfgID - Long Manufacturer ID
// Device - Part name
// DevID - Device ID
// LDevID - Long Device ID
// Mb - Flash size in Megabits
// Alg - Flash also should be set to 0
// Size - Flash size in bytes
// #sec - Total number of sectors
// Sector_Addresses - Starting address of each sector. Include the sector addresses up to 16MB.

// Mfg      MfgID    LMfgID,          Device  DevID    LDevID,          Mb Alg    Size #sec  Sector_Addresses
"Infineon", 0x0001, 0x0000000000000001, "S70GL02GS", 0x227E, 0x000000000000227E, 128, 0, 0x100000, 128, 0x0, 0x2000, 0x40
0xBA0000, 0xBC0000, 0xBE0000, 0xC00000, 0xC20000, 0xC40000, 0xC60000, 0xC80000, 0xCA0000, 0xCC0000, 0xCE0000, 0xD00000, 0xD200
// "Micron", 0x0020, 0x0000000000000020, "M29DW128G", 0x227E, 0x000000000000227E, 128, 0, 0x100000, 70, 0, 0x1000, 0x2000
// "Micron", 0x0089, 0x0000000000000089, "JS28F032M", 0x227E, 0x000000000000227E, 32, 0, 0x100000, 128, 0x0, 0x1000, 0x2000
// "Micron", 0x0089, 0x0000000000000089, "JS28F064M", 0x227E, 0x000000000000227E, 64, 0, 0x100000, 128, 0x0, 0x1000, 0x2000
6D0000, 0x6E0000, 0x6F0000, 0x700000, 0x710000, 0x720000, 0x730000, 0x740000, 0x750000, 0x760000, 0x770000, 0x780000, 0x7900
// "Micron", 0x0089, 0x0000000000000089, "JS28F128M", 0x227E, 0x000000000000227E, 128, 0, 0x100000, 128, 0x0, 0x2000, 0x40
0xBA0000, 0xBC0000, 0xBE0000, 0xC00000, 0xC20000, 0xC40000, 0xC60000, 0xC80000, 0xCA0000, 0xCC0000, 0xCE0000, 0xD00000, 0xD200
// "Micron", 0x0089, 0x0000000000000089, "MT28EW256", 0x227E, 0x000000000000227E, 128, 0, 0x100000, 128, 0x0, 0x2000, 0x40
0xBA0000, 0xBC0000, 0xBE0000, 0xC00000, 0xC20000, 0xC40000, 0xC60000, 0xC80000, 0xCA0000, 0xCC0000, 0xCE0000, 0xD00000, 0xD200
// "Micron", 0x0089, 0x0000000000000089, "MT28EW512", 0x227E, 0x000000000000227E, 128, 0, 0x100000, 128, 0x0, 0x2000, 0x40
0xBA0000, 0xBC0000, 0xBE0000, 0xC00000, 0xC20000, 0xC40000, 0xC60000, 0xC80000, 0xCA0000, 0xCC0000, 0xCE0000, 0xD00000, 0xD200
// "Infineon", 0x0001, 0x0000000000000001, "S29GL064P", 0x227E, 0x000000000000227E, 64, 0, 0x80000, 128, 0x0, 0x1000, 0x2000
```

**Figure 3-26. Flash Device Parameters Text File**

### 3.11 JTAG Flash Programming

The DLPC900 JTAG Programmer Tool is included in the [DLPC900REF-SW](#) bundle, allowing a user to program the bootloader image into the flash using the JTAG boundary scan connector if, for example, the bootloader becomes corrupted and the board is rendered inoperable. The tool can also be used for updating the entire firmware image. Programming the entire firmware image using this method can take a long time. TI recommends to program only the bootloader using this method, and then use the DLP LightCrafter DLPC900 GUI with a USB connection to upload the entire firmware image. The following steps are for programming only the bootloader image.

#### Note

Before using JTAG to reload the bootloader, first install the BOOTHold jumper (J11) and turn the system on with the USB cable connected to the PC. The system shows as connected in the GUI but does not recognize the attached DMD. Manually select the button that matches the current DMD and navigate to the firmware tab. Select the correct firmware image and reprogram. Then power down, remove the BOOTHold jumper, and restart the system. Reprogramming in this manner often recovers a system without requiring to reload the bootloader through JTAG.

The DLPC900 JTAG Programmer Tool requires the UM232H module with modifications, which is manufactured by Future Technology Devices International Ltd (FTDI Chip). The UM232H module can be purchased from Digi-key with the following part number: 768-1103-ND. The driver can be downloaded from the FTDI website ([www.ftdichip.com](http://www.ftdichip.com)). Select the VCP CDM WHQL Certified driver for Windows.

Make the following modifications to the UM232H and use [Figure 3-27](#) and [Figure 3-28](#) as a guide.

#### Connection Details

1. BUS power configuration
  - Short PIN #2 and PIN #3 of J1 (SYS PWR)
  - Short PIN #2 and PIN #3 of J2 (I/O PWR)
2. J2-6 → GND (PWR)
3. J2-7 → TCK (OUTPUT)
4. J2-8 → TDO1 (OUTPUT)
5. J2-9 → TDI (INPUT)
6. J2-10 → TMS1 (OUTPUT)
7. J2-11 → TRSTZ (OUTPUT)

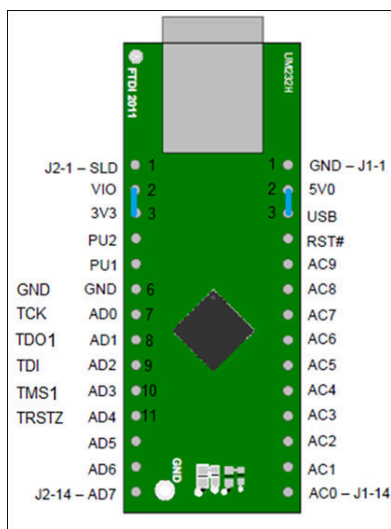


Figure 3-27. UM232H Module

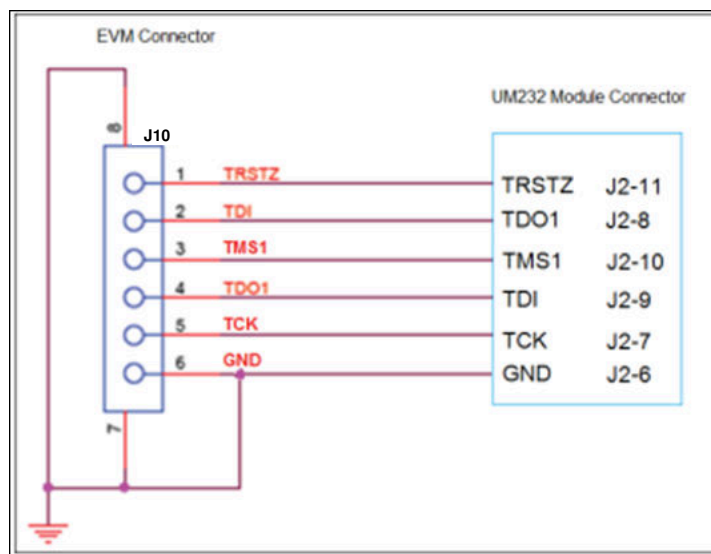


Figure 3-28. UM232H Wiring Diagram

Choose the driver found in the CDM WHQL Certified zip folder and allow driver installation to complete. Install a jumper at J9 and J11 on the DLP LightCrafter Single DLPC900 EVM. Connect the JTAG signals at J10 on the DLP LightCrafter Single DLPC900 EVM to the UM232H module as shown in [Figure 3-28](#). Finally, connect the UM232H to the PC with USB cable.

---

#### Note

The JTAG programmer has a copy of the FlashDeviceParameters.txt file, which is located in the DLPC900REF-SW-x.x.x\DLPC900REF-JTAG directory of the [DLPC900REF-SW](#) bundle. See [Section 3.10](#) for instructions on how to edit this file to match the flash. If users have already made changes to the FlashDeviceParameters.txt file in the DLPC900REF-SW-x.x.x\DLPC900REF-GUI\Flash directory that work with the GUI, then users can copy that file to the DLPC900REF-SW-x.x.x\DLPC900REF-JTAG directory.

---

Start the DLPC900 Flash Programmer program and select the BoardFile.brd and the bootloader.bin included the install folder. Select USB as communication method from the Settings menu.

Power-up the EVM board and click the *Detect Chain* tool button. The tool detects the DLPC900 in JTAG chain. On the EVM notice the **Red/Green** LEDs (D5 & D6) light up to confirm the DLPC900 is currently connected.

Click the *Flash Info* button and confirm the correct flash part is detected by the tool. Using the *Flash Erase* button select the first sectors (to cover first 128 kilobytes) and erase these sectors.

Once sector erase is complete, set the *Start Address* (HEX) to 0x00000000 and set the *Size* (HEX) to 0x20000. Then click the *Program Flash* button, and then wait until write is complete.

---

#### Note

Make sure that write is 100% complete.

---

Finally, power off the board and remove the UM232H JTAG programmer, remove the jumpers at J9 and J11, and then power the EVM back on. Plug in a USB cable between the EVM and the PC and allow the PC to detect the EVM USB connection.

### 3.12 Programming an EDID

The EDID can be programmed through two methods.

To program the EDID through HDMI:

1. Remove power from the EVM and install the jumper on J3 and remove jumper on J4 on the EVM to enable the HDMI EDID.
2. Plug in the HDMI cable to the EVM. Plug other end of cable to a PC or video generator and apply power to the EVM.
3. Run the GUI and click the Batch Files button at the top of the GUI.
4. Click the Load Batch File button and browse for the correct HDMI\_EDID.txt file.
5. Click the Execute All button.
6. Remove power from the EVM and remove jumper from J3.

To program the EDID through Display Port:

1. Remove power from the EVM and install the jumper on J4 and remove jumper on J3 on the EVM to enable the Display Port EDID.
2. Remove the HDMI and Display Port cable from the EVM and apply power to the EVM.
3. Run the GUI and click the Batch Files button at the top of the GUI.
4. Click the Load Batch File button and browse for correct DP\_EDID.txt file.
5. Click the Execute All button.
6. Remove power from the EVM and remove the jumper from J4.

## 4 Connectors

This chapter describes the connector pins of the DLP LightCrafter Single DLPC900 EVM.

### 4.1 Input Trigger Connectors

The input trigger connector J20 on the DLP LightCrafter Single DLPC900 EVM pins are listed in [Table 4-1](#). The trigger inputs have hysteresis. Two matching six-pin, 1.25mm connector part numbers are:

- Molex part number: 51021-0600
- Digi-Key part number: WM1724-ND

The corresponding connector terminal (crimp) part numbers are:

- Molex part number: 50079-8100
- Digi-Key part number: WM2023-ND

**Table 4-1. Trigger Input Connector Pins**

| Description         | Pin | Supply Range                        |
|---------------------|-----|-------------------------------------|
| Trigger In 1 Supply | 1   | External or internal 1.8V and 3.3V. |
| Trigger In 1        | 2   |                                     |
| Ground              | 3   | Ground                              |
| Trigger In 2 Supply | 4   | External or internal 1.8V and 3.3V. |
| Trigger In 2        | 5   |                                     |
| Ground              | 6   | Ground                              |

### 4.2 Output Trigger Connectors

The output trigger connector J24 on the DLP LightCrafter Single DLPC900 EVM pins are listed in [Table 4-2](#). Two matching six-pin, 1.25mm connector part numbers are:

- Molex part number: 51021-0600
- Digi-Key part number: WM1724-ND

The corresponding connector terminal (crimp) part numbers are:

- Molex part number: 50079-8100
- Digi-Key part number: WM2023-ND

**Table 4-2. Trigger Output Connector Pins**

| Description          | Pin | Supply Range                        |
|----------------------|-----|-------------------------------------|
| Trigger Out 1 Supply | 1   | External or internal 1.8V and 3.3V. |
| Trigger Out 1        | 2   |                                     |
| Ground               | 3   | Ground                              |
| Trigger Out 2 Supply | 4   | External or internal 1.8V and 3.3V. |
| Trigger Out 2        | 5   |                                     |
| Ground               | 6   | Ground                              |

### 4.3 DLPC900 UART

The UART output connector J6 on the DLP LightCrafter Single DLPC900EVM is shown in [Table 4-3](#) and is used solely for debug message output to a terminal. The TX out is 3.3V LVTTL level and requires an external transceiver to convert the LVTTL level signals to RS232. The connector is a 6 pin header.

#### Note

This header must be included in any design for debug purposes.

**Table 4-3. UART Connector Pins**

| Description      | Pin | Supply Range |
|------------------|-----|--------------|
| Power            | 1   | 3.3V         |
| TX               | 2   | 3.3V         |
| RX               | 3   | 3.3V         |
| CTS <sub>Z</sub> | 4   | 3.3V         |
| RTS <sub>Z</sub> | 5   | 3.3V         |
| Ground           | 6   | 0V           |

### 4.4 DLPC900 I<sup>2</sup>C Port 0

The I<sup>2</sup>C<sub>0</sub> connector J13 on the DLP LightCrafter Single DLPC900 EVM pins are shown in [Table 4-4](#). Two matching four-pin, 1.25mm connector part numbers are:

- Molex part number: 51021-0400
- Digi-Key part number: WM1722-ND

The corresponding terminal (crimp) part numbers are:

- Molex part number: 50079-8100
- Digi-Key part number: WM2023-ND

**Table 4-4. I<sup>2</sup>C Port 0 Connector Pins**

| Description          | Pin | Supply Range |
|----------------------|-----|--------------|
| I <sup>2</sup> C SCL | 1   | 3.3V         |
| I <sup>2</sup> C SDA | 2   | 3.3V         |
| 3.3V supply          | 3   | 3.3V         |
| Ground               | 4   | 0V           |

### 4.5 DLPC900 I<sup>2</sup>C Port 1

The I<sup>2</sup>C<sub>1</sub> connector J14 pins are shown in [Table 4-5](#) (see note). Two matching four-pin, 1.25mm connector part numbers are:

- Molex part number: 51021-0400
- Digi-Key part number: WM1722-ND

The corresponding terminal (crimp) part numbers are:

- Molex part number: 50079-8100
- Digi-Key part number: WM2023-ND

**Table 4-5. I<sup>2</sup>C Port 1 Connector Pins**

| Description          | Pin | Supply Range |
|----------------------|-----|--------------|
| I <sup>2</sup> C SCL | 1   | 3.3V         |
| I <sup>2</sup> C SDA | 2   | 3.3V         |
| 3.3V supply          | 3   | 3.3V         |
| Ground               | 4   | 0V           |

## 4.6 DLPC900 I<sup>2</sup>C Port 2

The I<sup>2</sup>C\_2 connector J15 pins are shown in [Table 4-6](#). Two matching four-pin, 1.25mm connector part numbers are:

- Molex part number: 51021-0400
- Digi-Key part number: WM1722-ND

The corresponding terminal (crimp) part numbers are:

- Molex part number: 50079-8100
- Digi-Key part number: WM2023-ND

**Table 4-6. I<sup>2</sup>C Port 2 Connector Pins**

| Description          | Pin | Supply Range |
|----------------------|-----|--------------|
| I <sup>2</sup> C SCL | 1   | 3.3V         |
| I <sup>2</sup> C SDA | 2   | 3.3V         |
| 3.3V supply          | 3   | 3.3V         |
| Ground               | 4   | 0V           |

## 4.7 JTAG Boundary Scan

The JTAG Boundary connector J10 on the DLP LightCrafter Single DLPC900 EVM pins are listed in [Table 4-7](#). Two matching six-pin, 1.25mm connector part numbers are:

- Molex part number: 51021-0600
- Digi-Key part number: WM1724-ND

The corresponding terminal (crimp) part numbers are:

- Molex part number: 50079-8100
- Digi-Key part number: WM2023-ND

**Table 4-7. JTAG Boundary Scan Connector Pins**

| Description | Pin | Supply Range |
|-------------|-----|--------------|
| TRSTZ       | 1   | 3.3V         |
| TDI         | 2   | 3.3V         |
| TMS1        | 3   | 3.3V         |
| TDO1        | 4   | 3.3V         |
| TCK         | 5   | 3.3V         |
| Ground      | 6   | Ground       |

## 4.8 GPIO and PWM

The GPIO and PWM connector J12 on the DLP LightCrafter Single DLPC900 EVM pins are listed in [Table 4-8](#). Two matching 14-pin, 2.00mm connector part numbers are:

- Molex part number: 87832-1420
- Digi-Key part number: WM18641-ND

A corresponding mating connector part numbers are:

- Molex part number: 87568-1493
- Digi-Key part number: WM14314-ND



**Table 4-8. GPIO and PWM Connector Pins**

| Description | Pin | Supply Range |
|-------------|-----|--------------|
| 3.3V        | 1   | 3.3V         |
| 3.3V        | 2   | 3.3V         |
| GPIO_PWM_00 | 3   | 3.3V         |
| GPIO_04     | 4   | 3.3V         |
| GPIO_PWM_01 | 5   | 3.3V         |
| GPIO_05     | 6   | 3.3V         |
| GPIO_PWM_02 | 7   | 3.3V         |
| GPIO_06     | 8   | 3.3V         |
| GPIO_PWM_03 | 9   | 3.3V         |
| GPIO_07     | 10  | 3.3V         |
| GPIO_08     | 11  | 3.3V         |
| Ground      | 12  | Ground       |
| Ground      | 13  | Ground       |
| Ground      | 14  | Ground       |

## 4.9 Power

The power socket J17 on the DLP LightCrafter Single DLPC900 pins are shown in [Power Connector Pins for J17](#). Two matching connector part numbers are:

- Switchcraft part number: 760
- Digi-Key part number: SC1051-ND

**Table 4-9. Power Connector Pins for J17**

| Description  | Pin | Supply Range    |
|--------------|-----|-----------------|
| Input Supply | 1   | 12V DC –5%/+10% |
| Ground       | 2   | 0V              |
| Ground       | 3   | 0V              |

The power socket J18 on the DLP LightCrafter Single DLPC900 pins are shown in [Table 4-10](#). Two matching connector part numbers are:

- Molex part number: 39039062
- Digi-Key part number: WM12978-ND

**Table 4-10. Power Connector Pins for J18**

| Description  | Pin | Supply Range    |
|--------------|-----|-----------------|
| Ground       | 1   | 0V              |
| Ground       | 2   | 0V              |
| Ground       | 3   | 0V              |
| Input Supply | 4   | 12V DC –5%/+10% |
| Input Supply | 5   | 12V DC –5%/+10% |
| Input Supply | 6   | 12V DC –5%/+10% |

## 4.10 External Parallel Video Connector

The external parallel video connector J26 on the DLP Lightcrafter Single DLPC900 EVM is a 120-pin connector header. A matching connector part number is:

- Samtec part number: QSH-060-01-L-D-A
- Digi-Key part number: SAM8181-ND

## 5 Power Supply Requirements

### 5.1 External Power Supply Requirements

The DLP LightCrafter Single DLPC900 EVM does not include a power supply. The external power supply requirements are:

- Nominal voltage: 12V DC -5%/+10%
- Minimum current: 5A
- Maximum Current: 7A
- J17 DC connector size:
  - Inner diameter: 2.5mm
  - Outer diameter: 5.5mm
  - Shaft: 9.5mm female, center positive
- A recommended power supply is [Digi-Key part number 102-3811-ND](#), or equivalent
- J18 matching connector part numbers:
  - Molex part number: 39039062
  - Digi-Key part number: WM12978-ND

---

#### Note

External Power Supply Regulatory Compliance Certifications: recommend selection and use of an external power supply which meets TI's required minimum electrical ratings in addition to complying with applicable regional product regulatory and safety certification requirements such as (by example) UL, CSA, VDE, CCC, PSE, and so forth.

---

## 6 Safety

### 6.1 Caution Labels

#### CAUTION



To minimize the risk of fire or equipment damage, make sure that air is allowed to circulate freely around the DLP LightCrafter DLPC900 EVM board when operating.

#### CAUTION



The kit contains ESD-sensitive components. Handle with care to prevent permanent damage.

## 7 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

| Changes from Revision D (July 2024) to Revision E (October 2025) | Page |
|------------------------------------------------------------------|------|
| • Added HDMI trademark information.....                          | 1    |

## STANDARD TERMS FOR EVALUATION MODULES

1. *Delivery:* TI delivers TI evaluation boards, kits, or modules, including any accompanying demonstration software, components, and/or documentation which may be provided together or separately (collectively, an "EVM" or "EVMs") to the User ("User") in accordance with the terms set forth herein. User's acceptance of the EVM is expressly subject to the following terms.
  - 1.1 EVMs are intended solely for product or software developers for use in a research and development setting to facilitate feasibility evaluation, experimentation, or scientific analysis of TI semiconductors products. EVMs have no direct function and are not finished products. EVMs shall not be directly or indirectly assembled as a part or subassembly in any finished product. For clarification, any software or software tools provided with the EVM ("Software") shall not be subject to the terms and conditions set forth herein but rather shall be subject to the applicable terms that accompany such Software
  - 1.2 EVMs are not intended for consumer or household use. EVMs may not be sold, sublicensed, leased, rented, loaned, assigned, or otherwise distributed for commercial purposes by Users, in whole or in part, or used in any finished product or production system.
2. *Limited Warranty and Related Remedies/Disclaimers:*
  - 2.1 These terms do not apply to Software. The warranty, if any, for Software is covered in the applicable Software License Agreement.
  - 2.2 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for a nonconforming EVM if (a) the nonconformity was caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI, (b) the nonconformity resulted from User's design, specifications or instructions for such EVMs or improper system design, or (c) User has not paid on time. Testing and other quality control techniques are used to the extent TI deems necessary. TI does not test all parameters of each EVM. User's claims against TI under this Section 2 are void if User fails to notify TI of any apparent defects in the EVMs within ten (10) business days after delivery, or of any hidden defects with ten (10) business days after the defect has been detected.
  - 2.3 TI's sole liability shall be at its option to repair or replace EVMs that fail to conform to the warranty set forth above, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

### **WARNING**

**Evaluation Kits are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems.**

**User shall operate the Evaluation Kit within TI's recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI's recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI's instructions for electrical ratings of interface circuits such as input, output and electrical loads.**

#### NOTE:

EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGRADATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.

### 3 Regulatory Notices:

#### 3.1 United States

##### 3.1.1 Notice applicable to EVMs not FCC-Approved:

**FCC NOTICE:** This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

##### 3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

#### **CAUTION**

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

#### **FCC Interference Statement for Class A EVM devices**

*NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a residential area. If this equipment does cause harmful interference in which case the user will be required to correct the interference at his own expense.*

#### **FCC Interference Statement for Class B EVM devices**

*NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:*

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

#### 3.2 Canada

##### 3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

#### **Concerning EVMs Including Radio Transmitters:**

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

#### **Concernant les EVMs avec appareils radio:**

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

#### **Concerning EVMs Including Detachable Antennas:**

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

### Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

#### 3.3 Japan

3.3.1 *Notice for EVMs delivered in Japan:* Please see [http://www.tij.co.jp/sds/ti\\_ja/general/eStore/notice\\_01.page](http://www.tij.co.jp/sds/ti_ja/general/eStore/notice_01.page) 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。

<https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-delivered-in-japan.html>

3.3.2 *Notice for Users of EVMs Considered "Radio Frequency Products" in Japan:* EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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3.3.3 *Notice for EVMs for Power Line Communication:* Please see [http://www.tij.co.jp/sds/ti\\_ja/general/eStore/notice\\_02.page](http://www.tij.co.jp/sds/ti_ja/general/eStore/notice_02.page)

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#### 3.4 European Union

3.4.1 *For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive):*

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.



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4 *EVM Use Restrictions and Warnings:*

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

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