











TPS7B6933, TPS7B6950

SLVSDI2-APRIL 2016

TPS7B69xx 150-mA, 40-V Ultralow-Quiescent-Current LDO

Features

- 4-V to 40-V Wide V_I Input Voltage Range With up to 45-V Transient
- Maximum Output Current: 150 mA
- Low Quiescent Current (I_O):
 - 15 µA Typical at Light Loads
 - 25 µA Maximum Under Full Temperature
- 450-mV Typical Low Dropout Voltage at 100 mA **Load Current**
- 10-mV Line Regulation Maximum
- 20-mV Load Regulation Maximum
- Stable With Low-ESR Ceramic Output Capacitor $(2.2 \mu F \text{ to } 100 \mu F)$
- Fixed 3.3-V and 5-V Output Voltage Options
- Integrated Fault Protection:
 - Thermal Shutdown
 - **Short-Circuit Protection**
- Packages:
 - 5-Pin SOT-23 Package
 - 4-Pin SOT-223 Package

2 Applications

- E-meters, Water Meters and Gas Meters
- Appliances and White Goods
- Fire Alarm. Smoke Detector
- Medical, Health, and Fitness Applications

3 Description

The TPS7B69xx device is a low-dropout linear regulator that operates at up to 40-V V_I. With only 15-µA (typical) quiescent current at light load, the device is applicable for standby microcontrol-unit systems, especially for always-on applications like emeters, fire alarms, and smoke detectors.

The devices have integrated short-circuit and overcurrent protection. The TPS7B69xx device operates over a -40°C to 105°C temperature range.

Device Information⁽¹⁾

PART NUMBER	PACKAGE	BODY SIZE (NOM)
TPS7B6933	SOT-223 (4)	6.50 mm × 3.50 mm
TPS7B6950	SOT-23 (5)	2.90 mm x 1.60 mm

(1) For all available packages, see the orderable addendum at the end of the datasheet.

Typical Application Schematic

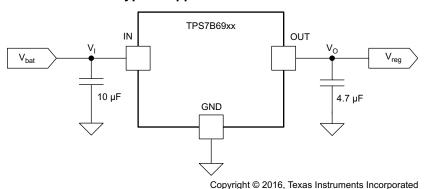




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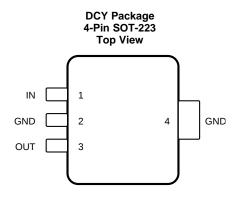
4 Revision History

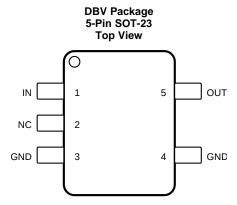
DATE	REVISION	NOTE
April 2016	*	Initial release

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5 Pin Configuration and Functions





NC - No internal connection

Pin Functions

	1 1								
	PIN								
NAME	NO.		NO.		TYPE	DESCRIPTION			
NAME	SOT-223	SOT-23							
GND	2, 4	3, 4	G	Ground reference					
IN	1	1	Р	Input power-supply voltage					
NC	_	2	_	Not connected pin					
OUT	3	5	Р	Output voltage					



6 Specifications

6.1 Absolute Maximum Ratings

over operating ambient temperature range (unless otherwise noted)⁽¹⁾

		MIN	MAX	UNIT
Unregulated input voltage	IN ⁽²⁾⁽³⁾	-0.3	45	V
Regulated output voltage	OUT ⁽²⁾	-0.3	7	V
Operating junction temperature ran	ie, T _J	-40	125	°C
Storage temperature, T _{stg}		-65	150	°C

⁽¹⁾ Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

6.2 ESD Ratings

			VALUE	UNIT
		Human-body model (HBM), per ANSI/ESDA/JEDEC JS-001 (1)	±2000	
V _(ESD)	V _(ESD) Electrostatic discharge	Charged-device model (CDM), per JEDEC specification JESD22-C101 (2)	±500	V

⁽¹⁾ JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

6.3 Recommended Operating Conditions

over operating ambient temperature range (unless otherwise noted)

		MIN	MAX	UNIT
V_{I}	Unregulated input voltage	4	40	V
Vo	Output voltage	0	5.5	٧
Co	Output capacitor requirements ⁽¹⁾	2.2	100	μF
ESR _{CO}	Output ESR requirements ⁽²⁾	0.001	2	Ω
T _A	Operating ambient temperature range	-40	105	°C

⁽¹⁾ The output capacitance range specified in this table is the effective value.

6.4 Thermal Information

		TPS7		
	THERMAL METRIC ⁽¹⁾⁽²⁾	DCY (SOT-223)	DBV (SOT-23)	UNIT
		4 PINS	5 PINS	
$R_{\theta JA}$	Junction-to-ambient thermal resistance	64.2	210.4	°C/W
R ₀ JC(top)	Junction-to-case (top) thermal resistance	46.8	126.1	°C/W
$R_{\theta JB}$	Junction-to-board thermal resistance	13.3	38.4	°C/W
ΨЈТ	Junction-to-top characterization parameter	6.3	16	°C/W
ΨЈВ	Junction-to-board characterization parameter	13.2	37.5	°C/W

⁽¹⁾ The thermal data is based on the JEDEC standard high-K profile, JESD 51-7, 2s2p four layer board with 2-oz copper. The copper pad is soldered to the thermal land pattern. Also correct attachment procedure must be incorporated.

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⁽²⁾ All voltage values are with respect to the GND terminal.

³⁾ Absolute maximum voltage, withstands 45 V for 200 ms.

⁽²⁾ JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

⁽²⁾ Relevant ESR value at f = 10 kHz

⁽²⁾ For more information about traditional and new thermal metrics, see the IC Package Thermal Metrics application report, SPRA953.



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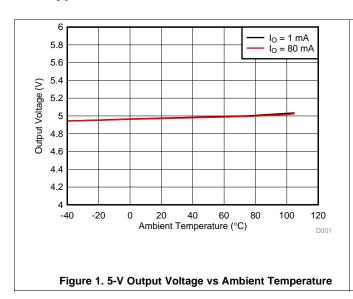
6.5 Electrical Characteristics

 $V_{IN} = 14 \text{ V}$, 1 m Ω < ESR < 2 Ω , $T_{J} = -40 ^{\circ}\text{C}$ to 125 $^{\circ}\text{C}$ (unless otherwise noted)

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
SUPPLY V	OLTAGE AND CURRENT (IN)						
V	Input voltage	Fixed 3.3-V output, I _O = 1 mA	4		40	V	
VI	input voltage	Fixed 5-V output, I _O = 1 mA	5.5		40	V	
IQ	Quiescent current	Fixed 3.3-V version, V_I = 4 to 40 V, , I_O = 0.2 mA Fixed 5-V version, V_I = 5.5 to 40 V, I_O = 0.2 mA		15	25	μΑ	
V _{IN(UVLO)}	IN undervolte as detection	Ramp V _I up until the output turns on	3.65			V	
VIN(UVLO)	IN undervoltage detection	Ramp V _I down until the output turns OFF			3	V	
REGULAT	ED OUTPUT (OUT)						
	Demilated autout	Fixed 3.3-V version, $V_I = 5$ to 40 V, $I_O = 1$ to 150 mA	-3%		3%		
Vo	Regulated output	Fixed 5-V version, $V_I = 6.5$ to 40 V, $I_O = 1$ to 150 mA	-3%		3%		
$\Delta V_{O(\Delta VI)}$	Line regulation	$V_I = 6 \text{ to } 40 \text{ V}, \Delta V_O, I_O = 10 \text{ mA}$			10	mV	
$\Delta V_{O(\Delta IL)}$	Load regulation	$I_O = 1$ to 150 mA, ΔV_O			20	mV	
7	Dropout voltage	Fixed 3.3-V version, $V_I - V_O$, $I_O = 50 \text{ mA}$			799		
		Fixed 3.3-V version, $V_I - V_O$, $I_O = 100 \text{ mA}$			800	.,	
V_{DROP}		Fixed 5-V version, $V_I - V_O$, $I_O = 50 \text{ mA}$		220	400	mV	
		Fixed 5-V version, $V_I - V_O$, $I_O = 100 \text{ mA}$		450	800		
Io	Output current	V _O in regulation	0		150	mA	
I _{OCL}	Output current-limit	OUT short to ground	150		500	mA	
PSRR	Power supply ripple rejection (1)	$V_{\rm rip}$ = 0.5 $V_{\rm pp}$, Load = 10 mA, f = 100 Hz, $C_{\rm O}$ = 2.2 $\mu{\rm F}$		60		dB	
OPERATI	NG TEMPERATURE RANGE						
T _{sd}	Junction shutdown temperature			175		°C	
T _{hys}	Hysteresis of thermal shutdown			25		°C	

⁽¹⁾ Design Information—Not tested, ensured by characterization.

6.6 Typical Characteristics



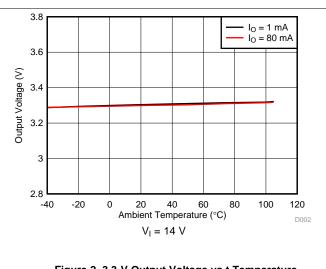
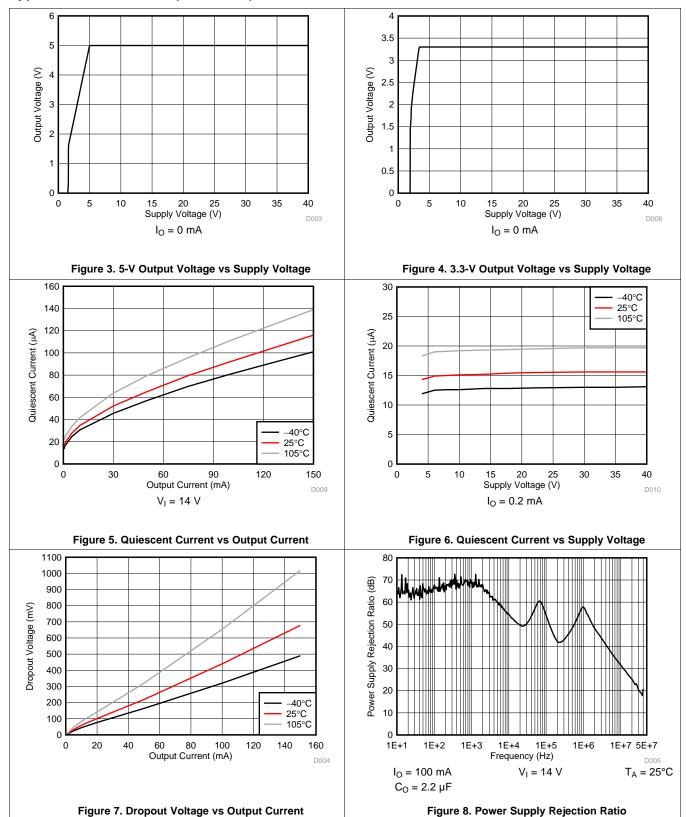


Figure 2. 3.3-V Output Voltage vs t Temperature

TEXAS INSTRUMENTS

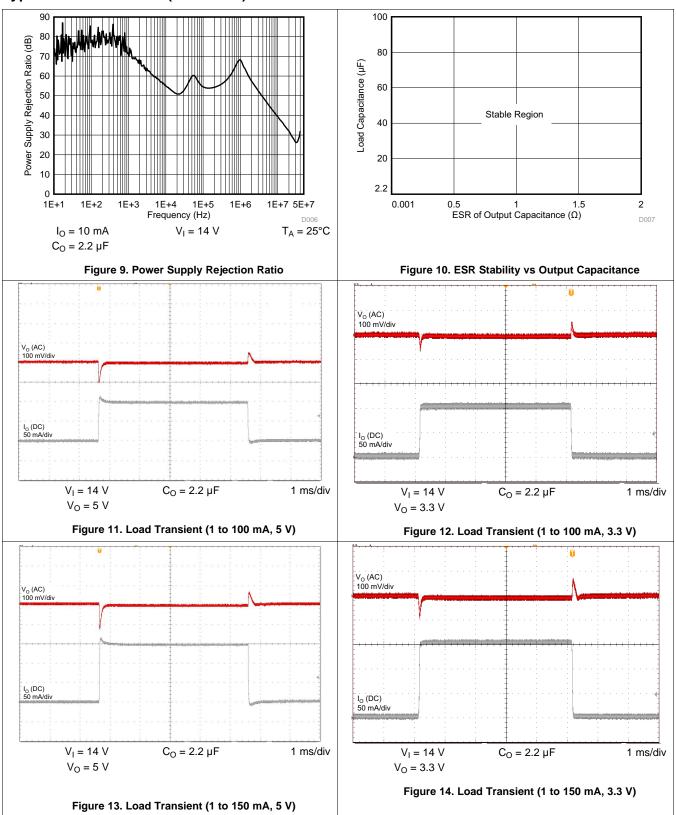
Typical Characteristics (continued)



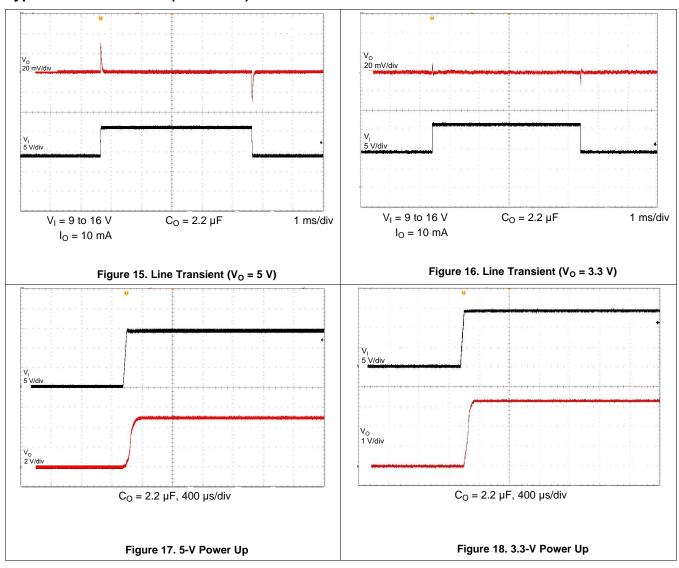




Typical Characteristics (continued)



Typical Characteristics (continued)



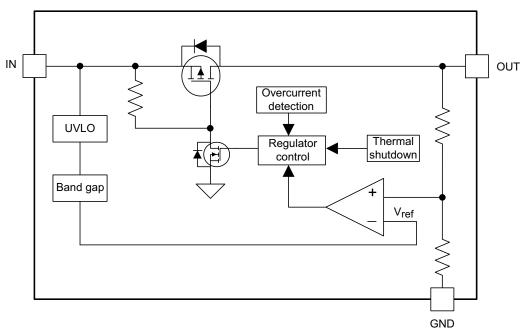


7 Detailed Description

7.1 Overview

The TPS7B69xx high-voltage linear regulator operates across a 4-V to 40-V input-voltage range. The device has an output current capacity of 150 mA and fixed output voltages of 3.3 V (TPS7B6933) or 5 V (TPS7B6950). The device features thermal shutdown and short-circuit protection to prevent damage during overtemperature and overcurrent conditions.

7.2 Functional Block Diagram



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7.3 Feature Description

7.3.1 Input (IN)

The IN pin is a high-voltage-tolerant pin. TI recommends that a capacitor with a value higher than 0.1 μ F be connected near this pin to improve the transient performance.

7.3.2 Output (OUT)

The OUT pin is the regulated output based on the required voltage. The output has current limitation. During the initial power up, the regulator has a soft start incorporated to control the initial current through the pass element and the output capacitor.

In the event that the regulator drops out of regulation, the output tracks the input minus a drop based on the load current. When the input voltage drops below the UVLO threshold, the regulator shuts down until the input voltage recovers above the minimum start-up level.

7.3.3 Output Capacitor Selection

For stable operation over the full temperature range and with load currents up to 150 mA, use a capacitor with an effective value between 2.2 μF and 100 μF and ESR smaller than 2 Ω . To improve the load-transient performance, an output capacitor, such as a ceramic capacitor with low ESR, is recommended.

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Feature Description (continued)

7.3.4 Low-Voltage Tracking

At low input voltages, the regulator drops out of regulation and the output voltage tracks input minus a voltage based on the load current (I₁) and switch resistor. This tracking allows for a smaller input capacitor and can possibly eliminate the need for a boost converter during cold-crank conditions.

7.3.5 Thermal Shutdown

The TPS7B69xx family of devices incorporates a thermal-shutdown (TSD) circuit as a protection from overheating. For continuous normal operation, the junction temperature should not exceed the TSD trip point. If the junction temperature exceeds the TSD trip point, the output turns off. When the junction temperature falls below the TSD trip point minus the hysteresis of TSD, the output turns on again. This cycling limits the dissipation of the regulator, protecting it from damage as a result of overheating.

The purpose of the design of the internal protection circuitry of the TPS7B69xx family of devices is for protection against overload conditions, not as a replacement for proper heat-sinking. Continuously running the TPS7B69xx family of devices into thermal shutdown degrades device reliability.

7.4 Device Functional Modes

7.4.1 Operation With V_I Less Than 4 V

The TPS7B69xx family of devices operates with input voltages above 4 V. The maximum UVLO voltage is 3 V and the device operates at an input voltage above 4 V. The device can also operate at lower input voltages; no minimum UVLO voltage is specified. At input voltages below the actual UVLO, the device shuts down.

7.4.2 Operation With V_I Greater Than 4 V

When V_1 is greater than 4 V, if the input voltage is higher than V_0 plus the dropout voltage, the output voltage is equal to the set value. Otherwise, the output voltage is equal to V_I minus the dropout voltage.

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Application and Implementation

NOTE

Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

8.1 Application Information

The TPS7B69xx family of devices is a 150-mA low-dropout linear regulator designed for up to 40-V V₁ operation with only 15-µA quiescent current at light loads. Use the PSpice transient model to evaluate the base function of the device. To download the PSpice transient model, go to the device product folder on www.Tl.com. In addition to this model, specific evaluation modules (EVM) are available for these devices. For the EVM and the EVM user guide, go to the device product folder.

8.2 Typical Application

Figure 19 shows the typical application circuit for the TPS7B69xx family of devices. Based on the endapplication, different values of external components can be used. An application can require a larger output capacitor during fast load steps to achieve better load transient response. TI recommends a low-ESR ceramic capacitor with a dielectric of type X5R or X7R for better load transient response.

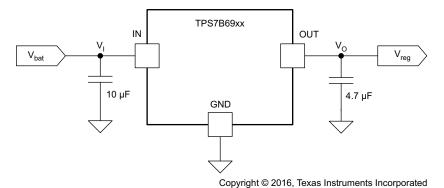


Figure 19. Typical Application Schematic for TPS7B69xx

8.2.1 Design Requirements

For this design example, use the parameters listed in Table 1.

Table 1. Design Parameters

DESIGN PARAMETER	EXAMPLE VALUES
Input voltage range	4 V to 40 V
Output voltage	3.3 V, 5 V
Output current rating	150 mA
Output capacitor range	2.2 μF to 100 μF
Output capacitor ESR range	1 mΩ to 2 Ω

8.2.2 Detailed Design Procedure

To begin the design process, determine the following:

- Input voltage range
- Output voltage
- Output current rating

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TEXAS INSTRUMENTS

8.2.2.1 Input Capacitor

The device requires an input decoupling capacitor, the value of which depends on the application. The typical recommend value for the decoupling capacitor is higher than 0.1 µF. The voltage rating must be greater than the maximum input voltage.

8.2.2.2 Output Capacitor

The device requires an output capacitor to stabilize the output voltage. The output capacitor value should be between 2.2 μ F and 100 μ F. The ESR value range should be between 1 m Ω and 2 Ω . TI recommends a ceramic capacitor with low ESR to improve the load-transient response.

8.2.2.3 Power Dissipation and Thermal Considerations

Use Equation 1 to calculate the power dissipated in the device.

$$P_D = I_O \times (V_I - V_O) + I_O \times V_I$$

where

- P_D = continuous power dissipation
- I_O = output current
- V_I = input voltage

•
$$V_O$$
 = output voltage (1)

Because $I_O \ll I_O$, the term $I_O \times V_I$ in Equation 1 can be ignored.

For a device under operation at a given ambient air temperature (T_A), use Equation 2 to calculate the junction temperature (T_J).

$$T_{J} = T_A + (Z_{\theta,JA} \times P_D)$$

where

$$Z_{A,IA}$$
 = junction-to-ambient air thermal impedance (2)

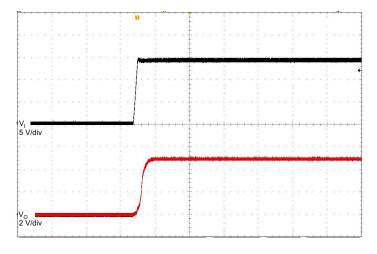
Use Equation 3 to calculate the rise in junction temperature because of power dissipation.

$$\Delta T = T_J - T_A = (Z_{\theta JA} \times P_D) \tag{3}$$

For a given maximum junction temperature (T_{Jmax}) , use Equation 4 to calculate the maximum ambient air temperature (T_{Amax}) at which the device can operate.

$$T_{A \max} = T_{J\max} - (Z_{\theta JA} \times P_D)$$
 (4)

8.2.3 Application Curve



 $C_O = 2.2 \mu F$, 400 $\mu s/div$

Figure 20. Power Up (5 V)

O Dower Supply December detions

9 Power Supply Recommendations

The device is designed to operate from an input-voltage supply range between 4 V and 40 V. This input supply must be well regulated. If the input supply is located more than a few inches from the TPS7B69xx device, TI recommends adding an electrolytic capacitor with a value of 10 µF and a ceramic bypass capacitor at the input.

10 Layout

10.1 Layout Guidelines

For the layout of TPS7B69xx family of devices, place the input and output capacitors near the devices as shown in Figure 21 and Figure 22. To enhance the thermal performance, TI recommends surrounding the device with some vias.

Minimize equivalent series inductance (ESL) and ESR to maximize performance and ensure stability. Place every capacitor as close as possible to the device and on the same side of the PCB as the regulator.

Do not place any of the capacitors on the opposite side of the PCB from where the regulator is installed. TI strongly discourages the use of long traces because they can impact system performance negatively and even cause instability.

If possible, and to ensure the maximum performance specified in this product data sheet, use the same layout pattern used for the TPS7B69xx evaluation board.

10.2 Layout Example

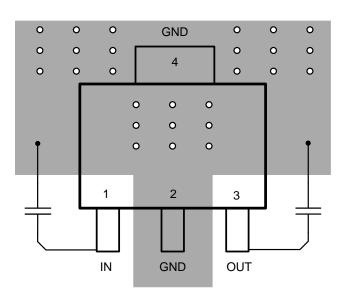


Figure 21. Layout Example for SOT-223 Package

Layout Example (continued)

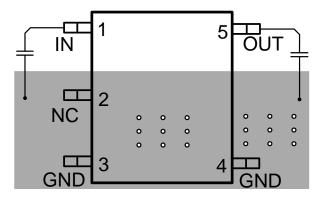


Figure 22. Layout Example for SOT-23 Package

11 Device and Documentation Support

11.1 Documentation Support

11.1.1 Related Documentation

For related documentation see the following: TPS7B6950EVM User's Guide, SLVUACO.

11.2 Related Links

The table below lists quick access links. Categories include technical documents, support and community resources, tools and software, and quick access to sample or buy.

Table 2. Related Links

PARTS	PRODUCT FOLDER	SAMPLE & BUY	TECHNICAL DOCUMENTS	TOOLS & SOFTWARE	SUPPORT & COMMUNITY
TPS7B6933	Click here	Click here	Click here	Click here	Click here
TPS7B6950	Click here	Click here	Click here	Click here	Click here

11.3 Trademarks

All trademarks are the property of their respective owners.

11.4 Electrostatic Discharge Caution



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

11.5 Glossary

SLYZ022 — TI Glossary.

This glossary lists and explains terms, acronyms, and definitions.

12 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most-current data available for the designated devices. This data is subject to change without notice and without revision of this document. For browser-based versions of this data sheet, see the left-hand navigation pane.

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PACKAGING INFORMATION

Orderable part number	Status	Material type	Package Pins	Package qty Carrier	RoHS	Lead finish/ Ball material	MSL rating/ Peak reflow	Op temp (°C)	Part marking
	(1)	(2)			(3)	(4)	(5)		(6)
TPS7B6933DBVR	Active	Production	SOT-23 (DBV) 5	3000 LARGE T&R	Yes	NIPDAU SN	Level-2-260C-1 YEAR	-40 to 105	ZBFY
TPS7B6933DBVR.A	Active	Production	SOT-23 (DBV) 5	3000 LARGE T&R	Yes	SN	Level-2-260C-1 YEAR	-40 to 105	ZBFY
TPS7B6950DBVR	Active	Production	SOT-23 (DBV) 5	3000 LARGE T&R	Yes	NIPDAU SN	Level-2-260C-1 YEAR	-40 to 105	ZAZT
TPS7B6950DBVR.A	Active	Production	SOT-23 (DBV) 5	3000 LARGE T&R	Yes	SN	Level-2-260C-1 YEAR	-40 to 105	ZAZT

⁽¹⁾ Status: For more details on status, see our product life cycle.

- (3) RoHS values: Yes, No, RoHS Exempt. See the TI RoHS Statement for additional information and value definition.
- (4) Lead finish/Ball material: Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.
- (5) MSL rating/Peak reflow: The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.
- (6) Part marking: There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

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OTHER QUALIFIED VERSIONS OF TPS7B69:

⁽²⁾ Material type: When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

PACKAGE OPTION ADDENDUM

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Automotive : TPS7B69-Q1

NOTE: Qualified Version Definitions:

• Automotive - Q100 devices qualified for high-reliability automotive applications targeting zero defects

PACKAGE MATERIALS INFORMATION

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TAPE AND REEL INFORMATION





A0	Dimension designed to accommodate the component width
В0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TPS7B6933DBVR	SOT-23	DBV	5	3000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
TPS7B6950DBVR	SOT-23	DBV	5	3000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3

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*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TPS7B6933DBVR	SOT-23	DBV	5	3000	208.0	191.0	35.0
TPS7B6950DBVR	SOT-23	DBV	5	3000	208.0	191.0	35.0



SMALL OUTLINE TRANSISTOR



NOTES:

- 1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
 2. This drawing is subject to change without notice.
 3. Reference JEDEC MO-178.

- 4. Body dimensions do not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.25 mm per side.
- 5. Support pin may differ or may not be present.



SMALL OUTLINE TRANSISTOR



NOTES: (continued)

6. Publication IPC-7351 may have alternate designs.

7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.



SMALL OUTLINE TRANSISTOR



NOTES: (continued)

- 8. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
- 9. Board assembly site may have different recommendations for stencil design.



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