

TPS650350-Q1 Automotive Camera PMIC

1 Features

- Qualified for automotive applications
- AEC-Q100 grade 1 qualified
 - -40°C to +125°C ambient operating temperature range
- Three step-down converters:
 - BUCK1 V_{IN} range from 4.0 V to 18.3 V
 - BUCK1 V_{OUT} range from 2.5 V to 4.0 V
 - BUCK1 output current up to 1500-mA
 - BUCK2 and BUCK3 V_{IN} range from 2.5 V to 5.5 V
 - BUCK2 and BUCK3 V_{OUT} range from 0.9 V to 1.9 V
 - BUCK2 and BUCK3 output current up to 1200-mA
 - Spread-spectrum clock (SSC) generation for reduced EMI
 - 2.3-MHz forced fixed switching frequency PWM operation
- One low dropout (LDO) regulator:
 - V_{IN} range from 2.5 V to 5.5 V
 - V_{OUT} range from 1.8 V to 3.3 V
 - Low noise and high PSRR
 - Adjustable output voltage through I²C
 - Up to 300-mA output current
- 3.0-mm × 3.5-mm 22-pin WQFN with wettable flanks

2 Applications

- Automotive camera modules
 - Surround view camera modules
 - Rear view camera modules
 - Driver monitor camera modules
 - Power over coax (POC) camera modules
 - E-mirror camera modules
 - Front view camera modules

3 Description

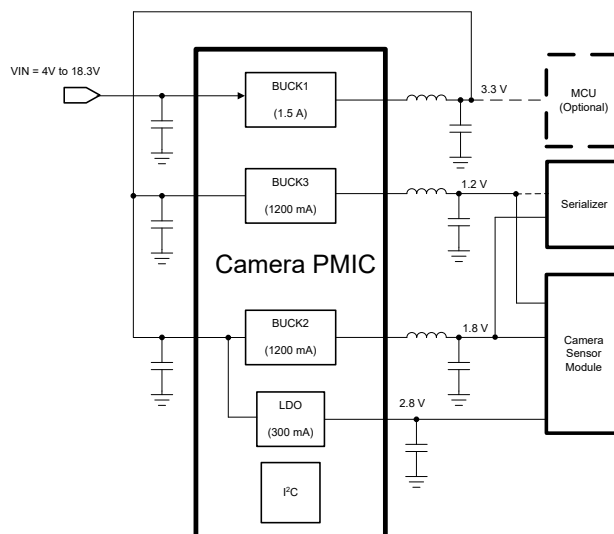
The TPS650350-Q1 device is a highly integrated power management IC for automotive camera modules. This device combines three step down converters and one low-dropout (LDO) regulator. The BUCK1 step-down converter has an input voltage range up to 18.3 V for connections to Power over Coax (PoC). All converters operate in a forced fixed-frequency PWM mode. The LDO can supply 300 mA and operate with an input voltage range from 2.5 V to 5.5 V. The step-down converters and the LDO have separate voltage inputs that enable maximum design and sequencing flexibility.

The TPS650350-Q1 is available in a 22-pin WQFN package (3.0 mm × 3.5 mm).

Device Information

PART NUMBER	PACKAGE ⁽¹⁾	BODY SIZE (NOM)
TPS650350-Q1	WQFN (22)	3.00 mm × 3.50 mm

(1) For all available packages, see the orderable addendum at the end of the data sheet.



TPS650350-Q1 Application Circuit



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4 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision * (November 2022) to Revision A (May 2023)	Page
• Changed the device status from Advance Information to Production Data.....	1

5 Device and Documentation Support

5.1 Device Support

5.1.1 Third-Party Products Disclaimer

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5.2 Receiving Notification of Documentation Updates

To receive notification of documentation updates, navigate to the device product folder on [ti.com](https://www.ti.com). Click on *Subscribe to updates* to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document.

5.3 Support Resources

[TI E2E™ support forums](#) are an engineer's go-to source for fast, verified answers and design help — straight from the experts. Search existing answers or ask your own question to get the quick design help you need.

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5.4 Trademarks

TI E2E™ is a trademark of Texas Instruments.

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5.5 Electrostatic Discharge Caution



This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

5.6 Glossary

[TI Glossary](#) This glossary lists and explains terms, acronyms, and definitions.

6 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

PACKAGING INFORMATION

Orderable part number	Status (1)	Material type (2)	Package Pins	Package qty Carrier	RoHS (3)	Lead finish/ Ball material (4)	MSL rating/ Peak reflow (5)	Op temp (°C)	Part marking (6)
TPS65035000RZDRQ1	Active	Production	WQFN-FCRLF (RZD) 22	3000 LARGE T&R	Yes	NIPDAU	Level-3-260C-168 HR	-40 to 125	O35000
TPS65035000RZDRQ1.A	Active	Production	WQFN-FCRLF (RZD) 22	3000 LARGE T&R	Yes	NIPDAU	Level-3-260C-168 HR	-40 to 125	O35000
TPS65035001RZDRQ1	Active	Production	WQFN-FCRLF (RZD) 22	3000 LARGE T&R	Yes	NIPDAU	Level-3-260C-168 HR	-40 to 125	O35001
TPS65035001RZDRQ1.A	Active	Production	WQFN-FCRLF (RZD) 22	3000 LARGE T&R	Yes	NIPDAU	Level-3-260C-168 HR	-40 to 125	O35001
TPS65035007RZDRQ1	Active	Production	WQFN-FCRLF (RZD) 22	3000 LARGE T&R	Yes	NIPDAU	Level-3-260C-168 HR	-40 to 125	O35007
TPS65035007RZDRQ1.A	Active	Production	WQFN-FCRLF (RZD) 22	3000 LARGE T&R	Yes	NIPDAU	Level-3-260C-168 HR	-40 to 125	O35007

⁽¹⁾ **Status:** For more details on status, see our [product life cycle](#).

⁽²⁾ **Material type:** When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

⁽³⁾ **RoHS values:** Yes, No, RoHS Exempt. See the [TI RoHS Statement](#) for additional information and value definition.

⁽⁴⁾ **Lead finish/Ball material:** Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

⁽⁵⁾ **MSL rating/Peak reflow:** The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

⁽⁶⁾ **Part marking:** There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

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TAPE AND REEL INFORMATION



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TPS65035000RZDRQ1	WQFN-FCRLF	RZD	22	3000	330.0	12.4	3.3	3.8	1.2	8.0	12.0	Q1
TPS65035001RZDRQ1	WQFN-FCRLF	RZD	22	3000	330.0	12.4	3.3	3.8	1.2	8.0	12.0	Q1
TPS65035007RZDRQ1	WQFN-FCRLF	RZD	22	3000	330.0	12.4	3.3	3.8	1.2	8.0	12.0	Q1

TAPE AND REEL BOX DIMENSIONS



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TPS65035000RZDRQ1	WQFN-FCRLF	RZD	22	3000	367.0	367.0	35.0
TPS65035001RZDRQ1	WQFN-FCRLF	RZD	22	3000	367.0	367.0	35.0
TPS65035007RZDRQ1	WQFN-FCRLF	RZD	22	3000	367.0	367.0	35.0



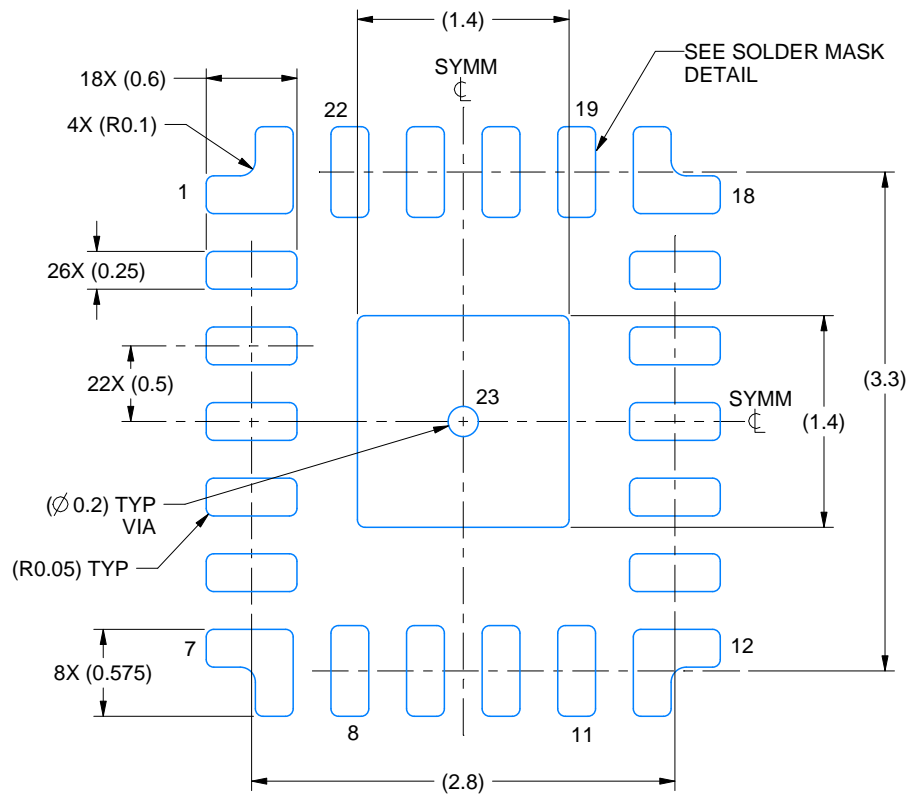
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. The package thermal pad must be soldered to the printed circuit board for thermal and mechanical performance.

EXAMPLE BOARD LAYOUT

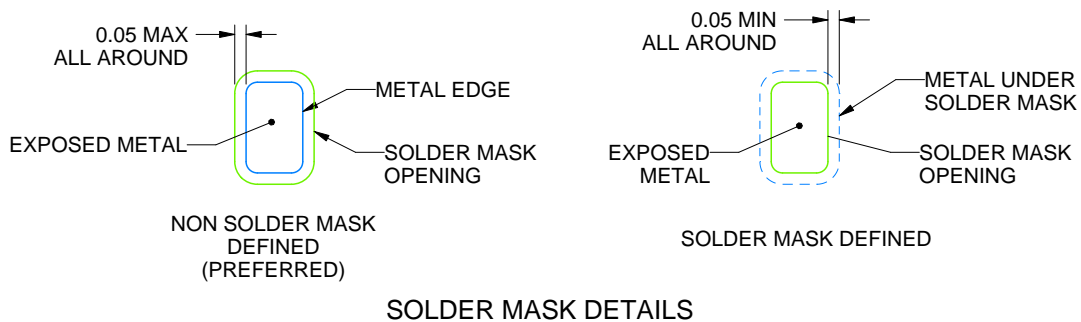
RZD0022A

WQFN-FCRLF - 0.7 mm max height

PLASTIC QUAD FLATPACK - NO LEAD



LAND PATTERN EXAMPLE
EXPOSED METAL SHOWN
SCALE: 20X



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NOTES: (continued)

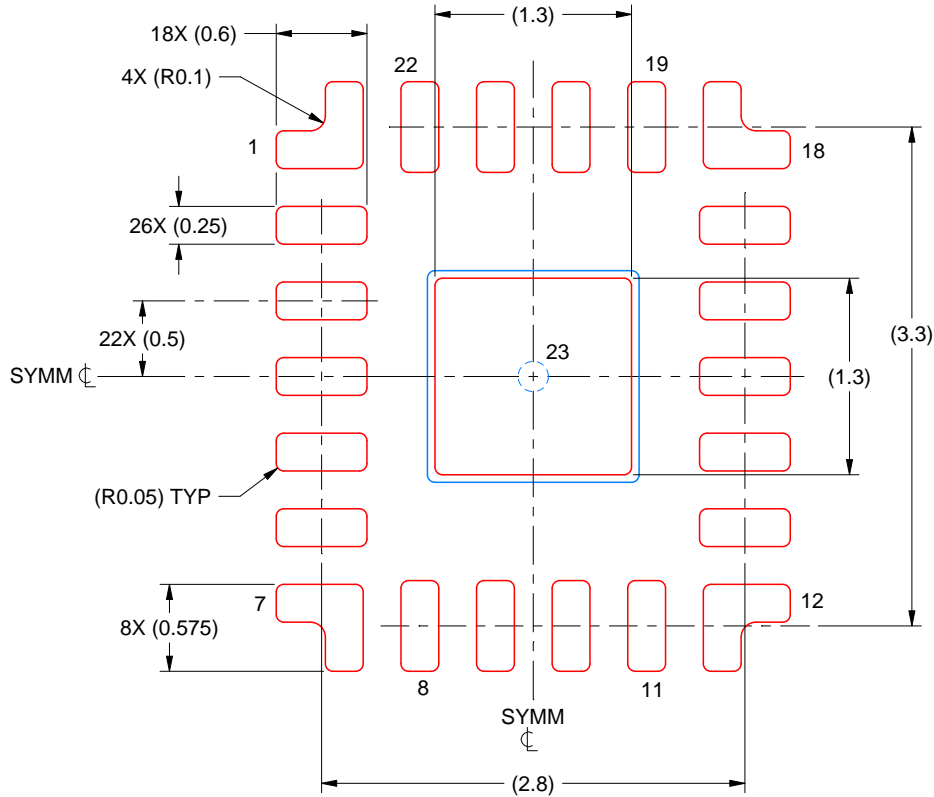
- This package is designed to be soldered to a thermal pad on the board. For more information, see Texas Instruments literature number SLUA271 (www.ti.com/lit/sluea271).
- Vias are optional depending on application, refer to device data sheet. If any vias are implemented, refer to their locations shown on this view. It is recommended that vias under paste be filled, plugged or tented.

EXAMPLE STENCIL DESIGN

RZD0022A

WQFN-FCRLF - 0.7 mm max height

PLASTIC QUAD FLATPACK - NO LEAD



SOLDER PASTE EXAMPLE
BASED ON 0.125 MM THICK STENCIL
SCALE: 20X

EXPOSED PAD 19
86% PRINTED SOLDER COVERAGE BY AREA UNDER PACKAGE

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NOTES: (continued)

6. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

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