DORNPACKAGE

SDAS038C - DECEMBER 1982 - REVISED DECEMBER 1994

- Generate Either Odd or Even Parity for Nine Data Lines
- Cascadable for n-Bit Parity
- Can Be Used to Upgrade Existing Systems Using MSI Parity Circuits
- Package Options Include Plastic Small-Outline (D) Packages and Standard Plastic (N) 300-mil DIPs

(TOP VIEW) G **□** ∨_{CC} нΠ 13 [] F 2 ис П 3 12 ПΕ Пο ı 4 11 Σ EVEN [] 5 10 ∏ C 9 **∏** B Σ ODD **GND** ПΑ 8

NC - No internal connection

description

These universal 9-bit parity generators/checkers utilize advanced Schottky high-performance

circuitry and feature odd (Σ ODD) and even (Σ EVEN) outputs to facilitate operation of either odd- or even-parity applications. The word-length capability is easily expanded by cascading.

These devices can be used to upgrade the performance of most systems utilizing the SN74ALS180 and SN74AS180 parity generators/checkers. Although the SN74ALS280 and SN74AS280 are implemented without expander inputs, the corresponding function is provided by the availability of an input (I) at terminal 4 and the absence of any internal connection at terminal 3. This permits the SN74ALS280 and SN74AS280 to be substituted for the SN74ALS180 and SN74AS180 in existing designs to produce an identical function even if the devices are mixed with existing SN74ALS180 and SN74AS180 devices.

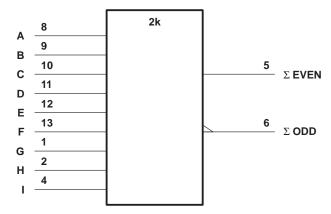
All SN74AS280 inputs are buffered to lower the drive requirements.

The SN74ALS280 and SN74AS280 are characterized for operation from 0°C to 70°C.

FUNCTION TABLE

NO. OF INPUTS	OUTI	PUTS
A – I THAT ARE HIGH	Σ EVEN	Σ ODD
0, 2, 4, 6, 8	Н	L
1, 3, 5, 7, 9	L	Н

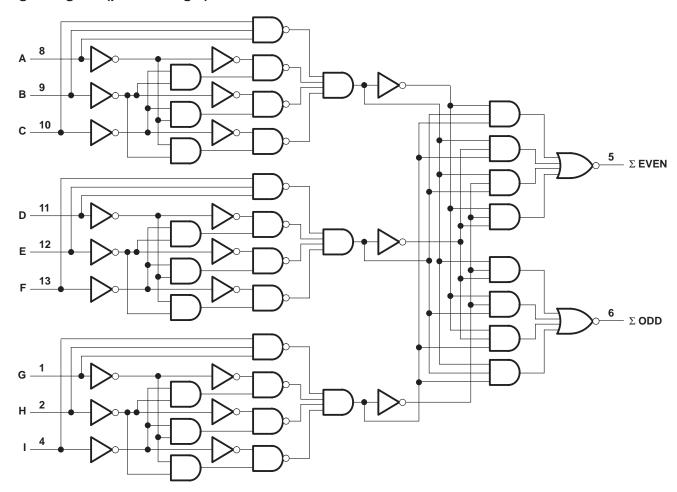
logic symbol†



† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.



logic diagram (positive logic)



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V _{CC}	7 V
Input voltage, V _I	7 V
Operating free-air temperature range, T _A : SN74ALS280	0°C to 70°C
Storage temperature range	. −65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

		SN	74ALS2	80	
		MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.5	5	5.5	V
VIH	High-level input voltage	2			V
VIL	Low-level input voltage			8.0	V
loh	High-level output current			-2.6	mA
l _{OL}	Low-level output current			24	mA
TA	Operating free-air temperature	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

			SN	74ALS2	80	
PARAMETER	TEST CON	IDITIONS	MIN	TYP†	MAX	UNIT
VIK	$V_{CC} = 4.5 \text{ V},$	$I_{I} = -18 \text{ mA}$			-1.5	V
, , , , , , , , , , , , , , , , , , ,	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	$I_{OH} = -0.4 \text{ mA}$	V _{CC} -2	!		
Voн	$V_{CC} = 4.5 \text{ V},$	$I_{OH} = -2.6 \text{ mA}$	2.4	3.3		V
V	V 45V	I _{OL} = 12 mA		0.25	0.4	V
V _{OL}	$V_{CC} = 4.5 \text{ V}$	I _{OL} = 24 mA		0.35	0.5	V
lį	$V_{CC} = 5.5 \text{ V},$	V _I = 7 V			0.1	mA
lіН	$V_{CC} = 5.5 \text{ V},$	V _I = 2.7 V			20	μΑ
I _{IL}	$V_{CC} = 5.5 \text{ V},$	V _I = 0.4 V			-0.1	mA
IO [‡]	$V_{CC} = 5.5 \text{ V},$	V _O = 2.25 V	-30		-112	mA
Icc	$V_{CC} = 5.5 V$			10	16	mA

switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC} = 4.5 C _L = 50 pF R _L = 500 g T _A = MIN t	- , 2,	UNIT	
			MIN	MAX	7	
^t PLH	A	7 EVEN	3	20		
^t PHL	Any	ΣEVEN	3	20	ns	
^t PLH	Any	ΣODD	3	20	ns	
t _{PHL}	Any	2 000	4	22	115	

[§] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

[†] All typical values are at V_{CC} = 5 V, T_A = 25°C. ‡ The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I_{OS}.

SN74ALS280, SN74AS280 9-BIT PARITY GENERATORS/CHECKERS

SDAS038C - DECEMBER 1982 - REVISED DECEMBER 1994

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

recommended operating conditions

		SN	174AS28	0	
		MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	V
VIH	High-level input voltage	2			V
VIL	Low-level input voltage			8.0	V
lOH	High-level output current			-2	mA
l _{OL}	Low-level output current			20	mA
TA	Operating free-air temperature	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

DADAMETED	PARAMETER TEST CO		SN74AS2	SN74AS280				
PARAMETER	TEST CON	IDITIONS	MIN TYP‡	MAX	UNIT			
VIK	$V_{CC} = 4.5 V,$	$I_{I} = -18 \text{ mA}$		-1.5	V			
VOH	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	$I_{OH} = -2 \text{ mA}$	V _{CC} -2		V			
VOL	$V_{CC} = 4.5 V,$	$I_{OL} = 20 \text{ mA}$	0.35	0.5	V			
ΙĮ	$V_{CC} = 5.5 V,$	V _I = 7 V		0.1	mA			
lін	$V_{CC} = 5.5 V,$	V _I = 2.7 V		20	μΑ			
I _{IL}	$V_{CC} = 5.5 V,$	V _I = 0.4 V		-0.5	mA			
ΙΟ§	$V_{CC} = 5.5 V$,	V _O = 2.25 V	-30	-112	mA			
Icc	V _{CC} = 5.5 V		25	35	mA			

[‡] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.

switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC} = 4.5 C _L = 50 pl R _L = 500 G T _A = MIN t	UNIT		
			MIN	MAX	1	
^t PLH	A	2 EVEN	3	12		
^t PHL	Any	ΣEVEN	3	11	ns	
tPLH	Any	ΣODD	3	12	nc	
^t PHL	Any	2 000	3	11.5	ns	

 $[\]P$ For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

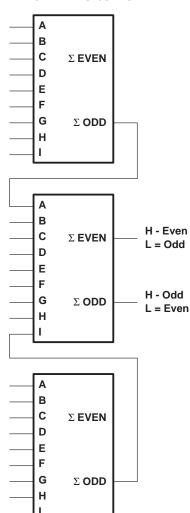


[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

[§] The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, IOS.

APPLICATION INFORMATION

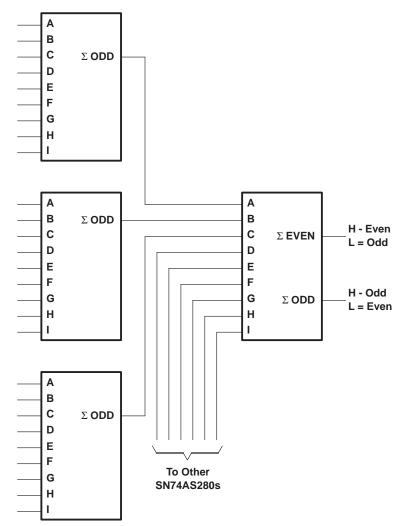
25-LINE PARITY GENERATOR/CHECKER



Three SN74ALS280/SN74AS280 devices can be used to implement a 25-line parity generator/checker.

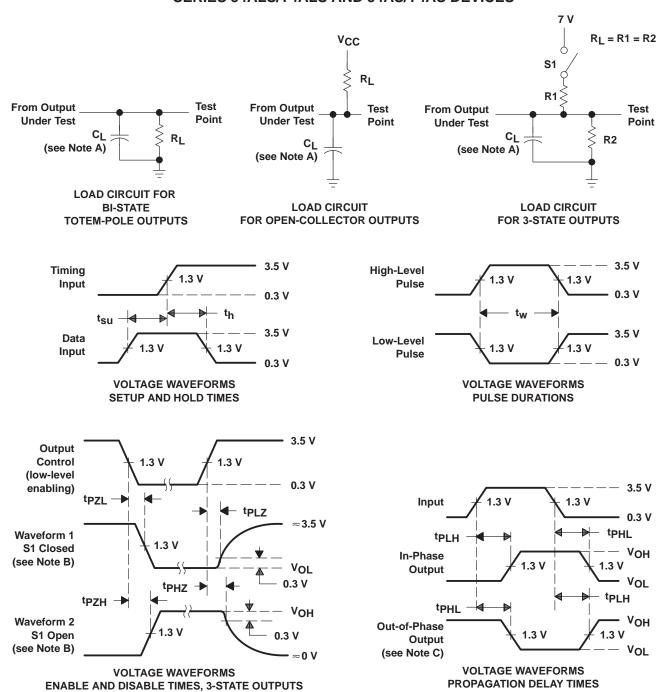
As an alternative, the Σ ODD outputs of two or three parity generators/checkers can be decoded with a 2-input ('AS86A or 'ALS86) exclusive-OR gate for 18- or 27-line parity applications.





Longer word lengths can be implemented by cascading SN74ALS280/SN74AS280 devices. Parity can be generated for word lengths up to 81 bits.

PARAMETER MEASUREMENT INFORMATION SERIES 54ALS/74ALS AND 54AS/74AS DEVICES



NOTES: A. C_L includes probe and jig capacitance.

- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. When measuring propagation delay items of 3-state outputs, switch S1 is open.
- D. All input pulses have the following characteristics: PRR \leq 1 MHz, $t_{\Gamma} = t_{f} = 2$ ns, duty cycle = 50%.
- E. The outputs are measured one at a time with one transition per measurement.

Figure 1. Load Circuits and Voltage Waveforms



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PACKAGING INFORMATION

Orderable part number	Status	Material type	Package Pins	Package qty Carrier	RoHS	Lead finish/ Ball material	MSL rating/ Peak reflow	Op temp (°C)	Part marking (6)
						(4)	(5)		
SN74AS280N	Active	Production	PDIP (N) 14	25 TUBE	Yes	NIPDAU	N/A for Pkg Type	0 to 70	SN74AS280N
SN74AS280N.A	Active	Production	PDIP (N) 14	25 TUBE	Yes	NIPDAU	N/A for Pkg Type	0 to 70	SN74AS280N
SN74AS280NSR	Active	Production	SOP (NS) 14	2000 LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	0 to 70	74AS280
SN74AS280NSR.A	Active	Production	SOP (NS) 14	2000 LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	0 to 70	74AS280

⁽¹⁾ Status: For more details on status, see our product life cycle.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

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⁽²⁾ Material type: When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

⁽³⁾ RoHS values: Yes, No, RoHS Exempt. See the TI RoHS Statement for additional information and value definition.

⁽⁴⁾ Lead finish/Ball material: Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

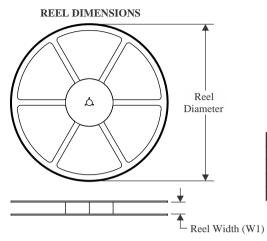
⁽⁵⁾ MSL rating/Peak reflow: The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

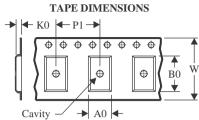
⁽⁶⁾ Part marking: There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

PACKAGE MATERIALS INFORMATION

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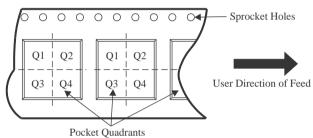
TAPE AND REEL INFORMATION





A0	Dimension designed to accommodate the component width
В0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

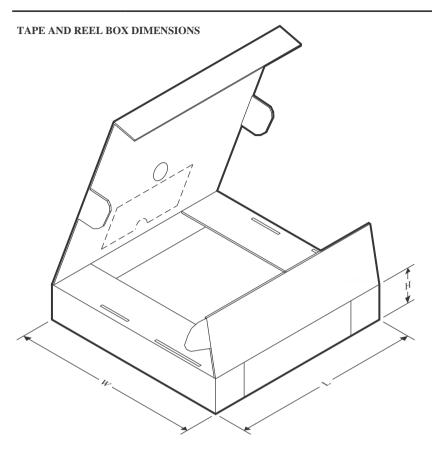


*All dimensions are nominal

Device	_	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN74AS280NSR	SOP	NS	14	2000	330.0	16.4	8.1	10.4	2.5	12.0	16.0	Q1

PACKAGE MATERIALS INFORMATION

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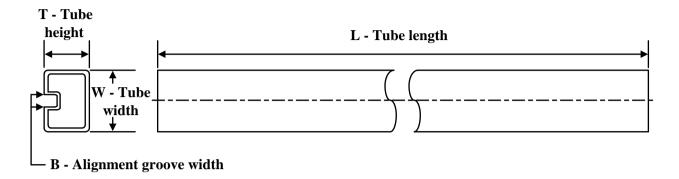
*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN74AS280NSR	SOP	NS	14	2000	353.0	353.0	32.0

PACKAGE MATERIALS INFORMATION

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TUBE



*All dimensions are nominal

Device	Package Name	Package Type	Pins	SPQ	L (mm)	W (mm)	T (µm)	B (mm)
SN74AS280N	N	PDIP	14	25	506	13.97	11230	4.32
SN74AS280N	N	PDIP	14	25	506	13.97	11230	4.32
SN74AS280N.A	N	PDIP	14	25	506	13.97	11230	4.32
SN74AS280N.A	N	PDIP	14	25	506	13.97	11230	4.32

N (R-PDIP-T**)

PLASTIC DUAL-IN-LINE PACKAGE

16 PINS SHOWN



NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
- The 20 pin end lead shoulder width is a vendor option, either half or full width.



MECHANICAL DATA

NS (R-PDSO-G**)

14-PINS SHOWN

PLASTIC SMALL-OUTLINE PACKAGE



NOTES:

- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.



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