



REG1118

800mA Low Dropout Positive Regulator with Current Source and Sink Capability

FEATURES

- SOURCES 800mA, SINKS 400mA
- 2.85V OUTPUT FOR SCSI ACTIVE NEGATION TERMINATION
- 1.3V max DROPOUT VOLTAGE AT I_o = 800mA
- INTERNAL CURRENT LIMIT
- THERMAL OVERLOAD PROTECTION
- SOT-223 SURFACE MOUNT PACKAGE

APPLICATIONS

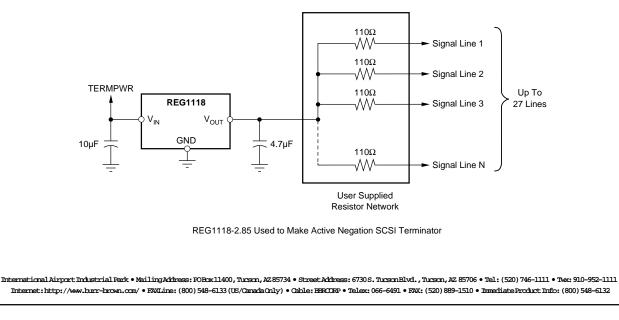
- SCSI-2 AND SCSI-3 DEVICES
- FAST-20 SCSI
- ACTIVE NEGATION SCSI TERMINATORS
- SUPPLY SPLITTER GROUND GENERATION

DESCRIPTION

The REG1118-2.85 is a three-terminal voltage regulators capable of sourcing up to 800mA and sinking up to 400mA. The sinking capability is important in SCSI applications where active negation line drivers are used. The REG1118-2.85 is useful for making a 27-line Boulay terminator capable of interfacing with active negation drivers required in FAST-20 SCSI applications. The regulator has active current limits for both sink and source currents as well as internal thermal limiting.

Laser trimming assures excellent output voltage accuracy. An NPN output stage allows output stage drive current to contribute to the source load current for maximum efficiency.

REG1118 is packaged in a SOT-223 surface-mount package, suitable for reflow soldering techniques.



©1996 Burr-Brown Corporation

SPECIFICATIONS

At $T_A = 0^{\circ}C$ to 70°C, TERMPWR = +5V, unless otherwise noted.

		REG1118-2.85	EG1118-2.85		
PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE					
	No Load, T _A = 25°C	2.82	2.85	2.88	V
	All Operating Conditions ⁽¹⁾	2.79	2.85	2.91	V
LINE REGULATION ⁽²⁾					
	$I_0 = 0, V_{IN} = 4.75V \text{ to } 6V$		0.3	4	mV
LOAD REGULATION ⁽²⁾					
	I _O = 0 to 800mA		2	25	mV
	$I_{O} = -400$ to 0mA		25	60	mV
DROPOUT VOLTAGE ⁽³⁾	$T_A = 25^{\circ}C$				
	I _O = 100mA		0.8	1.1	V
	I _O = 800mA		1.1	1.3	V
CURRENT LIMIT	$T_A = 25^{\circ}C$				
Positive (I _{SC} +) ⁽⁴⁾		800	1450		mA
Negative (I _{SC} -)	V _{OUT} = 3.7V		-750	-400	mA
QUIESCENT CURRENT	I _O = 0		3	7	mA
RIPPLE REJECTION	T _A = 25°C				
	$f = 120Hz, V_{IN} - V_{OUT} = 2V, V_{RIPPLE} = 0.5Vp-p$		56		dB
LONG-TERM STABILITY					
	T _A = +125°C, 1000Hr		0.2		%
THERMAL SHUTDOWN					
Junction Temperature Shutdown			150		°C
Junction Temperature Reset			140		°C
THERMAL RESISTANCE					
Specified Junction Temperature Range		0		70	°C
Operating Junction Temperature Range		0		125	°C
Storage Range		-55		125	°C
Thermal Resistance					
$ heta_{JC}$	(Junction-to-Case at Tab)		15		°C/W

NOTES: (1) All operating conditions include the combined effects of load current, input voltage, and temperature over each parameter's full specified range. (2) Load and line regulation are tested at a constant junction temperature by low duty cycle testing. (3) Dropout voltage is defined as the minimum input-to-output voltage that produces a 1% decrease in output voltage. (4) V_{OUT} to ground through 1 Ω .

ABSOLUTE MAXIMUM RATINGS

Input Voltage	7V
Output Voltage	0.2V to (V+) +0.5V
Short-Circuit Duration	
Operating Junction Temperature Range	0°C to +125°C
Storage Temperature Range	–55°C to +125°C
Lead Temperature (soldering, 10s) ⁽¹⁾	+300°C

NOTE: (1) See "Soldering Methods."

PACKAGE INFORMATION

PRODUCT	PACKAGE	PACKAGE DRAWING NUMBER ⁽¹⁾
REG1118-2.85	SOT-223	311

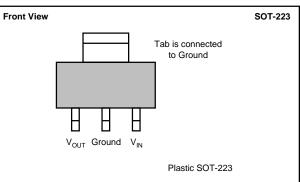
NOTE: (1) For detailed drawing and dimension table, please see end of data sheet, or Appendix C of Burr-Brown IC Data Book.

ORDERING INFORMATION

PRODUCT ⁽¹⁾	PART MARKING	PACKAGE
REG1118-2.85	BB11182	SOT-223

NOTE: (1) Available in Tape and Reel, add -TR to Model Number.

CONNECTION DIAGRAM



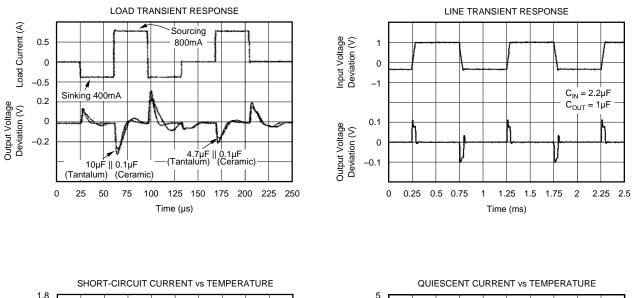
ELECTROSTATIC DISCHARGE SENSITIVITY

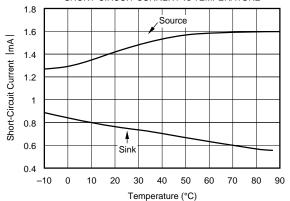
This integrated circuit can be damaged by ESD. Burr-Brown recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

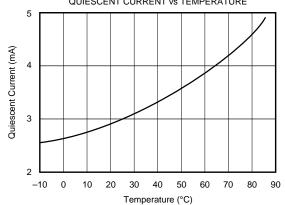


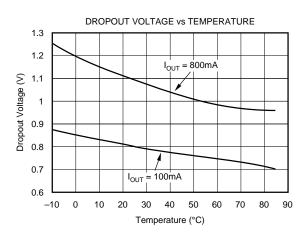
TYPICAL PERFORMANCE CURVE

At $T_A = 25^{\circ}C$, TERMPWR = +5V, unless otherwise specified.











APPLICATIONS INFORMATION

Figure 1 shows the basic hookup diagram for the REG1118. An output capacitor is required for proper operation and to improve high frequency load regulation. A high quality capacitor should be used to assure that the ESR (effective series resistance) is less than 0.5Ω . A capacitor of at least 1µF is recommended. Increasing C_{OUT} improves output voltage deviation, see the typical curve "Load Transient Response." It is suggested that the input be decoupled with 10µF low ESR capacitor.

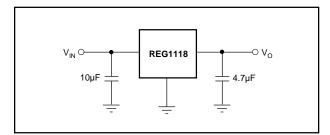


FIGURE 1. Basic Connections.

THERMAL CONSIDERATIONS

The REG1118 has current limit and thermal shutdown circuits that protect it from overload. The thermal shutdown activates at approximately $T_J = 150^{\circ}$ C. For continuous operation, however, the junction temperature should not be allowed to exceed 125°C. Any tendency to activate the thermal shutdown in normal use is an indication of an inadequate heat sink or excessive power dissipation. The power dissipation is equal to:

 $P_{D} = (V_{IN} - V_{OUT}) I_{OUT}$

The junction temperature can be calculated by:

 $\mathbf{T}_{\mathrm{J}} = \mathbf{T}_{\mathrm{A}} + \mathbf{P}_{\mathrm{D}} \left(\boldsymbol{\theta}_{\mathrm{JA}} \right)$

where T_A is the ambient temperature, and θ_{JA} is the junction-to-ambient thermal resistance

The REG1118 derives heat sinking from conduction through its copper leads, especially the large mounting tab. These must be soldered to a circuit board with a substantial amount of copper remaining (see Figure 2). Circuit board traces connecting to the tab and the leads should be made as large as practical. Other nearby circuit traces, including those on the back side of the circuit board, help conduct heat away from the device, even though they are not electrically connected. Make all nearby copper traces as wide as possible and leave only narrow gaps between traces. In addition, multiple contacts to internal power and ground planes increase heat sinking with minimal increase in circuit board area.

Table I shows approximate values of θ_{JA} for various circuit board and copper areas. Nearby heat dissipating components, circuit board mounting conditions and ventilation can dramatically affect the actual θ_{JA} .

A simple experiment will determine whether the maximum recommended junction temperature is exceeded in an actual circuit board and mounting configuration: Increase the ambient temperature above that expected in normal operation until the device's thermal shutdown is activated. If this occurs at more than 40°C above the maximum expected ambient temperature, then the T_J will be less than 125°C during normal operation.

SOLDERING METHODS

The REG1118 package is suitable for infrared reflow and vapor-phase reflow soldering techniques. The high rate of temperature change that occurs with wave soldering, or hand soldering can damage the REG1118.

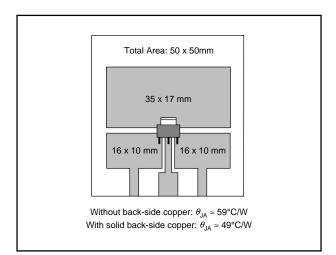


FIGURE 2. Circuit Board Layout Example.

TOTAL PC BOARD AREA	TOPSIDE ⁽¹⁾ COPPER AREA	BACKSIDE COPPER AREA	THERMAL RESISTANCE JUNCTION-TO-AMBIENT
2500mm ²	2500mm ²	2500mm ²	46°C/W
2500mm ²	1250mm ²	2500mm ²	47°C/W
2500mm ²	950mm ²	2500mm ²	49°C/W
2500mm ²	2500mm ²	0	51°C/W
2500mm ²	1800mm ²	0	53°C/W
1600mm ²	600mm ²	1600mm ²	55°C/W
2500mm ²	1250mm ²	0	58°C/W
2500mm ²	915mm ²	0	59°C/W
1600mm ²	600mm ²	0	67°C/W
900mm ²	340mm ²	900mm ²	72°C/W
900mm ²	340mm ²	0	85°C/W

NOTE: (1) Tab is attached to the topside copper.

TABLE I.

INSPEC Abstract Number: B91007604, C91012627 Kelly, E.G. "Thermal Characteristics of Surface 5WK9Ω Packages." The Proceedings of SMTCON. Surface Mount Technology Conference and Exposition: *Competitive Surface Mount Technology*, April 3-6, 1990, Atlantic City, NJ, USA. *Abstract Publisher*: IC Manage, 1990, Chicago, IL, USA.



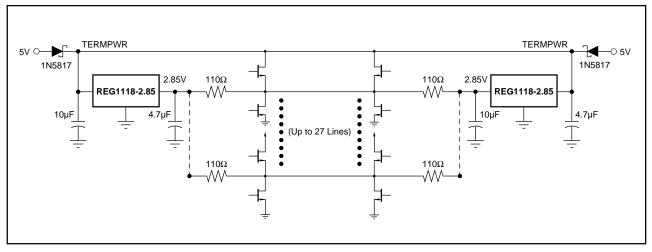


FIGURE 3. SCSI Active Termination Configuration.

The information provided herein is believed to be reliable; however, BURR-BROWN assumes no responsibility for inaccuracies or omissions. BURR-BROWN assumes no responsibility for the use of this information, and all use of such information shall be entirely at the user's own risk. Prices and specifications are subject to change without notice. No patent rights or licenses to any of the circuits described herein are implied or granted to any third party. BURR-BROWN does not authorize or warrant any BURR-BROWN product for use in life support devices and/or systems.





PACKAGING INFORMATION

Orderable part number	Status	Material type	Package Pins	Package qty Carrier	RoHS	Lead finish/	MSL rating/	Op temp (°C)	Part marking
	(1)	(2)			(3)	Ball material	Peak reflow		(6)
						(4)	(5)		
REG1118-2.85	Active	Production	SOT-223 (DCY) 4	80 TUBE	Yes	NIPDAU	Level-1-260C-UNLIM	0 to 125	BB11182
REG1118-2.85.A	Active	Production	SOT-223 (DCY) 4	80 TUBE	Yes	NIPDAU	Level-1-260C-UNLIM	0 to 125	BB11182
REG1118-2.85/2K5	Active	Production	SOT-223 (DCY) 4	2500 LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	0 to 125	BB11182
REG1118-2.85/2K5.A	Active	Production	SOT-223 (DCY) 4	2500 LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	0 to 125	BB11182
REG1118-2.85/2K5G4	Active	Production	SOT-223 (DCY) 4	2500 LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	0 to 125	BB11182

⁽¹⁾ **Status:** For more details on status, see our product life cycle.

⁽²⁾ Material type: When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

⁽³⁾ RoHS values: Yes, No, RoHS Exempt. See the TI RoHS Statement for additional information and value definition.

⁽⁴⁾ Lead finish/Ball material: Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

⁽⁵⁾ MSL rating/Peak reflow: The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

⁽⁶⁾ Part marking: There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

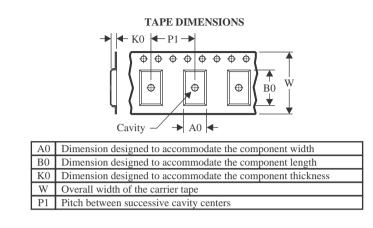
In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.



www.ti.com

TAPE AND REEL INFORMATION





QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions a	are nominal
-------------------	-------------

Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
REG1118-2.85/2K5	SOT-223	DCY	4	2500	330.0	12.4	7.1	7.45	1.88	8.0	12.0	Q3



www.ti.com

PACKAGE MATERIALS INFORMATION

23-May-2025



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
REG1118-2.85/2K5	SOT-223	DCY	4	2500	358.0	335.0	35.0

TEXAS INSTRUMENTS

www.ti.com

23-May-2025

TUBE



- B - Alignment groove width

*All dimensions are nominal

Device	Package Name	Package Type	Pins	SPQ	L (mm)	W (mm)	Τ (μm)	B (mm)
REG1118-2.85	DCY	SOT-223	4	80	542.9	8.6	3606	2.67
REG1118-2.85.A	DCY	SOT-223	4	80	542.9	8.6	3606	2.67

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2025, Texas Instruments Incorporated