

# REF30E and REF30, Low Current Voltage Reference in SOT-23-3

## 1 Features

- Small industry standard footprint: SOT23-3
- High accuracy
  - **REF30E:  $\pm 0.1\%$**
  - REF30:  $\pm 0.2\%$
- Excellent temperature drift performance:
  - **REF30E: 20ppm/°C**
  - REF30: 75ppm/°C
- **REF30E is drop-in replacement of REF30**
- Low  $I_Q$  (typical)
  - **REF30E: 25 $\mu$ A**
  - REF30: 42 $\mu$ A
- High output current
  - **REF30E:  $\pm 10$ mA**
  - REF30: 25mA
- Output voltage options
  - **REF30E: 1.25V to 5V**
  - REF30: 1.25V to 4.096V
- Temperature range: **-40°C to +125°C**

## 2 Applications

- [Field transmitter & sensor](#)
- [Solar energy](#)
- [PLC, DCS & PAC](#)
- [Energy storage systems](#)
- [Medical & healthcare](#)
- [AC inverter & VF drives](#)
- [Handheld Test Equipment](#)

## 3 Description

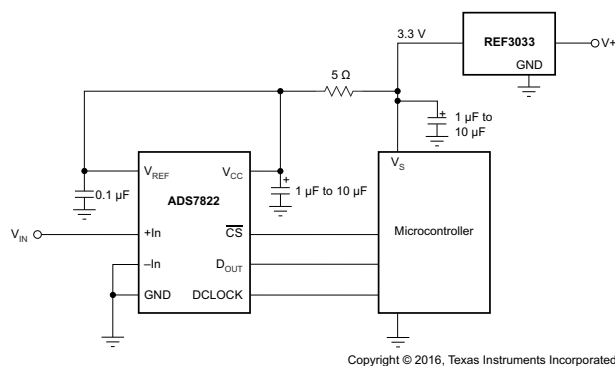
The REF30 is a precision, low-power, low-dropout voltage, reference family available in a tiny 3-pin SOT-23 package. REF30E is the enhanced performance version of REF30 family which is designed for precision applications. The REF30E offers improved temperature drift and initial accuracy while operating at a lower quiescent current of 25 $\mu$ A.

The low power consumption and the improved precision make the REF30E very attractive for loop-powered industrial applications such as pressure and temperature transmitter and battery-powered applications. The REF30/REF30E is specified over the extended industrial temperature range of -40°C to +125°C. The REF30 is easy to use in intrinsically safe and explosion-proof applications because the REF30 does not require a load capacitor to be stable.

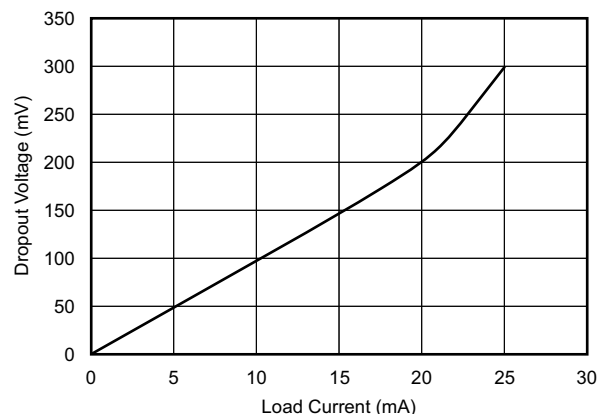
### Package Information

PART NUMBER	PACKAGE <sup>(1)</sup>	PACKAGE SIZE <sup>(2)</sup>
REF30xx	<a href="#">SOT-23 (3)</a>	2.92mm × 2.37mm
REF30xxE <sup>(3)</sup>	<a href="#">SOT-23 (3)</a>	2.92mm × 2.37mm

- (1) For all available packages, see the orderable addendum at the end of the data sheet.
- (2) The package size (length × width) is a nominal value and includes pins, where applicable.
- (3) This is preview device. Contact local TI support for samples.



**Typical Application**



**Dropout Voltage vs Load Current**



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## 4 Device Comparison Table

Device Comparison

PRODUCT		Voltage
REF30 <sup>(1)</sup>	REF30E <sup>(2)</sup>	
REF3012AIDBZR	REF3012EAIDBZR	1.25V
	REF3016EAIDBZR	1.65V
	REF3018EAIDBZR	1.8V
REF3020AIDBZR	REF3020EAIDBZR	2.048V
REF3025AIDBZR	REF3025EAIDBZR	2.5V
REF3030AIDBZR	REF3030EAIDBZR	3V
REF3033AIDBZR	REF3033EAIDBZR	3.3V
REF3040AIDBZR	REF3040EAIDBZR	4.096V
	REF3045EAIDBZR	4.5V
	REF3050EAIDBZR	5.0V

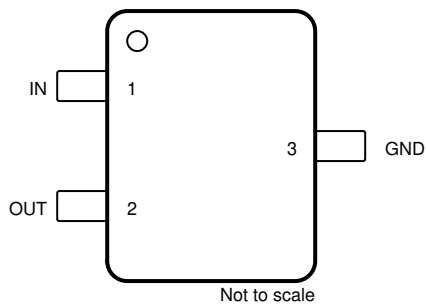
(1) This family is released to market.

(2) Product preview. Contact local TI support for samples.

Specification comparison

PART NUMBER	Initial Accuracy(%)	Max Temperature Drift (ppm/°C)	IQ (μA)
REF30	±0.2	75 (–40°C to +125°C), 65 (–40°C to +125°C), 50 (0°C to 70°C)	42
REF30E	±0.1	20 (–40°C to +125°C), 15 (–40°C to +125°C), 15 (0°C to 70°C)	25

## 5 Pin Configuration and Functions



**Figure 5-1. DBZ Package 3-Pin SOT-23 Top View**

**Table 5-1. Pin Functions**

PIN		I/O	DESCRIPTION
NO.	NAME		
1	IN	Input	Input supply voltage
2	OUT	Output	Reference output voltage
3	GND	—	Ground

## 6 Specifications

### 6.1 Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted)<sup>(1)</sup>

		MIN	MAX	UNIT
Supply voltage, IN to GND	REF30xx		7	V
	REF30xxE		6	V
Output short-circuit current <sup>(2)</sup>			70	mA
Operating temperature		-40	125	°C
Junction temperature (T <sub>J</sub> max)			150	°C
Storage temperature range (T <sub>stg</sub> )		-65	150	°C

(1) Stresses beyond those listed under Absolute Maximum Ratings can cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions. Exposure to absolute-maximum-rated conditions for extended periods can affect device reliability.

(2) Short circuit to ground.

### 6.2 ESD Ratings

			VALUE	UNIT
V <sub>(ESD)</sub>	Electrostatic discharge <sup>(3)</sup>	Human body model (HBM), per ANSI/ESDA/JEDEC JS-001, all pins <sup>(1)</sup>	±4000	V
		Charged device model (CDM), per JEDEC specification JESD22-C101, all pins <sup>(2)</sup>	±1500	
V <sub>(ESD)</sub>	Electrostatic discharge <sup>(4)</sup>	Human body model (HBM), per ANSI/ESDA/JEDEC JS-001, all pins <sup>(1)</sup>	±2000	
		Charged device model (CDM), per JEDEC specification JESD22-C101, all pins <sup>(2)</sup>	±500	

(1) JEDEC document JEP155 states that 500V HBM allows safe manufacturing with a standard ESD control process.

(2) JEDEC document JEP157 states that 250V CDM allows safe manufacturing with a standard ESD control process.

(3) Specification for REF30

(4) Specification for REF30E

### 6.3 Recommended Operating Conditions

over operating free-air temperature range (unless otherwise noted)

		MIN	NOM	MAX	UNIT
V <sub>IN</sub>	Input voltage <sup>(2)</sup>	V <sub>OUT</sub> + 0.05 <sup>(1)</sup>		5.5	V
V <sub>IN</sub>	Input voltage <sup>(3)</sup>	V <sub>OUT</sub> + 0.2 <sup>(1)</sup>		5.75	V
I <sub>LOAD</sub>	Load current <sup>(2)</sup>			25	mA
I <sub>LOAD</sub>	Load current <sup>(3)</sup>	-10		10	mA
T <sub>A</sub>	Operating temperature	-40		125	°C

(1) For I<sub>L</sub> > 0mA, see respective electrical table. Minimum supply voltage for REF3012, REF3012E AND REF3016E is 1.8V .

(2) Specification for REF30xx

(3) Specification for REF30xxE

## 6.4 Thermal Information

THERMAL METRIC <sup>(1)</sup>		REF30XX	REF30XXE	UNIT
		DBZ (SOT-23)	DBZ (SOT-23)	
		3 PINS	3 PINS	
R <sub>θJA</sub>	Junction-to-ambient thermal resistance	297.3	218.5	°C/W
R <sub>θJC(top)</sub>	Junction-to-case (top) thermal resistance	128.5	120.6	°C/W
R <sub>θJB</sub>	Junction-to-board thermal resistance	91.7	48.7	°C/W
Ψ <sub>JT</sub>	Junction-to-top characterization parameter	12.8	14.5	°C/W
Ψ <sub>JB</sub>	Junction-to-board characterization parameter	90.3	48.2	°C/W
R <sub>θJC(bot)</sub>	Junction-to-case (bottom) thermal resistance	N/A	N/A	°C/W

(1) For more information about traditional and new thermal metrics, see the [SPRA953](#) application report.

## 6.5 REF30E

at T<sub>A</sub> = 25°C, V<sub>IN</sub> = V<sub>OUT</sub> + 200mV, C<sub>IN</sub> = 0.1μF, C<sub>OUT</sub> = 0.1μF and I<sub>LOAD</sub> = 0mA (unless otherwise noted)

PARAMETER		TEST CONDITION	MIN	TYP	MAX	UNIT
<b>REF3012E (1.25V)<sup>(1)</sup></b>						
V <sub>OUT</sub>	Output Voltage		1.24875	1.25	1.25125	V
	Initial accuracy		-0.1		0.1	%
	Output voltage noise	f = 0.1Hz to 10Hz		31.25		μV <sub>PP</sub>
		f = 10Hz to 10kHz		30		μV <sub>rms</sub>
	Line regulation	1.8V ≤ V <sub>OUT</sub> ≤ 5.75V		31	125	μV/V
<b>REF3016E (1.65V)<sup>(1)</sup></b>						
V <sub>OUT</sub>	Output Voltage		1.64835	1.65	1.65165	V
	Initial accuracy		-0.1		0.1	%
	Output voltage noise	f = 0.1Hz to 10Hz		40		μV <sub>PP</sub>
		f = 10Hz to 10kHz		42		μV <sub>rms</sub>
	Line regulation	1.8V ≤ V <sub>OUT</sub> ≤ 5.75V		41	165	μV/V
<b>REF3018E (1.8V)</b>						
V <sub>OUT</sub>	Output Voltage		1.7982	1.8	1.8018	V
	Initial accuracy		-0.1		0.1	%
	Output voltage noise	f = 0.1Hz to 10Hz		45		μV <sub>PP</sub>
		f = 10Hz to 10kHz		51		μV <sub>rms</sub>
	Line regulation	V <sub>OUT</sub> + 200mV ≤ V <sub>IN</sub> ≤ 5.75V		41	165	μV/V
<b>REF3020E (2.048V)</b>						
V <sub>OUT</sub>	Output Voltage		2.045952	2.048	2.050048	V
	Initial accuracy		-0.1		0.1	%
	Output voltage noise	f = 0.1Hz to 10Hz		51.2		μV <sub>PP</sub>
		f = 10Hz to 10kHz		51		μV <sub>rms</sub>
	Line regulation	V <sub>OUT</sub> + 200mV ≤ V <sub>IN</sub> ≤ 5.75V		51	205	μV/V
<b>REF3025E (2.5V)</b>						
V <sub>OUT</sub>	Output Voltage		2.4975	2.5	2.5025	V
	Initial accuracy		-0.1		0.1	%
	Output voltage noise	f = 0.1Hz to 10Hz		62.5		μV <sub>PP</sub>
		f = 10Hz to 10kHz		62		μV <sub>rms</sub>
	Line regulation	V <sub>OUT</sub> + 200mV ≤ V <sub>IN</sub> ≤ 5.75V		63	250	μV/V

## 6.5 REF30E (continued)

at  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = V_{OUT} + 200\text{mV}$ ,  $C_{IN} = 0.1\mu\text{F}$ ,  $C_{OUT} = 0.1\mu\text{F}$  and  $I_{LOAD} = 0\text{mA}$  (unless otherwise noted)

PARAMETER		TEST CONDITION	MIN	TYP	MAX	UNIT
<b>REF3030E (3.0V)</b>						
$V_{OUT}$	Output Voltage		2.997	3	3.003	V
	Initial accuracy		-0.1		0.1	%
	Output voltage noise	$f = 0.1\text{Hz to } 10\text{Hz}$		75		$\mu\text{V}_{PP}$
		$f = 10\text{Hz to } 10\text{kHz}$		75		$\mu\text{V}_{rms}$
	Line regulation	$V_{OUT} + 200\text{mV} \leq V_{IN} \leq 5.75\text{V}$		75	300	$\mu\text{V/V}$
<b>REF3033E (3.3V)</b>						
$V_{OUT}$	Output Voltage		3.2967	3.3	3.3033	V
	Initial accuracy		-0.1		0.1	%
	Output voltage noise	$f = 0.1\text{Hz to } 10\text{Hz}$		82.5		$\mu\text{V}_{PP}$
		$f = 10\text{Hz to } 10\text{kHz}$		82.5		$\mu\text{V}_{rms}$
	Line regulation	$V_{OUT} + 200\text{mV} \leq V_{IN} \leq 5.75\text{V}$		83	330	$\mu\text{V/V}$
<b>REF3040E (4.096V)</b>						
$V_{OUT}$	Output Voltage		4.091904	4.096	4.100096	V
	Initial accuracy		-0.1		0.1	%
	Output voltage noise	$f = 0.1\text{Hz to } 10\text{Hz}$		102.4		$\mu\text{V}_{PP}$
	Output voltage noise	$f = 10\text{Hz to } 10\text{kHz}$		102		$\mu\text{V}_{rms}$
	Line regulation	$V_{OUT} + 200\text{mV} \leq V_{IN} \leq 5.75\text{V}$		102	470	$\mu\text{V/V}$
<b>REF3045E (4.5V)</b>						
$V_{OUT}$	Output Voltage		4.4955	4.5	4.5045	V
	Initial accuracy		-0.1		0.1	%
	Output voltage noise	$f = 0.1\text{Hz to } 10\text{Hz}$		112.5		$\mu\text{V}_{PP}$
		$f = 10\text{Hz to } 10\text{kHz}$		112		$\mu\text{V}_{rms}$
	Line regulation	$V_{OUT} + 200\text{mV} \leq V_{IN} \leq 5.75\text{V}$		110	520	$\mu\text{V/V}$
<b>REF3050E (5.0V)</b>						
$V_{OUT}$	Output Voltage		4.995	5	5.005	V
	Initial accuracy		-0.1		0.1	%
	Output voltage noise	$f = 0.1\text{Hz to } 10\text{Hz}$		125		$\mu\text{V}_{PP}$
		$f = 10\text{Hz to } 10\text{kHz}$		125		$\mu\text{V}_{rms}$
	Line regulation	$V_{REF} + 200\text{mV} \leq V_{IN} \leq 5.75\text{V}$		125	1000	$\mu\text{V/V}$

## 6.5 REF30E (continued)

at  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = V_{OUT} + 200\text{mV}$ ,  $C_{IN} = 0.1\mu\text{F}$ ,  $C_{OUT} = 0.1\mu\text{F}$  and  $I_{LOAD} = 0\text{mA}$  (unless otherwise noted)

PARAMETER		TEST CONDITION	MIN	TYP	MAX	UNIT
REF30xxE						
dV <sub>OUT</sub> /dT	Output voltage temperature drift for A grade <sup>(2)</sup>	0°C ≤ T <sub>A</sub> ≤ 70°C		7	15	ppm/°C
		−40°C ≤ T <sub>A</sub> ≤ 85°C		8	15	
		−40°C ≤ T <sub>A</sub> ≤ 125°C		10	20	
	Long Term stability	0000h to 1000h		40		ppm
		1000h to 2000h		15		
dV <sub>OUT</sub> /dI <sub>LOAD</sub>	Load regulation Source <sup>(3)</sup>	0mA < I <sub>LOAD</sub> < 10mA, V <sub>IN</sub> = V <sub>OUT</sub> + 500mV <sup>(4)</sup>		3	15	ppm/mA
	Load regulation Sink <sup>(3)</sup>	0mA > I <sub>LOAD</sub> > - 10mA, V <sub>IN</sub> = V <sub>OUTF</sub> + 500mV <sup>(4)</sup>		3	15	ppm/mA
dT	Thermal hysteresis <sup>(6)</sup>			25		ppm
V <sub>DO</sub>	Dropout voltage <sup>(1)</sup>			20	200	mV
I <sub>SC</sub>	Short-circuit current	REF3012E, REF3016E, REF3018E, REF3020E		20		mA
I <sub>SC</sub>	Short-circuit current	REF3025E, REF3030E, REF3033E, REF3040E, REF3045E, REF3050E		40		mA
	Turnon settling time	To 0.1% with C <sub>L</sub> = 1μF		2		ms
POWER SUPPLY						
I <sub>Q</sub>	Quiescent current	TA = 25°C		25		μA
		−40°C ≤ T <sub>A</sub> ≤ 125°C			38	
CAPACITIVE LOAD						
C <sub>IN</sub>	Stable input capacitor range	−40°C ≤ T <sub>A</sub> ≤ 125°C		0.1		μF
C <sub>L</sub>	Stable output capacitor range <sup>(5)</sup>	−40°C ≤ T <sub>A</sub> ≤ 125°C		0.1	10	μF

- (1) The minimum supply voltage for the REF3012E and REF3016E is 1.8V.
- (2) Box method used to determine over temperature drift.
- (3) Typical value of load regulation reflects measurements using a force and sense contacts see Section 8.3.6 section.
- (4)  $V_{IN} = 1.8\text{V} + 500\text{mV}$  for REF3016E
- (5) ESR for the capacitor can range from 10m $\Omega$  to 500m $\Omega$ .
- (6) Thermal hysteresis procedure explained in more detail in Section 8.3.2 section.

## 6.6 REF30

at  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = 5\text{V}$ , and  $I_{LOAD} = 0\text{mA}$  (unless otherwise noted)

PARAMETER		TEST CONDITION	MIN	TYP	MAX	UNIT
<b>REF3012 (1.25V)<sup>(1)</sup></b>						
$V_{OUT}$	Output Voltage		1.2475	1.25	1.2525	V
	Initial accuracy				0.2	%
	Output voltage noise	$f = 10\text{Hz to } 1\text{kHz}$		14		$\mu\text{V}_{PP}$
		$f = 10\text{Hz to } 10\text{kHz}$		42		$\mu\text{V}_{rms}$
	Line regulation	$1.8\text{V} \leq V_{IN} \leq 5.5\text{V}$		60	190	$\mu\text{V/V}$
<b>REF3020 (2.048V)</b>						
$V_{OUT}$	Output Voltage		2.044	2.048	2.052	V
	Initial accuracy				0.2	%
	Output voltage noise	$f = 10\text{Hz to } 1\text{kHz}$		23		$\mu\text{V}_{PP}$
		$f = 10\text{Hz to } 10\text{kHz}$		65		$\mu\text{V}_{rms}$
	Line regulation	$V_{REF} + 50\text{mV} \leq V_{IN} \leq 5.5\text{V}$		110	290	$\mu\text{V/V}$
<b>REF3025 (2.5V)</b>						
$V_{OUT}$	Output Voltage		2.495	2.5	2.505	V
	Initial accuracy				0.2	%
	Output voltage noise	$f = 10\text{Hz to } 1\text{kHz}$		28		$\mu\text{V}_{PP}$
		$f = 10\text{Hz to } 10\text{kHz}$		80		$\mu\text{V}_{rms}$
	Line regulation	$V_{REF} + 50\text{mV} \leq V_{IN} \leq 5.5\text{V}$		120	325	$\mu\text{V/V}$
<b>REF3030 (3.0V)</b>						
$V_{OUT}$	Output Voltage		2.994	3	3.06	V
	Initial accuracy				0.2	%
	Output voltage noise	$f = 10\text{Hz to } 1\text{kHz}$		33		$\mu\text{V}_{PP}$
		$f = 10\text{Hz to } 10\text{kHz}$		94		$\mu\text{V}_{rms}$
	Line regulation	$V_{REF} + 50\text{mV} \leq V_{IN} \leq 5.5\text{V}$		120	375	$\mu\text{V/V}$
<b>REF3033 (3.3V)</b>						
$V_{OUT}$	Output Voltage		3.294	3.3	3.306	V
	Initial accuracy				0.2	%
	Output voltage noise	$f = 10\text{Hz to } 1\text{kHz}$		36		$\mu\text{V}_{PP}$
		$f = 10\text{Hz to } 10\text{kHz}$		105		$\mu\text{V}_{rms}$
	Line regulation	$V_{REF} + 50\text{mV} \leq V_{IN} \leq 5.5\text{V}$		130	400	$\mu\text{V/V}$
<b>REF3040 (4.096V)</b>						
$V_{OUT}$	Output Voltage		4.088	4.096	4.104	V
	Initial accuracy				0.2	%
	Output voltage noise	$f = 10\text{Hz to } 1\text{kHz}$		45		$\mu\text{V}_{PP}$
		$f = 10\text{Hz to } 10\text{kHz}$		128		$\mu\text{V}_{rms}$
	Line regulation	$V_{REF} + 50\text{mV} \leq V_{IN} \leq 5.5\text{V}$		160	410	$\mu\text{V/V}$



## 6.6 REF30 (continued)

at  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = 5\text{V}$ , and  $I_{LOAD} = 0\text{mA}$  (unless otherwise noted)

PARAMETER		TEST CONDITION		MIN	TYP	MAX	UNIT
REF30xx							
dV <sub>OUT</sub> /dT	Output voltage temperature drift <sup>(2)</sup>	0°C ≤ T <sub>A</sub> ≤ 70°C			20	50	ppm/°C
		−30°C ≤ T <sub>A</sub> ≤ +85°C			28	60	
		−40°C ≤ T <sub>A</sub> ≤ +85°C			30	65	
		−40°C ≤ T <sub>A</sub> ≤ +125°C			35	75	
dV <sub>OUT</sub> /dI <sub>LOAD</sub>	Load regulation <sup>(3)</sup>	0mA < I <sub>LOAD</sub> < 25mA, V <sub>IN</sub> = V <sub>REF</sub> + 500mV <sup>(1)</sup>			3	100	ppm
	Thermal hysteresis <sup>(4)</sup>				25	100	ppm
	Long Term stability	0000h to 1000h			24		ppm
		1000h to 2000h			15		
V <sub>DO</sub>	Dropout voltage				1	50	mV
I <sub>SC</sub>	Short-circuit current				45		mA
	Turnon settling time	To 0.1% with C <sub>L</sub> = 1μF			120		μs
POWER SUPPLY							
I <sub>Q</sub>	Quiescent current	T <sub>A</sub> = 25C			42	50	μA
		−40°C ≤ T <sub>A</sub> ≤ +125°C				59	

(1) The minimum supply voltage for the REF3012 is 1.8V.

(2) Box method used to determine over temperature drift.

(3) Typical value of load regulation reflects measurements using a force and sense contacts see Section 8.3.6 section.

(4) Thermal hysteresis procedure explained in more detail in Section 8.3.2 section.

## 6.7 Typical Characteristics REF30E

at  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = 5\text{V}$ , and REF3025E used for typical characteristics (unless otherwise noted). All plots are preview and can change at the time of release to production.

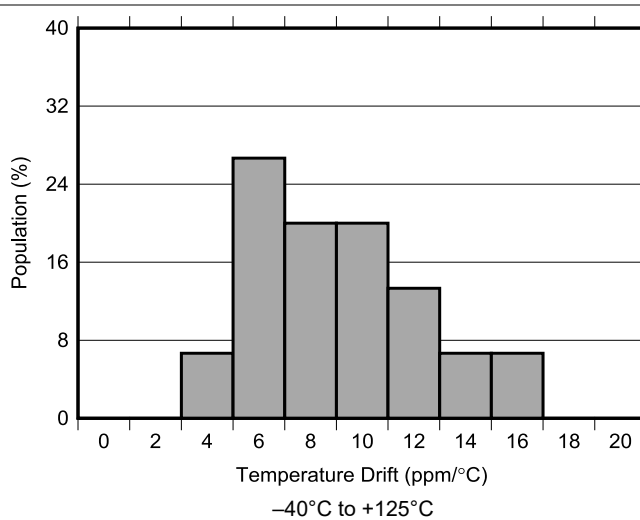


Figure 6-1. Temperature Drift Histogram

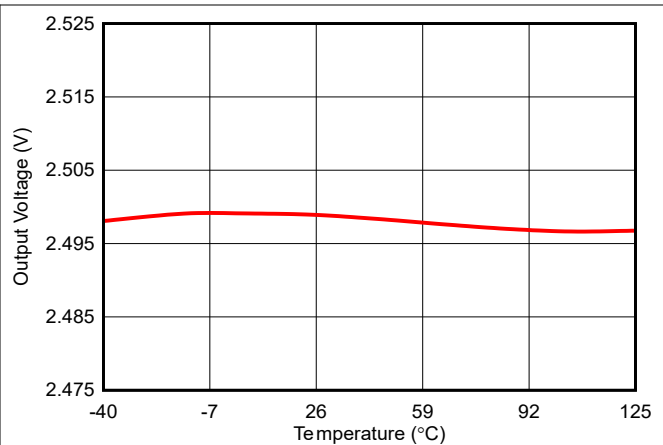


Figure 6-2. Output Voltage vs Temperature

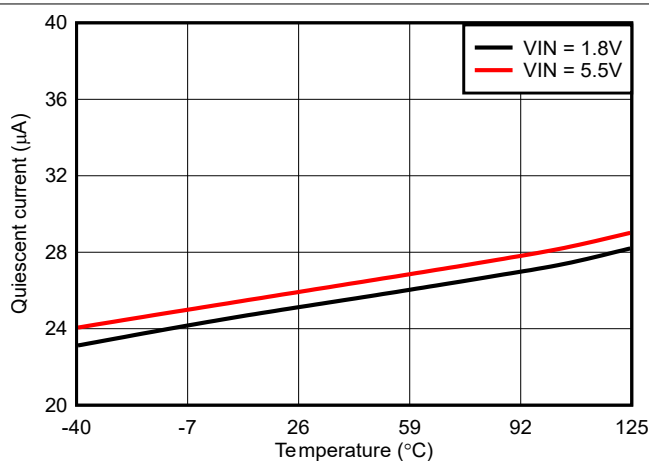


Figure 6-3. Quiescent Current vs Temperature  $V_{OUT} = 1.25\text{V}$

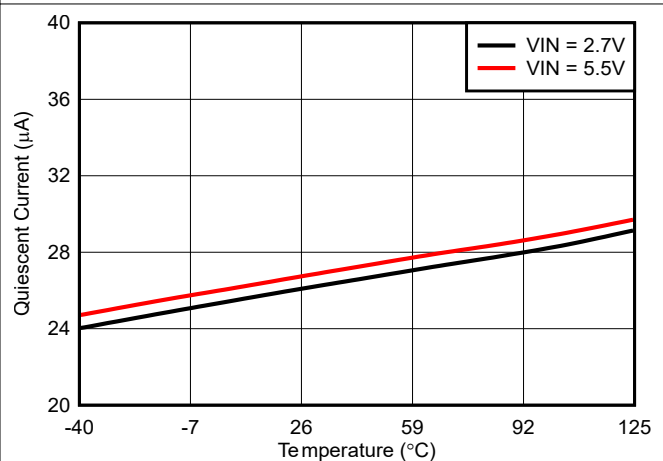


Figure 6-4. Quiescent Current vs Temperature  $V_{OUT} = 2.5\text{V}$

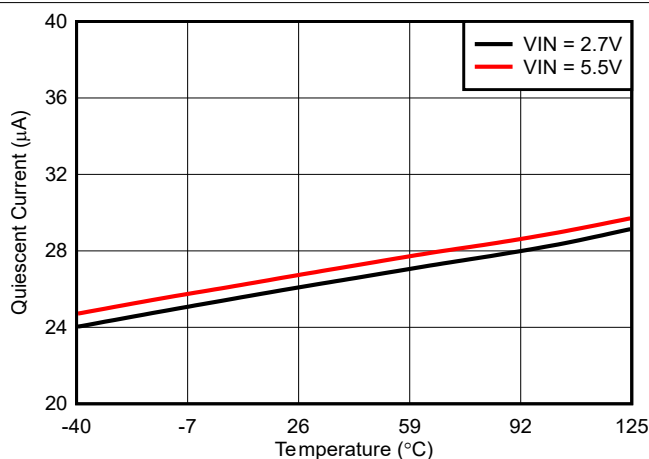


Figure 6-5. Quiescent Current vs Temperature  $V_{OUT} = 3\text{V}$

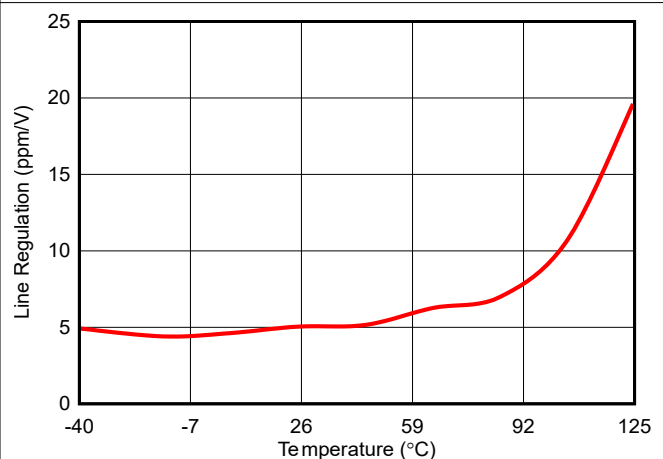


Figure 6-6. Line Regulation vs Temperature

## 6.7 Typical Characteristics REF30E (continued)

at  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = 5\text{V}$ , and REF3025E used for typical characteristics (unless otherwise noted). All plots are preview and can change at the time of release to production.

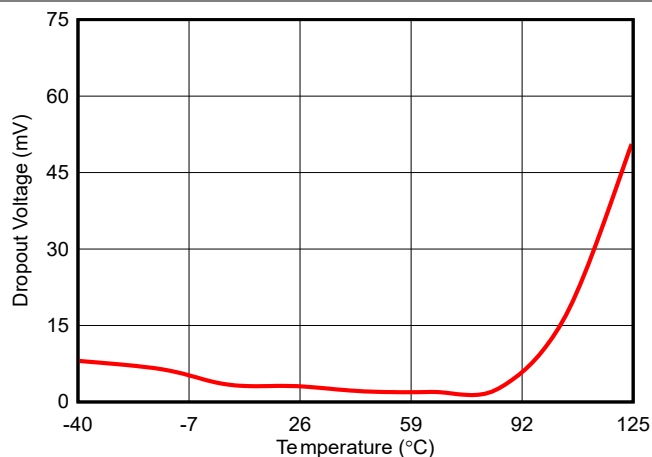


Figure 6-7. Dropout Voltage Vs Temperature

## 6.8 Typical Characteristics REF30

at  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = 5\text{V}$ , and REF3025 used for typical characteristics (unless otherwise noted)

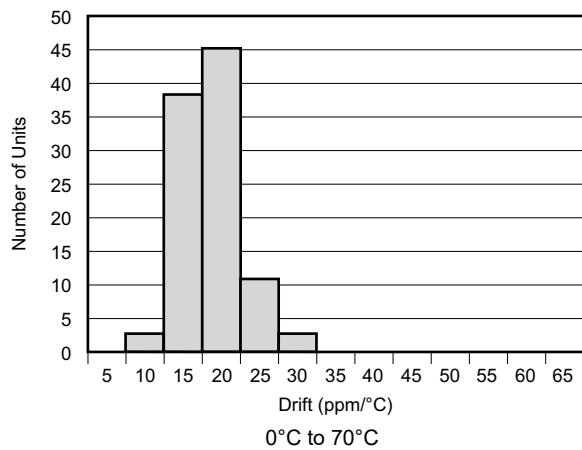


Figure 6-8. Temperature Drift

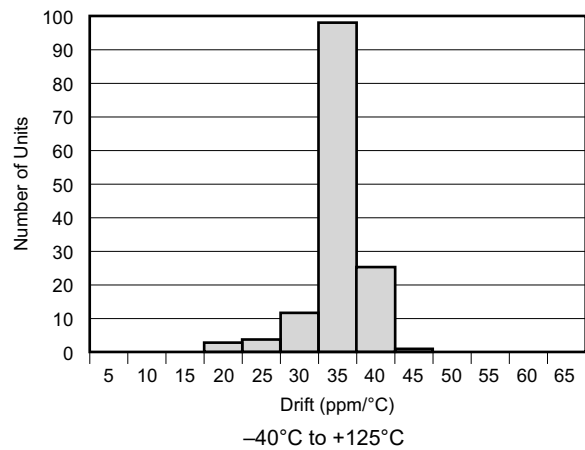


Figure 6-9. Temperature Drift

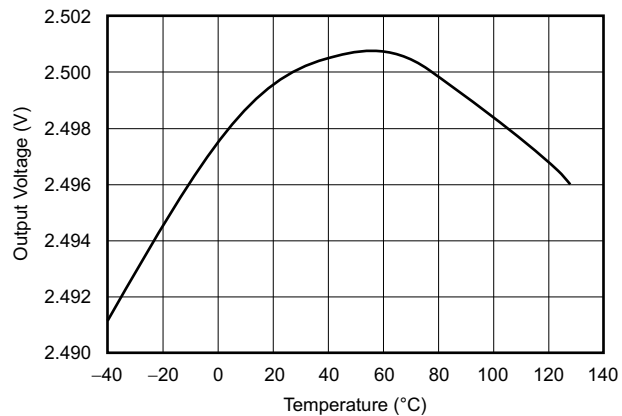


Figure 6-10. Output Voltage vs Temperature

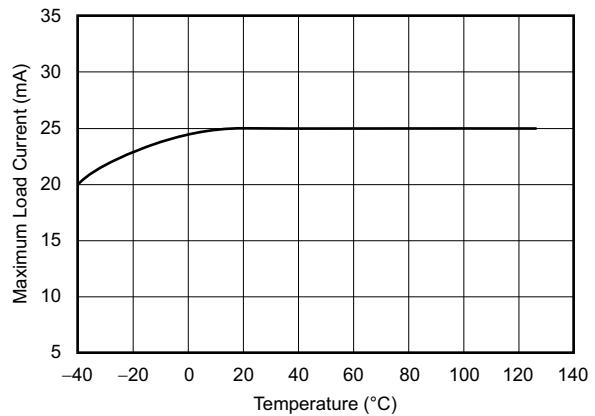


Figure 6-11. Maximum Load Current vs Temperature

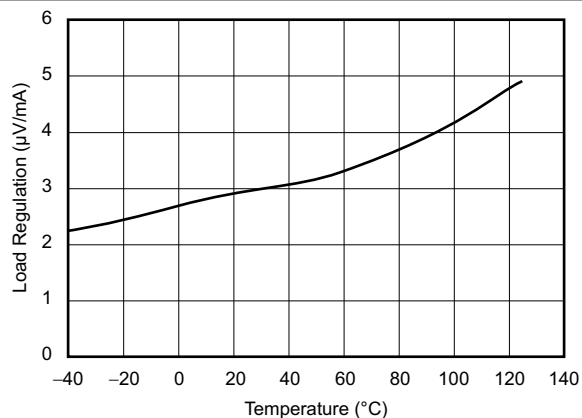


Figure 6-12. Load Regulation vs Temperature

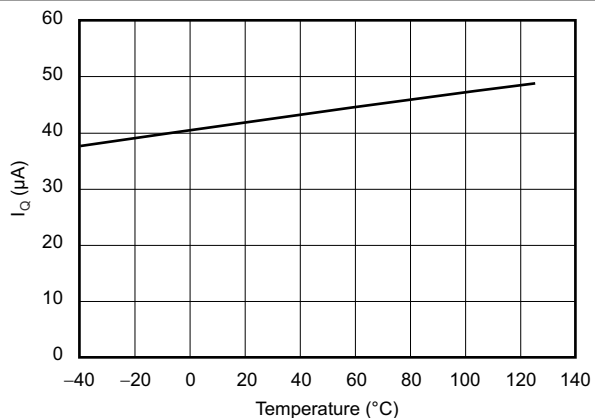


Figure 6-13. Quiescent Current vs Temperature

## 6.8 Typical Characteristics REF30 (continued)

at  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = 5\text{V}$ , and REF3025 used for typical characteristics (unless otherwise noted)

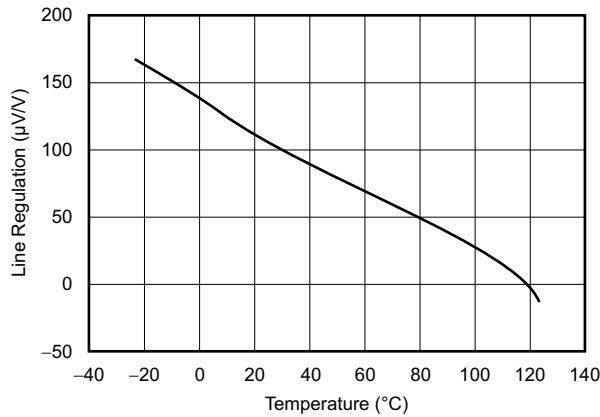


Figure 6-14. Line Regulation vs Temperature

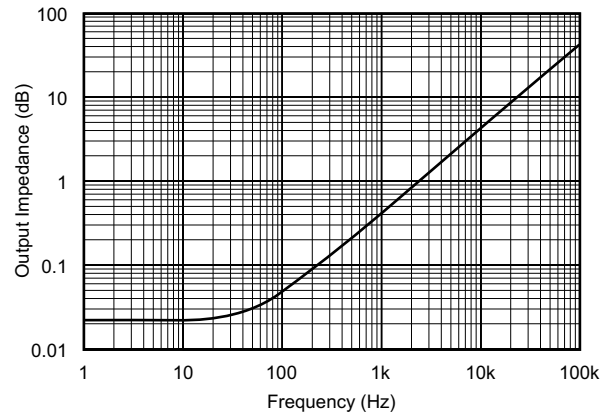


Figure 6-15. Output Impedance vs Frequency

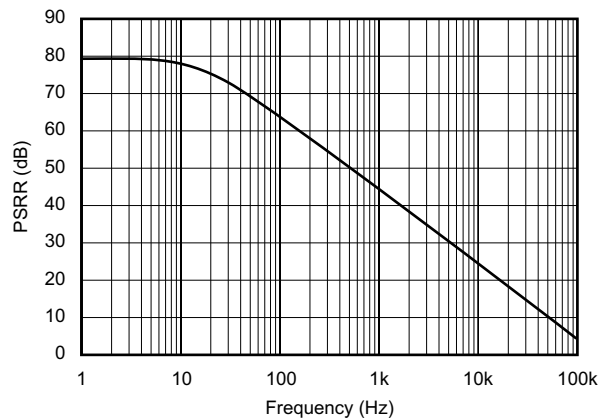


Figure 6-16. Power-Supply Rejection Ratio vs Frequency

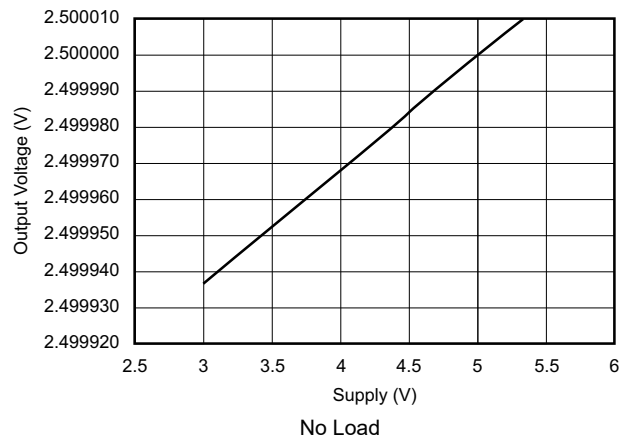


Figure 6-17. Output Voltage vs Supply Voltage

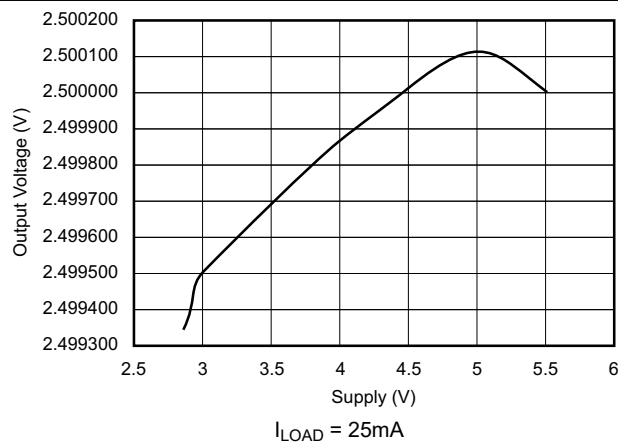


Figure 6-18. Output Voltage vs Supply Voltage

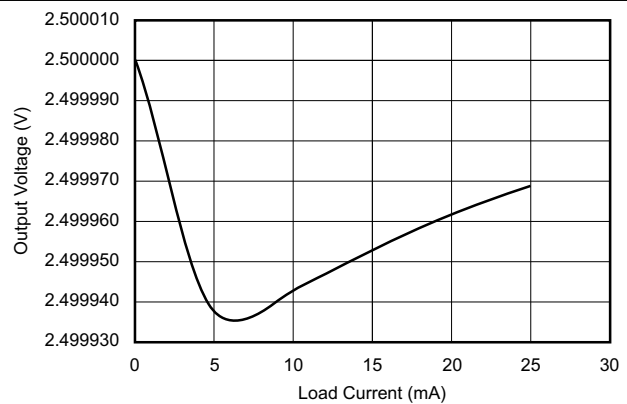


Figure 6-19. Output Voltage vs Load Current

## 6.8 Typical Characteristics REF30 (continued)

at  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = 5\text{V}$ , and REF3025 used for typical characteristics (unless otherwise noted)

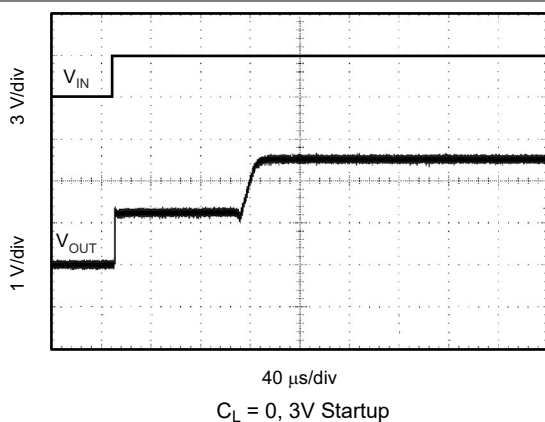


Figure 6-20. Step Response

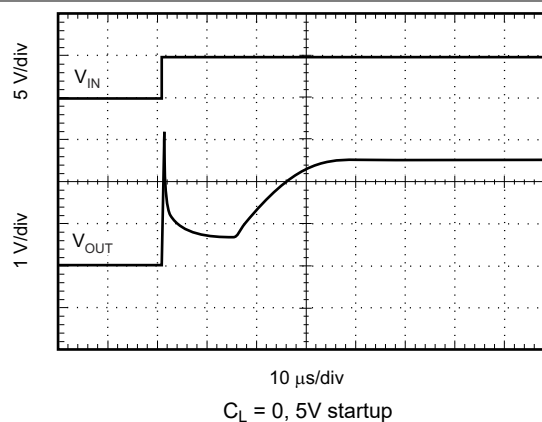


Figure 6-21. Step Response

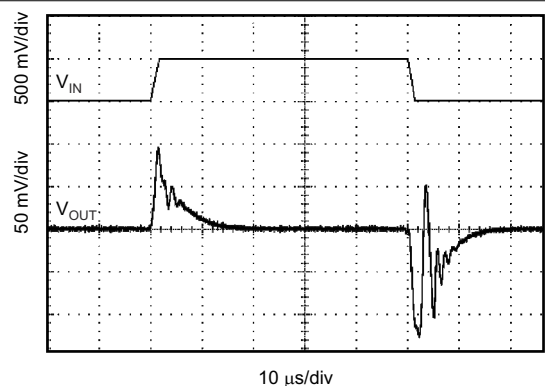


Figure 6-22. Line Transient Response

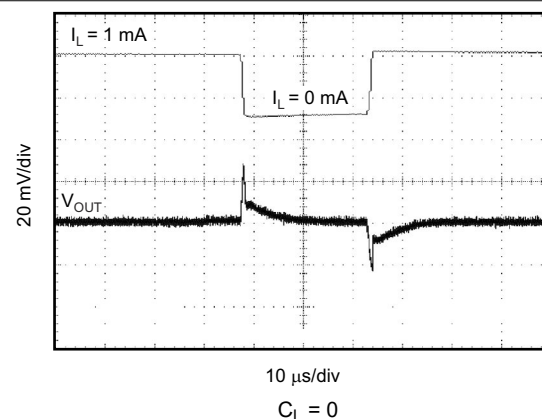


Figure 6-23. 0mA to 1mA Load Transient

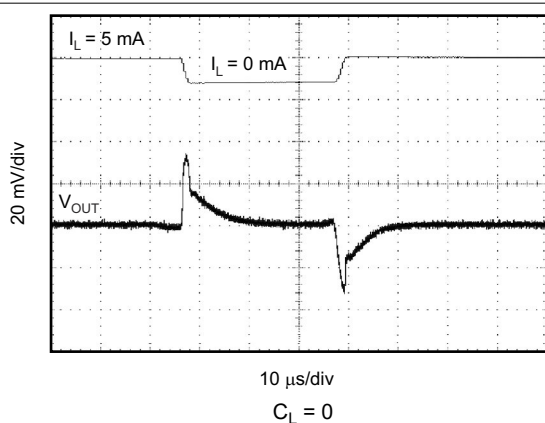


Figure 6-24. 0mA to 5mA Load Transient

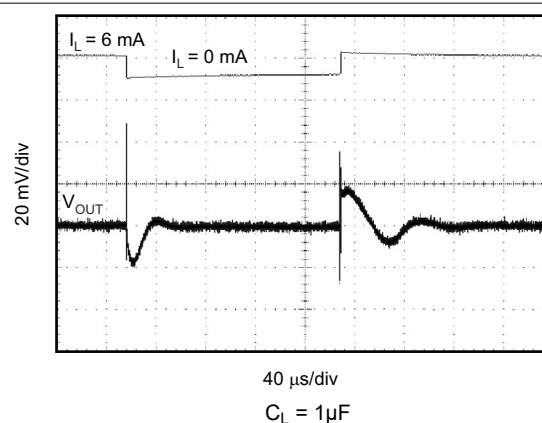
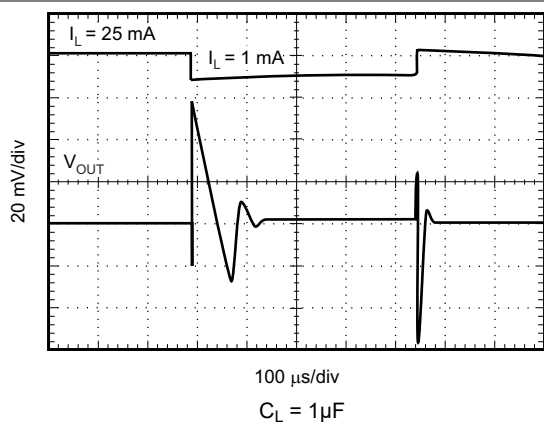


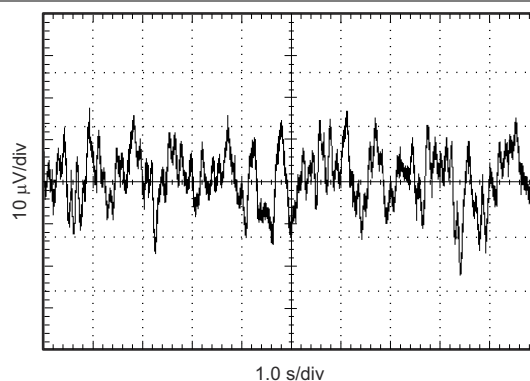
Figure 6-25. 1mA to 6mA Load Transient

## 6.8 Typical Characteristics REF30 (continued)

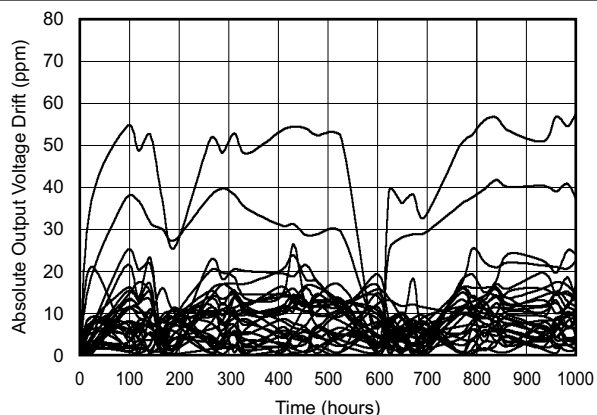
at  $T_A = 25^\circ\text{C}$ ,  $V_{IN} = 5\text{V}$ , and REF3025 used for typical characteristics (unless otherwise noted)



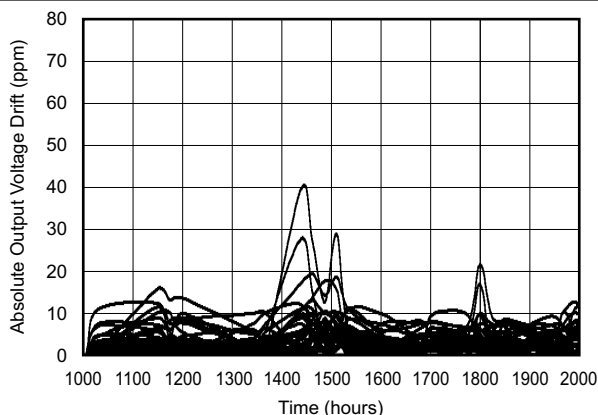
**Figure 6-26. 1mA to 25mA Load Transient**



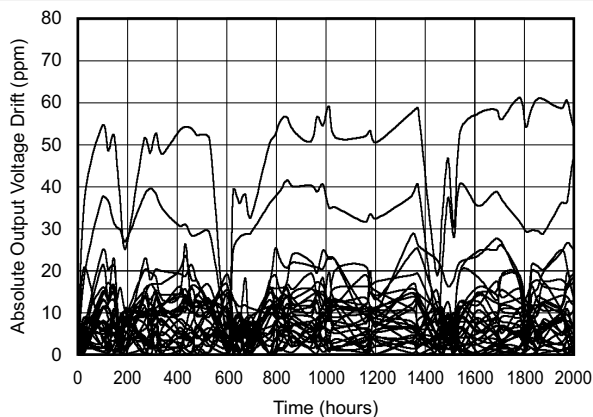
**Figure 6-27. 0.1Hz to 10Hz Noise**



**Figure 6-28. Long-Term Stability: 0 to 1000 Hours**



**Figure 6-29. Long-Term Stability: 1000 to 2000 Hours**



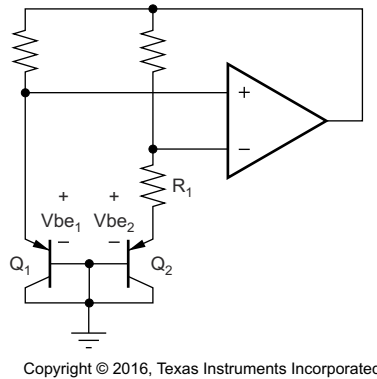
**Figure 6-30. Long-Term Stability: 0 to 2000 Hours**

## 7 Detailed Description

### 7.1 Overview

The REF30 is a series, precision bandgap voltage reference. The basic topology is shown in the [Section 7.2](#). Transistors  $Q_1$  and  $Q_2$  are biased so that the current density of  $Q_1$  is greater than that of  $Q_2$ . The difference of the two base-emitter voltages,  $V_{be1} - V_{be2}$ , has a positive temperature coefficient and is forced across resistor  $R_1$ . This voltage is gained up and added to the base-emitter voltage of  $Q_2$ , which has a negative coefficient. The resulting output voltage is virtually independent of temperature. The curvature of the bandgap voltage, as shown in [Figure 6-10](#), is due to the slightly nonlinear temperature coefficient of the base-emitter voltage of  $Q_2$ .

### 7.2 Functional Block Diagram



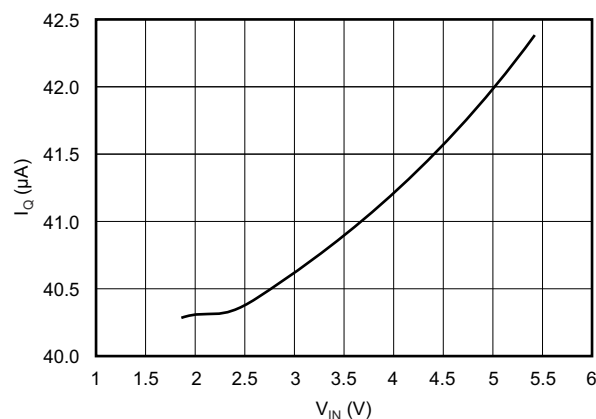
### 7.3 Feature Description

#### 7.3.1 Supply Voltage

The REF30 family of references features an extremely low dropout voltage. With the exception of the REF3012, which has a minimum supply requirement of 1.8V, the REF30 can be operated with a supply of only 1mV above the output voltage in an unloaded condition. For loaded conditions, a typical dropout voltage versus load is shown on the front page.

The REF30 features a low quiescent current that is extremely stable over changes in both temperature and supply. The typical room temperature quiescent current is 42μA, and the maximum quiescent current over temperature is just 59μA. Additionally, the quiescent current typically changes less than 2.5μA over the entire supply range, as shown in [Figure 7-1](#).

Supply voltages below the specified levels can cause the REF30 to momentarily draw currents greater than the typical quiescent current. Use a power supply with a fast rising edge and low output impedance to easily prevent this issue.



**Figure 7-1. Supply Current vs Supply Voltage**



### 7.3.2 Thermal Hysteresis

Thermal hysteresis for the REF30 is defined as the change in output voltage after operating the device at 25°C, cycling the device through the specified temperature range, and returning to 25°C, and can be expressed as shown in Equation 1:

$$V_{\text{HYST}} = \left( \frac{\text{abs}|V_{\text{PRE}} - V_{\text{POST}}|}{V_{\text{NOM}}} \right) \cdot 10^6 \text{ (ppm)} \quad (1)$$

where

- $V_{\text{HYST}}$  = Calculated hysteresis
- $V_{\text{PRE}}$  = Output voltage measured at 25°C pretemperature cycling
- $V_{\text{POST}}$  = Output voltage measured when device has been operated at 25°C, cycled through specified range of –40°C to +125°C, and returned to operation at 25°C.

### 7.3.3 Temperature Drift

The REF30 exhibits minimal drift error, defined as the change in output voltage over varying temperature. Using the *box* method of drift measurement, the REF30 features a typical drift coefficient of 20ppm from 0°C to 70°C, the primary temperature range of use for many applications. For industrial temperature ranges of –40°C to +125°C, the REF30 family drift increases to a typical value of 50ppm.

### 7.3.4 Noise Performance

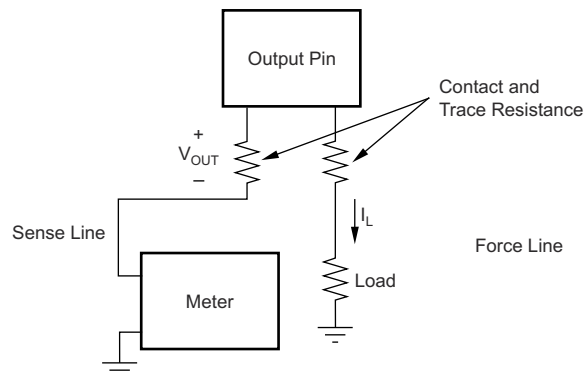
The REF30 generates noise less than 50μV<sub>PP</sub> between frequencies of 0.1Hz to 10Hz, and can be seen in Figure 6-27. The noise voltage of the REF30 increases with output voltage and operating temperature. Additional filtering can be used to improve output noise levels; however, make sure the output impedance does not degrade AC performance.

### 7.3.5 Long-Term Stability

Long-term stability refers to the change of the output voltage of a reference over a period of months or years. This effect lessens as time progresses as is apparent by the long-term stability curves. The typical drift value for the REF30 is 24ppm from 0 hours to 1000 hours, and 15ppm from 1000 hours to 2000 hours. This parameter is characterized by measuring 30 units at regular intervals for a period of 2000 hours.

### 7.3.6 Load Regulation

Load regulation is defined as the change in output voltage as a result of changes in load current. Use a 4 wire measurement (kelvin measurement) methodology for accurate load regulation measurement as shown in Figure 7-2. The force and sense lines tied to the contact area of the output pin reduce the impact of contact and trace resistance, resulting in accurate measurement of the load regulation contributed solely by the REF30xx.



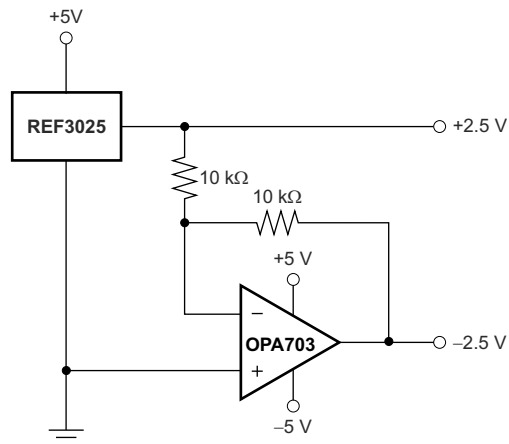
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**Figure 7-2. Accurate Load Regulation of REF30**

## 7.4 Device Functional Modes

### 7.4.1 Negative Reference Voltage

For applications requiring a negative and positive reference voltage, the [OPA703](#) and REF30 can be used to provide a dual-supply reference from a  $\pm 5\text{V}$  supply. [Figure 7-3](#) shows the REF3025 used to provide a  $\pm 2.5\text{V}$  supply reference voltage. The low offset voltage and low drift of the OPA703 complement the low drift performance of the REF30 to provide an accurate resolution for split-supply applications.

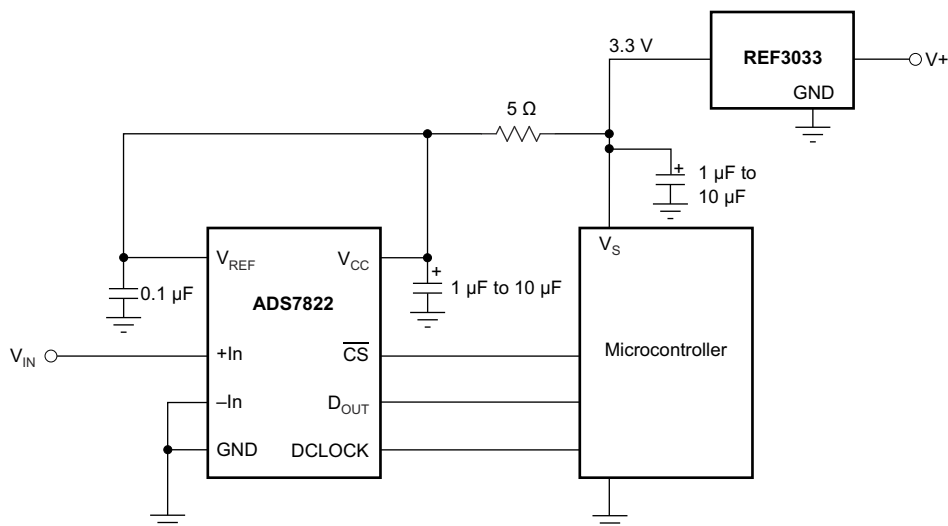


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**Figure 7-3. REF3025 Combined With OPA703 to Create Positive and Negative Reference Voltages.**

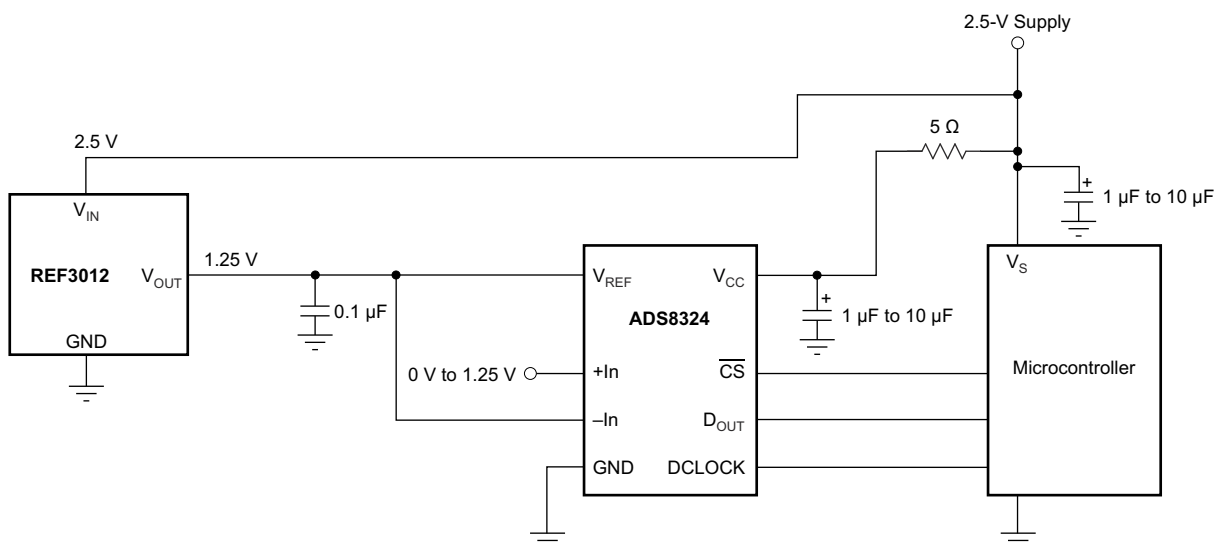
## 7.4.2 Data Acquisition

Often data acquisition systems require stable voltage references to maintain necessary accuracy. The REF30 family features stability and a wide range of voltages designed for most microcontrollers and data converters. Figure 7-4 and Figure 7-5 show two basic data acquisition systems.



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**Figure 7-4. Basic Data Acquisition System 1**



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**Figure 7-5. Basic Data Acquisition System 2**

## 8 Application and Implementation

### Note

Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes, as well as validating and testing their design implementation to confirm system functionality.

### 8.1 Application Information

For normal operation, the REF30 does not require a capacitor on the output. If a capacitive load is connected, take special care when using low equivalent series resistance (ESR) capacitors and high capacitance. This precaution is especially true for low-output voltage devices; therefore, for the REF3012 use a low-ESR capacitance of 10 $\mu$ F or less. Figure 8-1 shows the typical connections required for operation of the REF30. A supply bypass capacitor of 0.1 $\mu$ F is always recommended.

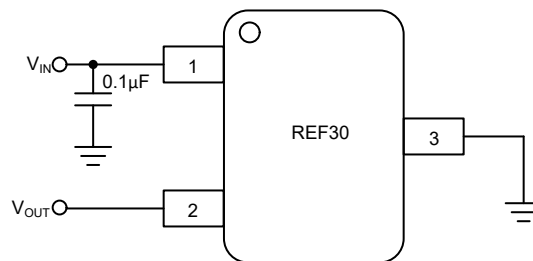
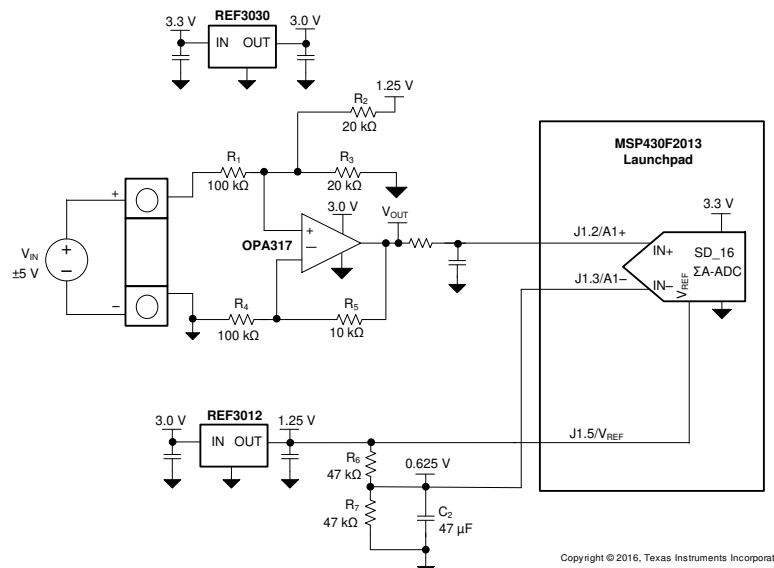


Figure 8-1. Typical Connections for Operating REF30

### 8.2 Typical Application

Figure 8-2 shows a low-power reference and conditioning circuit. This circuit attenuates and level-shifts a bipolar input voltage within the proper input range of a single-supply low power 16-Bit  $\Delta\Sigma$  ADC, such as the one inside the MSP430 or other similar single-supply ADCs. Precision reference circuits are used to level-shift the input signal, provide the ADC reference voltage and to create a well-regulated supply voltage for the low-power analog circuitry. A low-power, zero-drift, op-amp circuit is used to attenuate and level-shift the input signal.



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Figure 8-2. Low-Power Reference and Bipolar Voltage Conditioning Circuit for Low-Power ADCs

### 8.2.1 Design Requirements

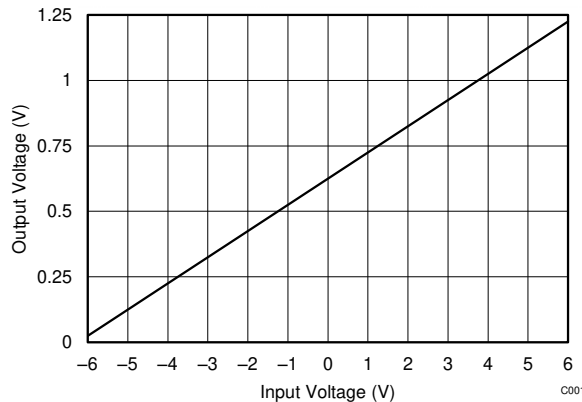
- Supply Voltage: 3.3V
- Maximum Input Voltage:  $\pm 6V$
- Specified Input Voltage:  $\pm 5V$
- ADC Reference Voltage: 1.25V

The goal for this design is to accurately condition a  $\pm 5V$  bipolar input voltage into a voltage that works for conversion by a low-voltage ADC with a 1.25V reference voltage,  $V_{REF}$ , and an input voltage range of  $V_{REF}/2$ . The circuit can function with reduced performance over a wider input range of at least  $\pm 6V$  to allow for easier protection of overvoltage conditions.

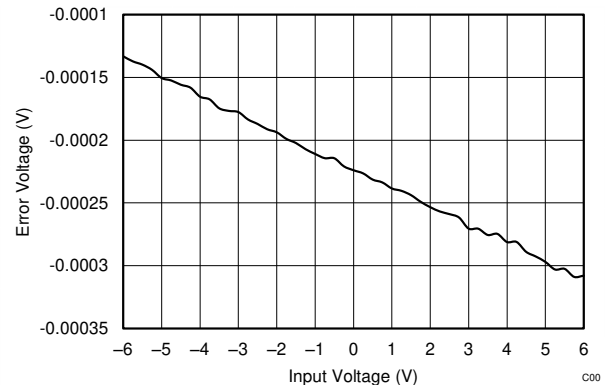
### 8.2.2 Detailed Design Procedure

Figure 8-2 depicts a simplified schematic for this design showing the MSP430 ADC inputs and full input conditioning circuitry. The ADC is configured for a bipolar measurement where final conversion result is the differential voltage between the voltage at the positive and negative ADC inputs. The bipolar, GND-referenced input signal must be level-shifted and attenuated by the op amp so that the output is biased to  $V_{REF}/2$  and has a differential voltage that is within the  $\pm V_{REF}/2$  input range of the ADC.

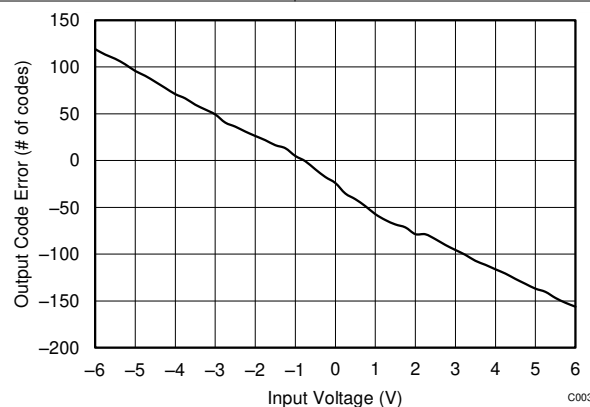
### 8.2.3 Application Curves



**Figure 8-3. OPA317 Output Voltage vs Input Voltage**



**Figure 8-4. OPA317 Output Voltage Error vs Input Voltage**



**Figure 8-5. Output Code Error vs Input Voltage**

### 8.3 Power Supply Recommendations

The REF30 family of references feature an extremely low-dropout voltage. These references can be operated with a supply of only 50mV above the output voltage. For loaded reference conditions, a typical dropout voltage versus load is shown in the front page plot, [Dropout Voltage vs Load Current](#). Use a supply bypass capacitor greater than 0.47μF.

### 8.4 Layout

#### 8.4.1 Layout Guidelines

Figure 8-6 illustrates an example of a printed-circuit board (PCB) layout using the REF30. Some key considerations are:

- Connect low-ESR, 0.1μF ceramic bypass capacitors at  $V_{IN}$  of the REF30.
- Decouple other active devices in the system per the device specifications.
- Use a solid ground plane to help distribute heat and reduces electromagnetic interference (EMI) noise pickup.
- Place the external components as close to the device as possible. This configuration prevents parasitic errors (such as the Seebeck effect) from occurring.
- Minimize trace length between the reference and bias connections to the INA and ADC to reduce noise pickup.
- Do not run sensitive analog traces in parallel with digital traces. Avoid crossing digital and analog traces if possible, and only make perpendicular crossings when absolutely necessary.

#### 8.4.2 Layout Example

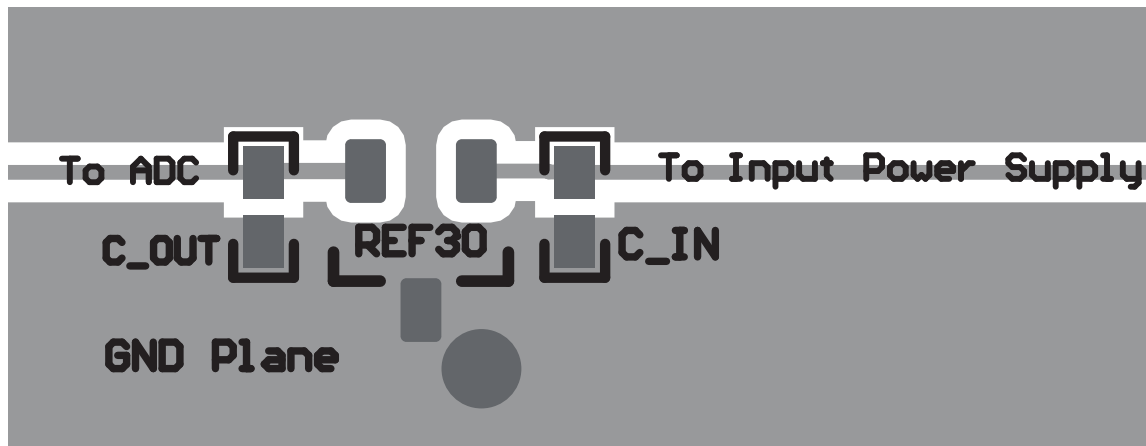


Figure 8-6. Layout Example

## 9 Device and Documentation Support

### 9.1 Documentation Support

#### 9.1.1 Related Documentation

[CMOS, Rail-to-Rail, I/O Operational Amplifiers](#) (SBOS180)

[REF29xx 100ppm/°C, 50µA in 3-Pin SOT-23 CMOS Voltage Reference](#) (SBVS033)

### 9.2 Related Links

[Table 9-1](#) lists quick access links. Categories include technical documents, support and community resources, tools and software, and quick access to sample or buy.

**Table 9-1. Related Links**

PARTS	PRODUCT FOLDER	SAMPLE & BUY	TECHNICAL DOCUMENTS	TOOLS & SOFTWARE	SUPPORT & COMMUNITY
REF3012	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>
REF3020	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>
REF3025	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>
REF3030	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>
REF3033	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>
REF3040	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>	<a href="#">Click here</a>

### 9.3 Receiving Notification of Documentation Updates

To receive notification of documentation updates, navigate to the device product folder on [ti.com](#). Click on *Notifications* to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document.

### 9.4 Support Resources

[TI E2E™ support forums](#) are an engineer's go-to source for fast, verified answers and design help — straight from the experts. Search existing answers or ask your own question to get the quick design help you need.

Linked content is provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's [Terms of Use](#).

### 9.5 Trademarks

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### 9.6 Electrostatic Discharge Caution



This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

### 9.7 Glossary

[TI Glossary](#) This glossary lists and explains terms, acronyms, and definitions.

## 10 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

<b>Changes from Revision I (July 2022) to Revision J (July 2025)</b>	<b>Page</b>
• Added preview information for upcoming device REF30E throughout the document.....	1
• Updated the GPN name from RE30xx to REF30 throughout the document.....	1
• Added REF30E device details.....	2
• Added typical characteristics plots for preview device REF30E.....	10
• Updated the bypass capacitor recommendation and typical connection diagram.....	20
• Updated the layout image for REF30 GPN.....	22

<b>Changes from Revision H (February 2018) to Revision I (July 2022)</b>	<b>Page</b>
• Updated the numbering format for tables, figures, and cross-references throughout the document.....	1

<b>Changes from Revision G (November 2015) to Revision H (February 2018)</b>	<b>Page</b>
• Added NOTE to the <a href="#">Section 8</a> section .....	20

<b>Changes from Revision F (August 2008) to Revision G (November 2015)</b>	<b>Page</b>
• Added <i>Device Information, ESD Ratings, Recommended Operating Conditions, and Thermal Information</i> tables.....	1
• Added <i>Detailed Description, Applications and Implementation, Power-Supply Recommendations, Layout, Device and Documentation Support, and Mechanical, Packaging, and Orderable Information</i> sections.....	1
• Changed text in <i>Description</i> section .....	1

## 11 Mechanical, Packaging, and Orderable Information

The following pages include mechanical packaging and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.



## PACKAGING INFORMATION

Orderable part number	Status (1)	Material type (2)	Package   Pins	Package qty   Carrier	RoHS (3)	Lead finish/ Ball material (4)	MSL rating/ Peak reflow (5)	Op temp (°C)	Part marking (6)
<a href="#">PREF3025EADBZR</a>	Active	Preproduction	SOT-23 (DBZ)   3	3000   LARGE T&R	-	Call TI	Call TI	-40 to 125	
<a href="#">PREF3030EADBZR</a>	Active	Preproduction	SOT-23 (DBZ)   3	3000   LARGE T&R	-	Call TI	Call TI	-40 to 125	
<a href="#">REF3012AIDBZR</a>	Active	Production	SOT-23 (DBZ)   3	3000   LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30A
<a href="#">REF3012AIDBZR.B</a>	Active	Production	SOT-23 (DBZ)   3	3000   LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30A
<a href="#">REF3012AIDBZT</a>	Active	Production	SOT-23 (DBZ)   3	250   SMALL T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30A
<a href="#">REF3012AIDBZT.B</a>	Active	Production	SOT-23 (DBZ)   3	250   SMALL T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30A
<a href="#">REF3020AIDBZR</a>	Active	Production	SOT-23 (DBZ)   3	3000   LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30B
<a href="#">REF3020AIDBZR.B</a>	Active	Production	SOT-23 (DBZ)   3	3000   LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30B
<a href="#">REF3020AIDBZT</a>	Active	Production	SOT-23 (DBZ)   3	250   SMALL T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30B
<a href="#">REF3020AIDBZT.B</a>	Active	Production	SOT-23 (DBZ)   3	250   SMALL T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30B
<a href="#">REF3025AIDBZR</a>	Active	Production	SOT-23 (DBZ)   3	3000   LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30C
<a href="#">REF3025AIDBZR.B</a>	Active	Production	SOT-23 (DBZ)   3	3000   LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30C
<a href="#">REF3025AIDBZT</a>	Active	Production	SOT-23 (DBZ)   3	250   SMALL T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30C
<a href="#">REF3025AIDBZT.B</a>	Active	Production	SOT-23 (DBZ)   3	250   SMALL T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30C
<a href="#">REF3030AIDBZR</a>	Active	Production	SOT-23 (DBZ)   3	3000   LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30F
<a href="#">REF3030AIDBZR.B</a>	Active	Production	SOT-23 (DBZ)   3	3000   LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30F
<a href="#">REF3030AIDBZT</a>	Active	Production	SOT-23 (DBZ)   3	250   SMALL T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30F
<a href="#">REF3030AIDBZT.B</a>	Active	Production	SOT-23 (DBZ)   3	250   SMALL T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30F
<a href="#">REF3033AIDBZR</a>	Active	Production	SOT-23 (DBZ)   3	3000   LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30D
<a href="#">REF3033AIDBZR.B</a>	Active	Production	SOT-23 (DBZ)   3	3000   LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30D
<a href="#">REF3033AIDBZT</a>	Active	Production	SOT-23 (DBZ)   3	250   SMALL T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30D
<a href="#">REF3033AIDBZT.B</a>	Active	Production	SOT-23 (DBZ)   3	250   SMALL T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30D
<a href="#">REF3040AIDBZR</a>	Active	Production	SOT-23 (DBZ)   3	3000   LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30E
<a href="#">REF3040AIDBZR.B</a>	Active	Production	SOT-23 (DBZ)   3	3000   LARGE T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30E
<a href="#">REF3040AIDBZT</a>	Active	Production	SOT-23 (DBZ)   3	250   SMALL T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30E
<a href="#">REF3040AIDBZT.B</a>	Active	Production	SOT-23 (DBZ)   3	250   SMALL T&R	Yes	NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	R30E

<sup>(1)</sup> **Status:** For more details on status, see our [product life cycle](#).

<sup>(2)</sup> **Material type:** When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

<sup>(3)</sup> **RoHS values:** Yes, No, RoHS Exempt. See the [TI RoHS Statement](#) for additional information and value definition.

<sup>(4)</sup> **Lead finish/Ball material:** Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

<sup>(5)</sup> **MSL rating/Peak reflow:** The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

<sup>(6)</sup> **Part marking:** There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

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## TAPE AND REEL INFORMATION



\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
REF3012AIDBZR	SOT-23	DBZ	3	3000	179.0	8.4	3.15	2.95	1.22	4.0	8.0	Q3
REF3012AIDBZT	SOT-23	DBZ	3	250	179.0	8.4	3.15	2.95	1.22	4.0	8.0	Q3
REF3020AIDBZR	SOT-23	DBZ	3	3000	179.0	8.4	3.15	2.95	1.22	4.0	8.0	Q3
REF3020AIDBZT	SOT-23	DBZ	3	250	179.0	8.4	3.15	2.95	1.22	4.0	8.0	Q3
REF3025AIDBZR	SOT-23	DBZ	3	3000	180.0	8.4	3.15	2.95	1.22	4.0	8.0	Q3
REF3025AIDBZT	SOT-23	DBZ	3	250	179.0	8.4	3.15	2.95	1.22	4.0	8.0	Q3
REF3030AIDBZR	SOT-23	DBZ	3	3000	179.0	8.4	3.15	2.95	1.22	4.0	8.0	Q3
REF3030AIDBZT	SOT-23	DBZ	3	250	179.0	8.4	3.15	2.95	1.22	4.0	8.0	Q3
REF3033AIDBZR	SOT-23	DBZ	3	3000	179.0	8.4	3.15	2.95	1.22	4.0	8.0	Q3
REF3033AIDBZT	SOT-23	DBZ	3	250	179.0	8.4	3.15	2.95	1.22	4.0	8.0	Q3
REF3040AIDBZR	SOT-23	DBZ	3	3000	179.0	8.4	3.15	2.95	1.22	4.0	8.0	Q3
REF3040AIDBZT	SOT-23	DBZ	3	250	179.0	8.4	3.15	2.95	1.22	4.0	8.0	Q3

## TAPE AND REEL BOX DIMENSIONS



\*All dimensions are nominal

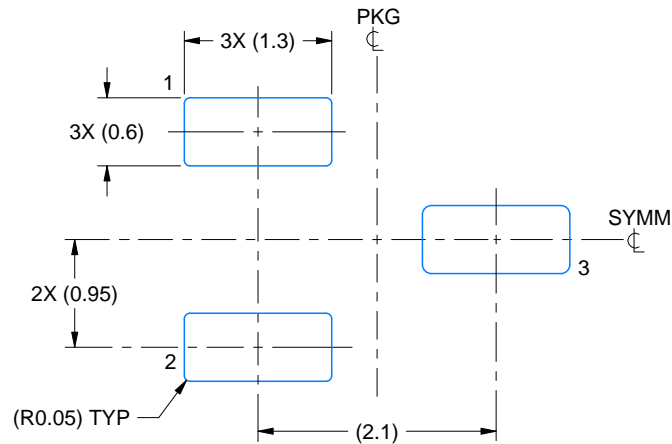
Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
REF3012AIDBZR	SOT-23	DBZ	3	3000	200.0	183.0	25.0
REF3012AIDBZT	SOT-23	DBZ	3	250	200.0	183.0	25.0
REF3020AIDBZR	SOT-23	DBZ	3	3000	200.0	183.0	25.0
REF3020AIDBZT	SOT-23	DBZ	3	250	200.0	183.0	25.0
REF3025AIDBZR	SOT-23	DBZ	3	3000	200.0	183.0	25.0
REF3025AIDBZT	SOT-23	DBZ	3	250	200.0	183.0	25.0
REF3030AIDBZR	SOT-23	DBZ	3	3000	200.0	183.0	25.0
REF3030AIDBZT	SOT-23	DBZ	3	250	200.0	183.0	25.0
REF3033AIDBZR	SOT-23	DBZ	3	3000	200.0	183.0	25.0
REF3033AIDBZT	SOT-23	DBZ	3	250	200.0	183.0	25.0
REF3040AIDBZR	SOT-23	DBZ	3	3000	200.0	183.0	25.0
REF3040AIDBZT	SOT-23	DBZ	3	250	200.0	183.0	25.0



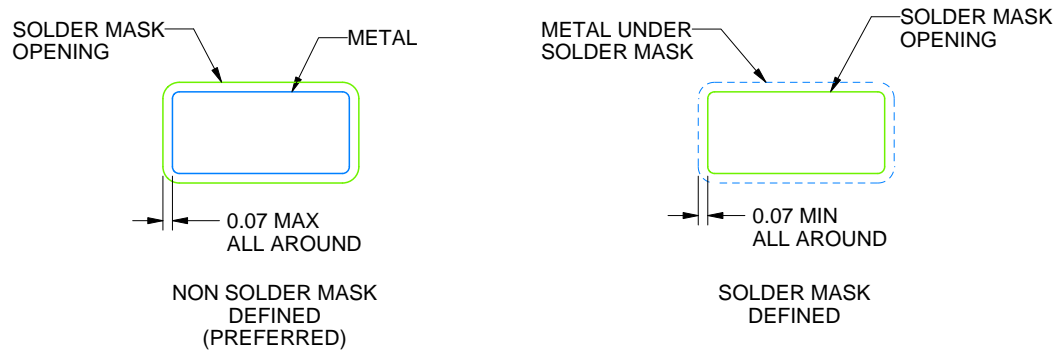
**DBZ0003A**

**SOT-23 - 1.12 mm max height**

### SMALL OUTLINE TRANSISTOR



LAND PATTERN EXAMPLE  
SCALE:15X



## SOLDER MASK DETAILS

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NOTES: (continued)

5. Publication IPC-7351 may have alternate designs.
6. Solder mask tolerances between and around signal pads can vary based on board fabrication site.

# EXAMPLE STENCIL DESIGN

DBZ0003A

SOT-23 - 1.12 mm max height

SMALL OUTLINE TRANSISTOR



SOLDER PASTE EXAMPLE  
BASED ON 0.125 THICK STENCIL  
SCALE:15X

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NOTES: (continued)

7. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
8. Board assembly site may have different recommendations for stencil design.

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