www.ti.com

SBOS610B - MARCH 2012 - REVISED MARCH 2012

HIGH-PRECISION, LOW-NOISE, RAIL-TO-RAIL OUTPUT, 11-MHZ JFET OPERATIONAL AMPLIFIER

Check for Samples: OPA140A-DIE

FEATURES

- Very Low Offset Drift
- Very Low Offset
- Low Input Bias Current
- Very Low 1/f Noise
- Low Noise
- Slew Rate
- Low Supply Current
- Input Voltage Range Includes V

 Supply
- Single-Supply Operation: 4.5 V to 36 V
 Dual-Supply Operation: ±2.25 V to ±18 V
- No Phase Reversal

APPLICATIONS

- Battery-Powered Instruments
- Industrial Controls
- Medical Instrumentation
- · Photodiode Amplifiers
- Active Filters
- Data Acquisition Systems
- Automatic Test Systems

DESCRIPTION

The OPA140A operational amplifier is a low-power JFET input amplifier that features good drift and low input bias current. The rail-to-rail output swing and input range that includes V– allow designers to take advantage of the low-noise characteristics of JFET amplifiers while also interfacing to modern, single-supply, precision analog-to-digital converters (ADCs) and digital-to-analog converters (DACs). The OPA140A runs on a single 4.5-V to 36-V supply or dual ±2.25-V to ±18-V supplies.

ORDERING INFORMATION(1)

PRODUCT	PACKAGE DESIGNATOR	PACKAGE ⁽²⁾	ORDERABLE PART NUMBER	PACKAGE QUANTITY	
ODA1404	TD	Dave Die In Weffle Deels	OPA140ATDD1	252	
OPA140A		Bare Die In Waffle Pack	OPA140ATDD2	10	

⁽¹⁾ For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI web site at www.ti.com.



⁽²⁾ Processing is per the Texas Instruments commercial production baseline and is in compliance with the Texas Instruments Quality Control System in effect at the time of manufacture. Electrical screening consists of DC parametric and functional testing at room temperature only. Unless otherwise specified by Texas Instruments AC performance and performance over temperature is not warranted. Visual Inspection is performed in accordance with MIL-STD-883 Test Method 2010 Condition B at 75X minimum.





This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

BARE DIE INFORMATION

DIE THICKNESS	BACKSIDE FINISH	BACKSIDE POTENTIAL	BOND PAD METALLIZATION COMPOSITION	BOND PAD THICKNESS	
15 mils.	Silicon with backgrind	Floating	TiW/AlCu (0.5%)	1100 nm	

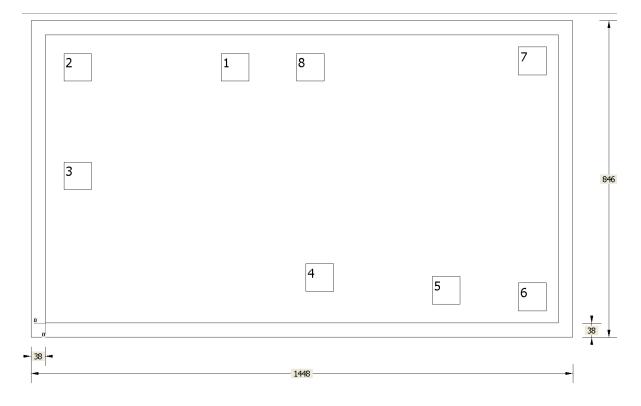


Table 1. Bond Pad Coordinates in Microns

DESCRIPTION	PAD NUMBER	X MIN	Y MIN	X MAX	Y MAX
VIN	1	469.600	-720.500	544.600	-645.500
N/C	2				
VIP	3	49.500	-430.500	124.500	-355.500
N/C	4				
V-	5	1034.050	-124.550	1109.050	-49.550
OUT	6	1264.500	-107.500	1339.500	-32.500
V+	7	1264.500	-737.500	1339.500	-662.500

2

Product Folder Link(s): OPA140A-DIE

www.ti.com 29-May-2025

PACKAGING INFORMATION

Orderable part number	Status	Material type	Package Pins	Package qty Carrier	RoHS	Lead finish/ Ball material	MSL rating/ Peak reflow	Op temp (°C)	Part marking (6)
						(4)	(5)		
OPA140ATDD1	Active	Production	null (null) 0	252 null	Yes	Call TI	N/A for Pkg Type	-	
OPA140ATDD1.B	Active	Production	null (null) 0	252 null	Yes	Call TI	N/A for Pkg Type	See OPA140ATDD1	
OPA140ATDD2	Active	Production	null (null) 0	10 null	Yes	Call TI	N/A for Pkg Type	-	
OPA140ATDD2.B	Active	Production	null (null) 0	10 null	Yes	Call TI	N/A for Pkg Type	See OPA140ATDD2	

⁽¹⁾ Status: For more details on status, see our product life cycle.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

⁽²⁾ Material type: When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

⁽³⁾ RoHS values: Yes, No, RoHS Exempt. See the TI RoHS Statement for additional information and value definition.

⁽⁴⁾ Lead finish/Ball material: Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

⁽⁵⁾ MSL rating/Peak reflow: The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

⁽⁶⁾ Part marking: There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2025. Texas Instruments Incorporated