

LMV2011 High Precision, Rail-to-Rail Output Operational Amplifier

Check for Samples: LMV2011

FEATURES

- (For Vs = 5V, Typical Unless Otherwise Noted)
- Low Ensured V_{os} Over Temperature 35μV
- Low Noise with no 1/f 35nV/√Hz
- High CMRR 130dB
- High PSRR 120dB
- High A_{VOL} 130dB
- Wide Gain-Bandwidth Product 3MHz
- High Slew Rate 4V/µs
- Low Supply Current 930μA
- Rail-to-Rail Output 30mV
- No External Capacitors Required

APPLICATIONS

- Precision Instrumentation Amplifiers
- Thermocouple Amplifiers
- Strain Gauge Bridge Amplifier

Connection Diagrams

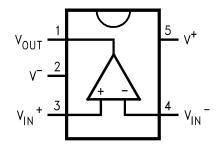


Figure 1. 5-Pin SOT-23 (Top View) See DBV Package

DESCRIPTION

The LMV2011 is a new precision amplifier that offers unprecedented accuracy and stability at an affordable price and is offered in a miniature (5-pin SOT-23) package and in an 8-lead SOIC package. This device utilizes patented techniques to measure and continually correct the input offset error voltage. The result is an amplifier which is ultra stable over time and temperature. It has excellent CMRR and PSRR ratings, and does not exhibit the familiar 1/f voltage and current noise increase that plagues traditional amplifiers. The combination of the LMV2011 characteristics makes it a good choice for transducer amplifiers, high gain configurations, ADC buffer amplifiers, DAC I-V conversion, and any other 2.7V-5V application requiring precision and long term stability.

Other useful benefits of the LMV2011 are rail-to-rail output, a low supply current of 930µA, and wide gain-bandwidth product of 3MHz. These extremely versatile features found in the LMV2011 provide high performance and ease of use.

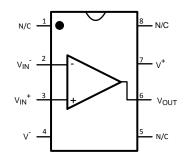


Figure 2. 8-Pin SOIC (Top View)
See D Package



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

M

Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



Absolute Maximum Ratings(1)(2)

	Human Body Model	2000V		
ESD Tolerance	Machine Model	200V		
	Supply Voltage	5.5V		
Common-Mode Input Voltage	$-0.3 \le V_{CM} \le V_{CC} + 0.3V$			
Differential Input Voltage	Differential Input Voltage			
Current At Input Pin		30mA		
Current At Output Pin		30mA		
Current At Power Supply Pin	Current At Power Supply Pin			
Junction Temperature (T _J)	150°C			
Lead Temperature (soldering 10 sec	+300°C			

⁽¹⁾ Absolute Maximum Ratings indicate limits beyond which damage may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but specific performance is not ensured. For ensured specifications and test conditions, see the Electrical Characteristics

Operating Ratings⁽¹⁾

Supply Voltage	2.7V to 5.25V
Storage Temperature Range	−65°C to 150°C
Operating Temperature Range	0°C to 70°C

⁽¹⁾ Absolute Maximum Ratings indicate limits beyond which damage may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but specific performance is not ensured. For ensured specifications and test conditions, see the Electrical Characteristics.

2.7V DC Electrical Characteristics

Unless otherwise specified, all limits ensured for T $_J$ = 25°C, V⁺ = 2.7V, V⁻= 0V, V $_{CM}$ = 1.35V, V $_O$ = 1.35V and R $_L$ > 1M Ω . **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Conditions	Min	Тур	Max	Units
Vos	Input Offset Voltage			0.8	25 35	μV
	Offset Calibration Time			0.5	10 12	ms
TCV _{OS}	Input Offset Voltage			0.015		μV/°C
	Long-Term Offset Drift			0.006		μV/month
	Lifetime V _{OS} Drift			2.5	5	μV
I _{IN}	Input Current			-3		pA
I _{OS}	Input Offset Current			6		pA
R _{IND}	Input Differential Resistance			9		МΩ
CMRR	Common Mode Rejection Ratio	$-0.3 \le V_{CM} \le 0.9V$ $0 \le V_{CM} \le 0.9V$		130	95 90	dB
PSRR	Power Supply Rejection Ratio	2.7V ≤ V ⁺ ≤ 5V		120	95 90	dB
A _{VOL}	On an Laur Valtage Cain	$R_L = 10k\Omega$		130	95 90	AD.
	Open Loop Voltage Gain	$R_L = 2k\Omega$		124	90 85	- dB
Vo		$R_L = 10k\Omega$ to 1.35V	2.665 2.655	2.68		
	Output Output	$V_{IN}(diff) = \pm 0.5V$		0.033	0.060 0.075	V
	Output Swing	$R_1 = 2k\Omega$ to 1.35V	2.630 2.615	2.65		.,,
		$V_{IN}(diff) = \pm 0.5V$		0.061	0.085 0.105	V

Product Folder Links: LMV2011

⁽²⁾ If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/ Distributors for availability and specifications.



2.7V DC Electrical Characteristics (continued)

Unless otherwise specified, all limits ensured for T $_J$ = 25°C, V⁺ = 2.7V, V⁻= 0V, V $_{CM}$ = 1.35V, V $_O$ = 1.35V and R $_L$ > 1M Ω . **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Conditions	Min	Тур	Max	Units
l _O Outp	Outrast Comment	Sourcing, $V_O = 0V$ $V_{IN}(diff) = \pm 0.5V$		12	5 3	^
	Output Current	Sinking, $V_O = 5V$ V _{IN} (diff) = ±0.5V		18	5 3	- mA
R _{OUT}	Output Impedance			0.05		Ω
I _S	Supply Current			0.919	1.20 1.50	mA

2.7V AC Electrical Characteristics

 $T_J = 25^{\circ}C$, $V^+ = 2.7V$, $V^- = 0V$, $V_{CM} = 1.35V$, $V_O = 1.35V$, and $R_L > 1M\Omega$. **Boldface** limits apply at the temperature extremes

Symbol	Parameter	Conditions		Min	Тур	Max	Units	
GBW	Gain-Bandwidth Product				3		MHz	
SR	Slew Rate				4		V/µs	
θ _m	Phase Margin				60		Deg	
G _m	Gain Margin				-14		dB	
e _n	Input-Referred Voltage Noise				35		nV/√ Hz	
in	Input-Referred Current Noise				150		fA/√Hz	
e _n p-p	Input-Referred Voltage Noise	$R_S = 100\Omega$, DC to 10Hz			850		nV _{pp}	
t _{rec}	Input Overload Recovery Time				50		ms	
t _s			1%		0.9			
	Output Settling Time	$A_V = -1$, $R_L = 2k\Omega$ 1V Step	0.1%		49		μs	
İ		1 v Otop	0.01%		100			

5V DC Electrical Characteristics

Unless otherwise specified, all limits ensured for T $_J$ = 25°C, V⁺ = 5V, V $_C$ = 0V, V $_C$ = 2.5V, V $_O$ = 2.5V and R $_L$ > 1M Ω . **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Conditions	Min	Тур	Max	Units
V _{OS}	Input Offset Voltage			0.12	25 35	μV
	Offset Calibration Time			0.5	10 12	ms
TCV _{OS}	Input Offset Voltage			0.015		μV/°C
	Long-Term Offset Drift			0.006		μV/month
	Lifetime V _{OS} Drift			2.5	5	μV
I _{IN}	Input Current			-3		pA
Ios	Input Offset Current			6		pA
R _{IND}	Input Differential Resistance			9		ΜΩ
CMRR	Common Mode Rejection Ratio	$-0.3 \le V_{CM} \le 3.2$ $0 \le V_{CM} \le 3.2$		130	100 90	dB
PSRR	Power Supply Rejection Ratio	2.7V ≤ V ⁺ ≤ 5V		120	95 90	dB
A _{VOL}	On and a ser Vallage Cain	$R_L = 10k\Omega$		130	105 100	40
	Open Loop Voltage Gain	$R_L = 2k\Omega$		132	95 90	- dB

Product Folder Links: LMV2011



5V DC Electrical Characteristics (continued)

Unless otherwise specified, all limits ensured for T $_J$ = 25°C, V⁺ = 5V, V⁻= 0V, V $_{CM}$ = 2.5V, V $_O$ = 2.5V and R $_L$ > 1M Ω . **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Conditions	Min	Тур	Max	Units
V _O Output Swing		$R_1 = 10k\Omega$ to 2.5V	4.96 4.95	4.978		
	Output Cuin a	$V_{IN}(diff) = \pm 0.5V$		0.040	0.070 0.085	V
	Output Swing	$R_1 = 2k\Omega$ to 2.5V	4.895 4.875	4.919		
		$V_{IN}^{-}(diff) = \pm 0.5V$		0.091	0.115 0.140	V
Output Current	Output Current	Sourcing, $V_0 = 0V$ $V_{IN}(diff) = \pm 0.5V$		15	8 6	A
	Output Current	Sinking, $V_O = 5V$ $V_{IN}(diff) = \pm 0.5V$		17	8 6	mA mA
R _{OUT}	Output Impedance			0.05		Ω
s	Supply Current per Channel			0.930	1.20 1.50	mA

5V AC Electrical Characteristics

 $T_J = 25^{\circ}C$, $V^+ = 5V$, $V^- = 0V$, $V_{CM} = 2.5V$, $V_O = 2.5V$, and $R_L > 1M\Omega$. **Boldface** limits apply at the temperature extremes.

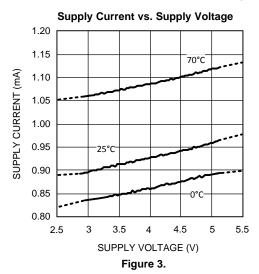
Symbol	Parameter	Conditions		Min	Тур	Max	Units
GBW	Gain-Bandwidth Product				3		MHz
SR	Slew Rate				4		V/µs
θ _m	Phase Margin				60		deg
G _m	Gain Margin				-15		dB
e _n	Input-Referred Voltage Noise				35		nV/√ Hz
i _n	Input-Referred Current Noise				150		fA/√Hz
e _n p-p	Input-Referred Voltage Noise	$R_S = 100\Omega$, DC to 10Hz			850		nV_{pp}
t _{rec}	Input Overload Recovery Time				50		ms
t _s			1%		0.8		
	Output Settling Time	$A_V = -1$, $R_L = 2k\Omega$ 1V Step	0.1%		36		us
		1. C.Op	0.01%		100		

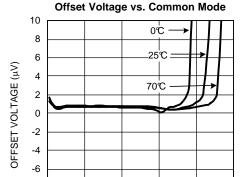
Product Folder Links: LMV2011



Typical Performance Characteristics

 T_A =25C, V_S = 5V unless otherwise specified.





-8

-0.2

0.3

-10

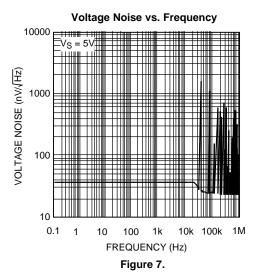
COMMON MODE VOLTAGE (V) Figure 5.

1.3

1.8

2.3

0.8



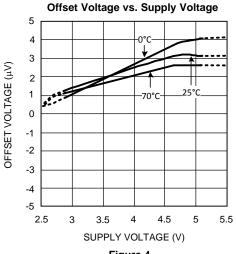
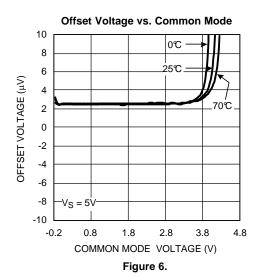


Figure 4.



Input Bias Current vs. Common Mode 500 $V_S = 5V$ 400 300 BIAS CURRENT (pA) 200 100 -100 -200 -300 -400 -500 0.0 0.5 1.0 1.5 2.0 2.5 3.0 3.5 4.0 4.5 5.0

V_{CM} (V) Figure 8.



 T_A =25C, V_S = 5V unless otherwise specified.

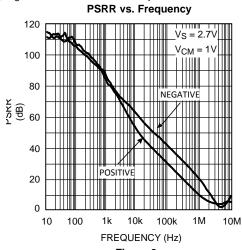
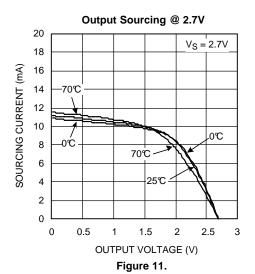
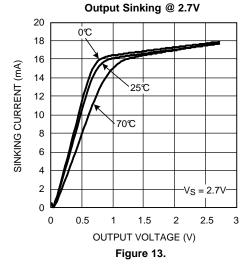


Figure 9.





PSRR vs. Frequency

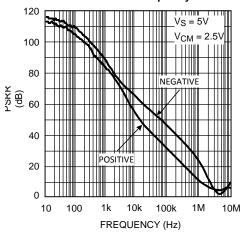


Figure 10.

Output Sourcing @ 5V

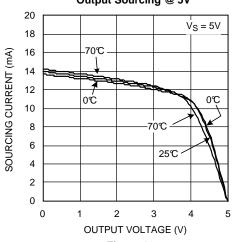


Figure 12.

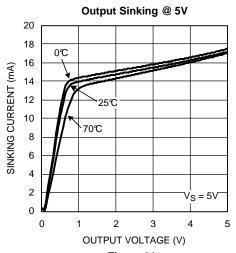


Figure 14.



 T_A =25C, V_S = 5V unless otherwise specified.

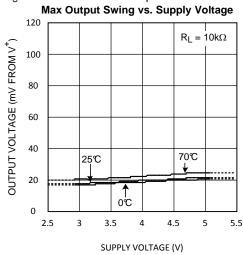


Figure 15.

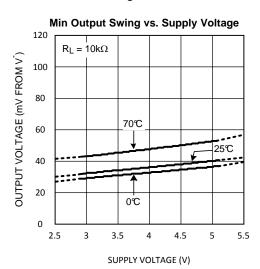
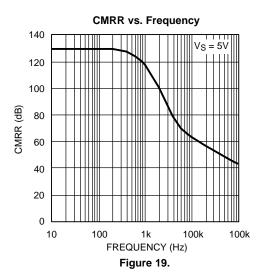


Figure 17.



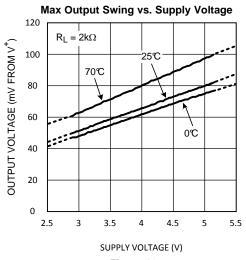


Figure 16.

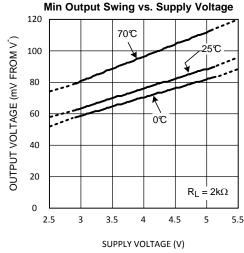


Figure 18.

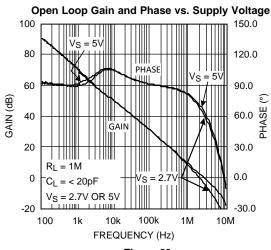
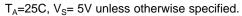
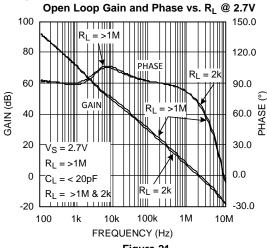


Figure 20.







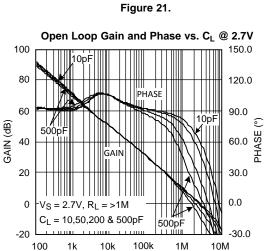
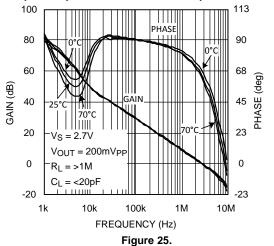


Figure 23.

FREQUENCY (Hz)

Open Loop Gain and Phase vs. Temperature @ 2.7V



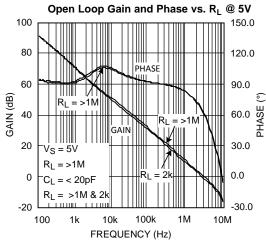
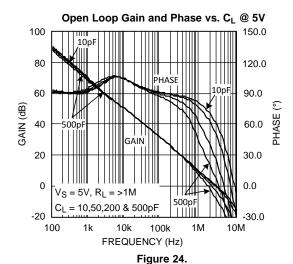
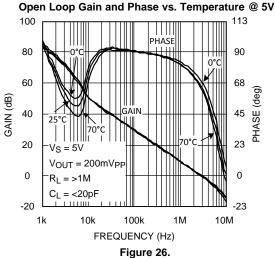


Figure 22.



-



rigure 20.



 T_A =25C, V_S = 5V unless otherwise specified.

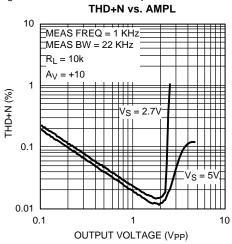


Figure 27.

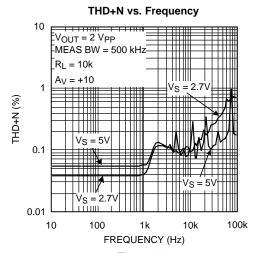
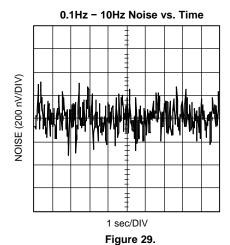


Figure 28.





APPLICATION INFORMATION

THE BENEFITS OF LMV2011 NO 1/f NOISE

Using patented methods, the LMV2011 eliminates the 1/f noise present in other amplifiers. That noise, which increases as frequency decreases, is a major source of measurement error in all DC-coupled measurements. Low-frequency noise appears as a constantly-changing signal in series with any measurement being made. As a result, even when the measurement is made rapidly, this constantly-changing noise signal will corrupt the result. The value of this noise signal can be surprisingly large. For example: If a conventional amplifier has a flat-band noise level of $10\text{nV}/\sqrt{\text{Hz}}$ and a noise corner of 10Hz, the RMS noise at 0.001Hz is $1\mu\text{V}/\sqrt{\text{Hz}}$. This is equivalent to a $0.50\mu\text{V}$ peak-to-peak error, in the frequency range 0.001 Hz to 1.0 Hz. In a circuit with a gain of 1000, this produces a 0.50mV peak-to-peak output error. This number of 0.001 Hz might appear unreasonably low, but when a data acquisition system is operating for 17 minutes, it has been on long enough to include this error. In this same time, the LMV2011 will only have a 0.21mV output error. This is smaller by 2.4 x. Keep in mind that this 1/f error gets even larger at lower frequencies. At the extreme, many people try to reduce this error by integrating or taking several samples of the same signal. This is also doomed to failure because the 1/f nature of this noise means that taking longer samples just moves the measurement into lower frequencies where the noise level is even higher.

The LMV2011 eliminates this source of error. The noise level is constant with frequency so that reducing the bandwidth reduces the errors caused by noise.

Another source of error that is rarely mentioned is the error voltage caused by the inadvertent thermocouples created when the common "Kovar type" IC package lead materials are soldered to a copper printed circuit board. These steel-based leadframe materials can produce over $35\mu\text{V/°C}$ when soldered onto a copper trace. This can result in thermocouple noise that is equal to the LMV2011 noise when there is a temperature difference of only 0.0014°C between the lead and the board!

For this reason, the lead-frame of the LMV2011 is made of copper. This results in equal and opposite junctions which cancel this effect. The extremely small size of the SOT-23 package results in the leads being very close together. This further reduces the probability of temperature differences and hence decreases thermal noise.

OVERLOAD RECOVERY

The LMV2011 recovers from input overload much faster than most chopper-stabilized opamps. Recovery from driving the amplifier to 2X the full scale output, only requires about 40ms. Many chopper-stabilized amplifiers will take from 250ms to several seconds to recover from this same overload. This is because large capacitors are used to store the unadjusted offset voltage.

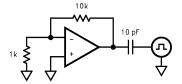


Figure 30. Overload Recovery Test

The wide bandwidth of the LMV2011 enhances performance when it is used as an amplifier to drive loads that inject transients back into the output. ADCs (Analog-to-Digital Converters) and multiplexers are examples of this type of load. To simulate this type of load, a pulse generator producing a 1V peak square wave was connected to the output through a 10pF capacitor (Figure 30). The typical time for the output to recover to 1% of the applied pulse is 80ns. To recover to 0.1% requires 860ns. This rapid recovery is due to the wide bandwidth of the output stage and large total GBW.

NO EXTERNAL CAPACITORS REQUIRED

The LMV2011 does not need external capacitors. This eliminates the problems caused by capacitor leakage and dielectric absorption, which can cause delays of several seconds from turn-on until the amplifier's error has settled.

Product Folder Links: LMV2011



MORE BENEFITS

The LMV2011 offers the benefits mentioned above and more. It has a rail-to-rail output and consumes only 950µA of supply current while providing excellent DC and AC electrical performance. In DC performance, the LMC2001 achieves 130dB of CMRR, 120dB of PSRR and 130dB of open loop gain. In AC performance, the LMV2011 provides 3MHz of gain-bandwidth product and 4V/µs of slew rate.

HOW THE LMV2011 WORKS

The LMV2011 uses new, patented techniques to achieve the high DC accuracy traditionally associated with chopper-stabilized amplifiers without the major drawbacks produced by chopping. The LMV2011 continuously monitors the input offset and corrects this error. The conventional chopping process produces many mixing products, both sums and differences, between the chopping frequency and the incoming signal frequency. This mixing causes large amounts of distortion, particularly when the signal frequency approaches the chopping frequency. Even without an incoming signal, the chopper harmonics mix with each other to produce even more trash. If this sounds unlikely or difficult to understand, look at the plot (Figure 31), of the output of a typical (MAX432) chopper-stabilized opamp. This is the output when there is no incoming signal, just the amplifier in a gain of -10 with the input grounded. The chopper is operating at about 150Hz; the rest is mixing products. Add an input signal and the noise gets much worse. Compare this plot with Figure 32 of the LMV2011. This data was taken under the exact same conditions. The auto-zero action is visible at about 30kHz but note the absence of mixing products at other frequencies. As a result, the LMV2011 has very low distortion of 0.02% and very low mixing products.

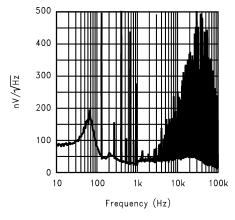


Figure 31. The Output of a Chopper Stabilized Op Amp (MAX432)

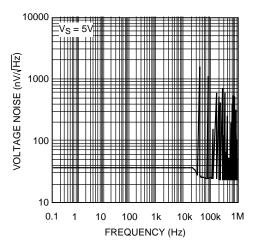


Figure 32. The Output of the LMV2011

Copyright © 2003–2013, Texas Instruments Incorporated



INPUT CURRENTS

The LMV2011's input currents are different than standard bipolar or CMOS input currents in that it appears as a current flowing in one input and out the other. Under most operating conditions, these currents are in the picoamp level and will have little or no effect in most circuits. These currents tend to increase slightly when the common-mode voltage is near the minus supply (See the Typical Performance Characteristics). At high temperatures such as 85°C, the input currents become larger, 0.5nA typical, and are both positive except when the V_{CM} is near V^- . If operation is expected at low common-mode voltages and high temperature, do not add resistance in series with the inputs to balance the impedances. Doing this can cause an increase in offset voltage. A small resistance such as $1k\Omega$ can provide some protection against very large transients or overloads, and will not increase the offset significantly.

PRECISION STRAIN-GAUGE AMPLIFIER

This Strain-Gauge amplifier (Figure 32) provides high gain (1006 or ~60 dB) with very low offset and drift. Using the resistors' tolerances as shown, the worst case CMRR will be greater than 108 dB. The CMRR is directly related to the resistor mismatch. The rejection of common-mode error, at the output, is independent of the differential gain, which is set by R3. The CMRR is further improved, if the resistor ratio matching is improved, by specifying tighter-tolerance resistors, or by trimming.

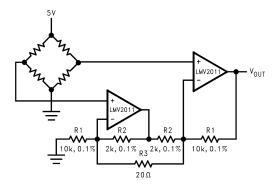


Figure 33. Precision Strain Gauge Amplifier

Extending Supply Voltages and Output Swing by Using a Composite Amplifier Configuration:

In cases where substantially higher output swing is required with higher supply voltages, arrangements like the ones shown in Figure 34 and Figure 35 could be used. These configurations utilize the excellent DC performance of the LMV2011 while at the same time allow the superior voltage and frequency capabilities of the LM6171 to set the dynamic performance of the overall amplifier. For example, it is possible to achieve $\pm 12V$ output swing with 300MHz of overall GBW ($A_V = 100$) while keeping the worst case output shift due to V_{OS} less than 4mV. The LMV2011 output voltage is kept at about mid-point of its overall supply voltage, and its input common mode voltage range allows the V- terminal to be grounded in one case (Figure 34, inverting operation) and tied to a small non-critical negative bias in another (Figure 35, non-inverting operation). Higher closed-loop gains are also possible with a corresponding reduction in realizable bandwidth. Table 1 shows some other closed loop gain possibilities along with the measured performance in each case.

Product Folder Links: LMV2011

Copyright © 2003-2013, Texas Instruments Incorporated



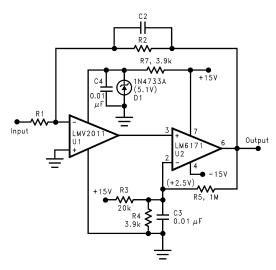


Figure 34. Composite Amplifier Configuration

Table 1. Composite Amplifier Measured Performance

A _V	R1 (Ω)	R2 (Ω)	C2 (pF)	BW (MHz)	SR (V/µs)	en p-p (mV _{PP})
50	200	10k	8	3.3	178	37
100	100	10k	10	2.5	174	70
100	1k	100k	0.67	3.1	170	70
500	200	100k	1.75	1.4	96	250
1000	100	100k	2.2	0.98	64	400

In terms of the measured output peak-to-peak noise, the following relationship holds between output noise voltage, e_n p-p, for different closed-loop gain, A_V , settings, where -3dB Bandwidth is BW:

$$\frac{e_{npp1}}{e_{npp2}} = \sqrt{\frac{BW1}{BW2}} \bullet \frac{A_V 1}{A_V 2}$$
 (1)

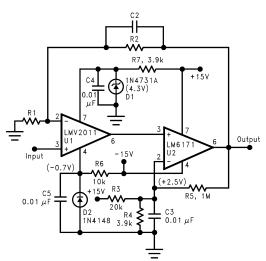


Figure 35. Composite Amplifier Configuration

It should be kept in mind that in order to minimize the output noise voltage for a given closed-loop gain setting, one could minimize the overall bandwidth. As can be seen from Equation 1 above, the output noise has a square-root relationship to the Bandwidth.

Product Folder Links: LMV2011



In the case of the inverting configuration, it is also possible to increase the input impedance of the overall amplifier, by raising the value of R1, without having to increase the feed-back resistor, R2, to impractical values, by utilizing a "Tee" network as feedback. See the LMC6442 data sheet (Application Notes section) for more details on this.

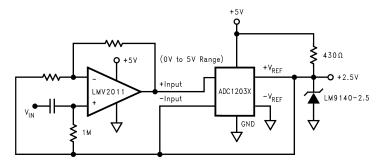


Figure 36. AC Coupled ADC Driver

LMV2011 AS ADC INPUT AMPLIFIER

The LMV2011 is a great choice for an amplifier stage immediately before the input of an ADC (Analog-to-Digital Converter), whether AC or DC coupled. See Figure 36 and Figure 37. This is because of the following important characteristics:

- A) Very low offset voltage and offset voltage drift over time and temperature allow a high closed-loop gain setting without introducing any short-term or long-term errors. For example, when set to a closed-loop gain of 100 as the analog input amplifier for a 12-bit A/D converter, the overall conversion error over full operation temperature and 30 years life of the part (operating at 50°C) would be less than 5 LSBs.
- **B)** Fast large-signal settling time to 0.01% of final value (1.4μs) allows 12 bit accuracy at 100KH_Z or more sampling rate.
- C) No flicker (1/f) noise means unsurpassed data accuracy over any measurement period of time, no matter how long. Consider the following opamp performance, based on a typical low-noise, high-performance commercially-available device, for comparison:

Opamp flatband noise = $8nV/\sqrt{Hz}$

1/f corner frequency = 100Hz

 $A_{V} = 2000$

Measurement time = 100 sec

Bandwidth = 2Hz

This example will result in about 2.2 mV_{PP} (1.9 LSB) of output noise contribution due to the opamp alone, compared to about $594\mu V_{PP}$ (less than 0.5 LSB) when that opamp is replaced with the LMV2011 which has no 1/f contribution. If the measurement time is increased from 100 seconds to 1 hour, the improvement realized by using the LMV2011 would be a factor of about 4.8 times (2.86mV_{PP} compared to $596\mu V$ when LMV2011 is used) mainly because the LMV2011 accuracy is not compromised by increasing the observation time.

- D) Copper leadframe construction minimizes any thermocouple effects which would degrade low level/high gain data conversion application accuracy (see THE BENEFITS OF LMV2011 NO 1/f NOISE).
- E) Rail-to-Rail output swing maximizes the ADC dynamic range in 5-Volt single-supply converter applications. Below are some typical block diagrams showing the LMV2011 used as an ADC amplifier (Figure 36 and Figure 37).

Product Folder Links: LMV2011



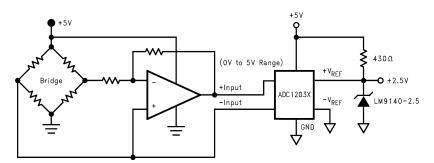


Figure 37. DC Coupled ADC Driver

SNOSA32C - AUGUST 2003 - REVISED MARCH 2013



REVISION HISTORY

Cł	nanges from Revision B (March 2013) to Revision C	Pa	ıge
•	Changed layout of National Data Sheet to TI format		15

Product Folder Links: LMV2011

www.ti.com 11-Nov-2025

PACKAGING INFORMATION

Orderable part number	Status (1)	Material type	Package Pins	Package qty Carrier	RoHS (3)	Lead finish/ Ball material	MSL rating/ Peak reflow	Op temp (°C)	Part marking (6)
LMV2011MA/NOPB	Active	Production	SOIC (D) 8	95 TUBE	Yes	SN	Level-1-260C-UNLIM	0 to 70	LMV20 11MA
LMV2011MA/NOPB.A	Active	Production	SOIC (D) 8	95 TUBE	Yes	SN	Level-1-260C-UNLIM	0 to 70	LMV20 11MA
LMV2011MAX/NOPB	Active	Production	SOIC (D) 8	2500 LARGE T&R	Yes	SN	Level-1-260C-UNLIM	0 to 70	LMV20 11MA
LMV2011MAX/NOPB.A	Active	Production	SOIC (D) 8	2500 LARGE T&R	Yes	SN	Level-1-260C-UNLIM	0 to 70	LMV20 11MA
LMV2011MF/NOPB	Active	Production	SOT-23 (DBV) 5	1000 SMALL T&R	Yes	SN	Level-1-260C-UNLIM	0 to 70	A84A
LMV2011MF/NOPB.A	Active	Production	SOT-23 (DBV) 5	1000 SMALL T&R	Yes	SN	Level-1-260C-UNLIM	0 to 70	A84A
LMV2011MFX/NOPB	Active	Production	SOT-23 (DBV) 5	3000 LARGE T&R	Yes	SN	Level-1-260C-UNLIM	0 to 70	A84A
LMV2011MFX/NOPB.A	Active	Production	SOT-23 (DBV) 5	3000 LARGE T&R	Yes	SN	Level-1-260C-UNLIM	0 to 70	A84A

⁽¹⁾ Status: For more details on status, see our product life cycle.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

⁽²⁾ Material type: When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

⁽³⁾ RoHS values: Yes, No, RoHS Exempt. See the TI RoHS Statement for additional information and value definition.

⁽⁴⁾ Lead finish/Ball material: Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

⁽⁵⁾ MSL rating/Peak reflow: The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

⁽⁶⁾ Part marking: There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.



PACKAGE OPTION ADDENDUM

www.ti.com 11-Nov-2025

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

PACKAGE MATERIALS INFORMATION

www.ti.com 5-Sep-2025

TAPE AND REEL INFORMATION





A0	Dimension designed to accommodate the component width
В0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LMV2011MAX/NOPB	SOIC	D	8	2500	330.0	12.4	6.5	5.4	2.0	8.0	12.0	Q1
LMV2011MF/NOPB	SOT-23	DBV	5	1000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3
LMV2011MFX/NOPB	SOT-23	DBV	5	3000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3

PACKAGE MATERIALS INFORMATION

www.ti.com 5-Sep-2025



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
LMV2011MAX/NOPB	SOIC	D	8	2500	367.0	367.0	35.0
LMV2011MF/NOPB	SOT-23	DBV	5	1000	208.0	191.0	35.0
LMV2011MFX/NOPB	SOT-23	DBV	5	3000	208.0	191.0	35.0

PACKAGE MATERIALS INFORMATION

www.ti.com 5-Sep-2025

TUBE



*All dimensions are nominal

Device	Package Name	Package Type	Pins	SPQ	L (mm)	W (mm)	T (µm)	B (mm)
LMV2011MA/NOPB	D	SOIC	8	95	495	8	4064	3.05
LMV2011MA/NOPB.A	D	SOIC	8	95	495	8	4064	3.05



SMALL OUTLINE TRANSISTOR



NOTES:

- 1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
 2. This drawing is subject to change without notice.
 3. Reference JEDEC MO-178.

- 4. Body dimensions do not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.25 mm per side.
- 5. Support pin may differ or may not be present.



SMALL OUTLINE TRANSISTOR



NOTES: (continued)

6. Publication IPC-7351 may have alternate designs.

7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.



SMALL OUTLINE TRANSISTOR



NOTES: (continued)

- 8. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
- 9. Board assembly site may have different recommendations for stencil design.





SMALL OUTLINE INTEGRATED CIRCUIT



NOTES:

- 1. Linear dimensions are in inches [millimeters]. Dimensions in parenthesis are for reference only. Controlling dimensions are in inches. Dimensioning and tolerancing per ASME Y14.5M.
- 2. This drawing is subject to change without notice.
- 3. This dimension does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 [0.15] per side.
- 4. This dimension does not include interlead flash.
- 5. Reference JEDEC registration MS-012, variation AA.



SMALL OUTLINE INTEGRATED CIRCUIT



NOTES: (continued)

6. Publication IPC-7351 may have alternate designs.

7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.



SMALL OUTLINE INTEGRATED CIRCUIT



NOTES: (continued)

- 8. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
- 9. Board assembly site may have different recommendations for stencil design.



IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you fully indemnify TI and its representatives against any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale, TI's General Quality Guidelines, or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products. Unless TI explicitly designates a product as custom or customer-specified, TI products are standard, catalog, general purpose devices.

TI objects to and rejects any additional or different terms you may propose.

Copyright © 2025, Texas Instruments Incorporated

Last updated 10/2025