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Single-Supply, Rail-to-Rail Output, CMOS INSTRUMENTATION AMPLIFIER

FEATURES

- RAIL-TO-RAIL OUTPUT SWING: Within 10mV
- LOW OFFSET VOLTAGE: $\pm 200\mu\text{V}$
- LOW OFFSET DRIFT: $\pm 5\mu\text{V}/^\circ\text{C}$
- INTERNAL FIXED GAIN = 10V/V OR 50V/V
- SPECIFIED TEMPERATURE RANGE:
-55°C to +125°C
- LOW INPUT BIAS CURRENT: 0.2pA
- WIDE BANDWIDTH: 550kHz in G = 10
- HIGH SLEW RATE: 6.5V/ μs
- LOW COST
- SO-8 AND TINY MSOP-8 PACKAGES

APPLICATIONS

- **INDUSTRIAL SENSOR AMPLIFIERS**
Bridge, RTD, Thermocouple, Flow, Position
- **MEDICAL EQUIPMENT**
ECG, EEG, EMG Amplifiers
- **DRIVING A/D CONVERTERS**
- **PCMCIA CARDS**
- **AUDIO PROCESSING**
- **COMMUNICATIONS**
- **TEST EQUIPMENT**
- **LOW COST AUTOMOTIVE INSTRUMENTATION**

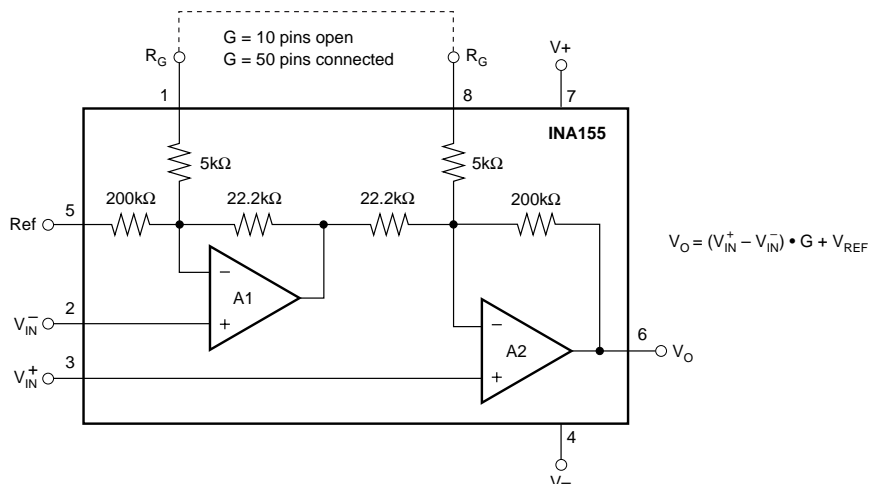
DESCRIPTION

The INA155 is a low-cost CMOS instrumentation amplifier with rail-to-rail output swing optimized for low voltage, single-supply operation.

Wide bandwidth (550kHz in G = 10) and high slew rate (6.5V/ μ s) make the INA155 suitable for driving sampling A/D converters as well as general purpose and audio applications. Fast settling time allows use with higher speed sensors and transducers and rapid scanning data acquisition systems.

Gain can be set to 10V/V or 50V/V by pin strapping. Gains between these two values can be obtained with the addition of a single resistor. The INA155 is fully specified over the supply range of +2.7 to +5.5V.

The INA155 is available in MSOP-8 and SO-8 surface-mount packages. Both are specified for operation over the temperature range -55°C to 125°C .



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SPECIFICATIONS: $V_S = +2.7V$ to $+5.5V$

Boldface limits apply over the specified temperature range, $T_A = -40^{\circ}C$ to $+85^{\circ}C$

At $T_A = +25^{\circ}C$, $R_L = 10k\Omega$ connected to $V_S/2$. R_G pins open ($G = 10$), and $Ref = V_S/2$, unless otherwise noted.

PARAMETER	CONDITION	INA155E, U			INA155EA, UA			UNITS
		MIN	TYP	MAX	MIN	TYP	MAX	
INPUT								
Offset Voltage, RTI V_{OS}	$V_S = +5.0V$, $V_{CM} = V_S/2$		± 0.2	± 1		*	*	mV
Over Temperature				± 1.5		*	*	mV
Drift dV_{OS}/dT			± 5			*	*	$\mu V/^{\circ}C$
vs Power Supply	$V_S = +2.7V$ to $+6V$, $V_{CM} = 0.2 \cdot V_S$		± 50	± 200		*	*	$\mu V/V$
Over Temperature				± 250		*	*	$\mu V/V$
vs Time			± 0.4			*	*	$\mu V/mo$
INPUT VOLTAGE RANGE								
Safe Input Voltage		$(V-) - 0.5$		$(V+) + 0.5$	*		*	V
Common-Mode Range ⁽¹⁾ V_{CM}	$V_S = 5.5V$	0.3		$5.2^{(2)}$	*		*	V
	$V_S = 2.7V$	0.2		$2.5^{(2)}$	*		*	V
Common-Mode Rejection Ratio $CMRR$	$V_S = 5.5V$, $0.6V < V_{CM} < 3.7V$, $G = 10$	92	100		80	*		dB
Over Temperature		85			79			dB
	$V_S = 5.5V$, $0.6V < V_{CM} < 3.7V$, $G = 50$	86	90		77	*		dB
Over Temperature		85			76			dB
INPUT IMPEDANCE								
Differential			$10^{13} \parallel 3$			*		$\Omega \parallel pF$
Common-Mode			$10^{13} \parallel 3$			*		$\Omega \parallel pF$
INPUT BIAS CURRENT								
Input Bias Current I_B			± 1	± 10		*	*	pA
Offset Current I_{OS}			± 1	± 10		*	*	pA
NOISE, RTI	$R_S = 0\Omega$, $G = 10$ or 50							
Voltage Noise: $f = 0.1Hz$ to $10Hz$			4.5			*		$\mu V/Vp-p$
Voltage Noise Density: $f = 10Hz$			260			*		nV/\sqrt{Hz}
$f = 100Hz$			99			*		nV/\sqrt{Hz}
$f = 1kHz$			40			*		nV/\sqrt{Hz}
Current Noise: $f = 1kHz$			2			*		fA/\sqrt{Hz}
GAIN								
Gain Equation		10		50	*		*	V/V
Gain Error ⁽³⁾	$V_S = 5.5V$, $V_O = 0.01V$ to $5.49V$, $G = 10$	$G = 10 + 400k\Omega/(10k\Omega + R_G)$				*	*	V/V
vs Temperature			± 0.02	± 0.1		*	*	%
	$V_S = 5.5V$, $V_O = 0.05V$ to $5.45V$, $G = 50$		± 2	± 10		*	*	ppm/ $^{\circ}C$
vs Temperature			± 0.05	± 0.25		*	*	%
Nonlinearity	$V_S = 5.5V$, $G = 10$ or 50		± 15	± 30		*	*	ppm/ $^{\circ}C$
Over Temperature			± 0.005	± 0.015		*	*	% of FSR
				± 0.015		*	*	% of FSR
OUTPUT								
Voltage Output Swing from Rail	$R_L = 10k\Omega$, $G_{ERR} < 0.1\%$		5	10		*	*	mV
Over Temperature				10		*	*	mV
Short-Circuit Current	Short Circuit to Ground		± 50			*	*	mA
Capacitance Load (stable operation)			See Typical Curve			*	*	
FREQUENCY RESPONSE								
Bandwidth, $-3dB$ BW	$G = 10$		550			*		kHz
	$G = 50$		110			*		kHz
Slew Rate SR	$V_S = 5.5V$, $C_L = 100pF$		6.5			*		V/ μs
Settling Time: 0.1% t_S	$V_S = 5.5V$, $V_O = 2V$ Step, $C_L = 100pF$, $G = 10$		5			*		μs
	$V_S = 5.5V$, $V_O = 2V$ Step, $C_L = 100pF$, $G = 50$		11			*		μs
	$V_S = 5.5V$, $V_O = 2V$ Step, $C_L = 100pF$, $G = 10$		8			*		μs
	$V_S = 5.5V$, $V_O = 2V$ Step, $C_L = 100pF$, $G = 50$		15			*		μs
Overload Recovery	50% Input Overload		0.2			*		μs
Total Harmonic Distortion + Noise $THD+N$			See Typical Curve			*		μs
POWER SUPPLY								
Specified Voltage Range		+2.7		+5.5	*		*	V
Operating Voltage Range			+2.5 to +6			*	*	V
Quiescent Current	$V_{IN} = 0$, $I_O = 0$		1.7	2.1		*	*	mA
Over Temperature	$V_{IN} = 0$, $I_O = 0$			2.6		*	*	mA
TEMPERATURE RANGE								
Specified Range		-40		+85	*		*	$^{\circ}C$
Operating Range		-65		+150	*		*	$^{\circ}C$
Storage Range		-65		+150	*		*	$^{\circ}C$
Thermal Resistance θ_{JA}								
MSOP-8 Surface Mount			150			*		$^{\circ}C/W$
SO-8 Surface Mount			150			*		$^{\circ}C/W$

* Same as INA155E, U.

NOTES: (1) For further information, refer to typical performance curves on common-mode input range. (2) Operation above $(V+) - 1.8V$ (max) results in reduced common-mode rejection. See discussion and Figure 6 in the text of this data sheet. (3) Does not include error and TCR of additional optional gain-setting resistor in series with R_G , if used.

SPECIFICATIONS: $V_S = +2.7V$ to $+5.5V$

Boldface limits apply over the specified temperature range, $T_A = -55^{\circ}C$ to $+125^{\circ}C$

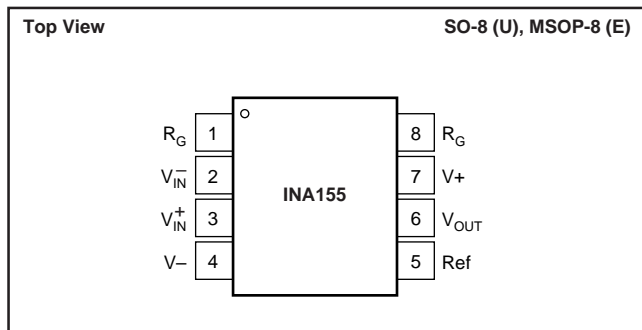
At $T_A = +25^{\circ}C$, $R_L = 10k\Omega$ connected to $V_S/2$. R_G pins open ($G = 10$), and $Ref = V_S/2$, unless otherwise noted.

PARAMETER	CONDITION	INA155E, U			INA155EA, UA			UNITS
		MIN	TYP	MAX	MIN	TYP	MAX	
INPUT								
Offset Voltage, RTI V_{OS}	$V_S = +5.0V$, $V_{CM} = V_S/2$		± 0.2	± 1		*	*	mV
Over Temperature				± 2		*	*	mV
Drift dV_{OS}/dT			± 5			*	*	$\mu V/^{\circ}C$
vs Power Supply PSRR	$V_S = +2.7V$ to $+6V$, $V_{CM} = 0.2 \cdot V_S$		± 50	± 200		*	*	$\mu V/V$
Over Temperature				± 250		*	*	$\mu V/V$
vs Time			± 0.4			*		$\mu V/mo$
INPUT VOLTAGE RANGE								
Safe Input Voltage		$(V-) - 0.5$		$(V+) + 0.5$	*		*	V
Common-Mode Range ⁽¹⁾ V_{CM}	$V_S = 5.5V$	0.3		5.2 ⁽²⁾	*		*	V
	$V_S = 2.7V$	0.2		2.5 ⁽²⁾	*		*	V
Common-Mode Rejection Ratio $CMRR$	$V_S = 5.5V$, $0.6V < V_{CM} < 3.7V$, $G = 10$	92	100		80	*		dB
Over Temperature		82			78			dB
	$V_S = 5.5V$, $0.6V < V_{CM} < 3.7V$, $G = 50$	86	90		77	*		dB
Over Temperature		84			76			dB
INPUT IMPEDANCE								
Differential			$10^{13} \parallel 3$			*		$\Omega \parallel pF$
Common-Mode			$10^{13} \parallel 3$			*		$\Omega \parallel pF$
INPUT BIAS CURRENT								
Input Bias Current I_B			± 1	± 10		*	*	pA
Offset Current I_{OS}			± 1	± 10		*	*	pA
NOISE, RTI	$R_S = 0\Omega$, $G = 10$ or 50							
Voltage Noise: $f = 0.1Hz$ to $10Hz$			4.5			*		$\mu V/Vp-p$
Voltage Noise Density: $f = 10Hz$			260			*		nV/\sqrt{Hz}
$f = 100Hz$			99			*		nV/\sqrt{Hz}
$f = 1kHz$			40			*		nV/\sqrt{Hz}
Current Noise: $f = 1kHz$			2			*		fA/\sqrt{Hz}
GAIN								
Gain Equation		10		50	*		*	V/V
Gain Error ⁽³⁾	$V_S = 5.5V$, $V_O = 0.01V$ to $5.49V$, $G = 10$	$G = 10 + 400k\Omega/(10k\Omega + R_G)$				*	*	V/V
vs Temperature			± 0.02	± 0.1		*	*	%
	$V_S = 5.5V$, $V_O = 0.05V$ to $5.45V$, $G = 50$		± 2	± 10		*	*	ppm/ $^{\circ}C$
vs Temperature			± 0.05	± 0.25		*	*	%
Nonlinearity	$V_S = 5.5V$, $G = 10$ or 50		± 15	± 30		*	*	ppm/ $^{\circ}C$
Over Temperature			± 0.005	± 0.015		*	*	% of FSR
				± 0.015		*	*	% of FSR
OUTPUT								
Voltage Output Swing from Rail	$R_L = 10k\Omega$, $G_{ERR} < 0.1\%$		5	10		*	*	mV
Over Temperature				10		*	*	mV
Short-Circuit Current	Short Circuit to Ground		± 50			*	*	mA
Capacitance Load (stable operation)			See Typical Curve			*		
FREQUENCY RESPONSE								
Bandwidth, $-3dB$ BW	$G = 10$		550			*		kHz
	$G = 50$		110			*		kHz
Slew Rate SR	$V_S = 5.5V$, $C_L = 100pF$		6.5			*		V/ μs
Settling Time: 0.1% t_S	$V_S = 5.5V$, $V_O = 2V$ Step, $C_L = 100pF$, $G = 10$		5			*		μs
	$V_S = 5.5V$, $V_O = 2V$ Step, $C_L = 100pF$, $G = 50$		11			*		μs
	$V_S = 5.5V$, $V_O = 2V$ Step, $C_L = 100pF$, $G = 10$		8			*		μs
	$V_S = 5.5V$, $V_O = 2V$ Step, $C_L = 100pF$, $G = 50$		15			*		μs
Overload Recovery	50% Input Overload		0.2			*		μs
Total Harmonic Distortion + Noise $THD+N$			See Typical Curve			*		
POWER SUPPLY								
Specified Voltage Range		+2.7		+5.5	*		*	V
Operating Voltage Range			+2.5 to +6			*	*	V
Quiescent Current	$V_{IN} = 0$, $I_O = 0$		1.7	2.1		*	*	mA
Over Temperature	$V_{IN} = 0$, $I_O = 0$			2.8		*	*	mA
TEMPERATURE RANGE								
Specified Range		-55		+125	*		*	$^{\circ}C$
Operating Range		-65		+150	*		*	$^{\circ}C$
Storage Range		-65		+150	*		*	$^{\circ}C$
Thermal Resistance θ_{JA}								
MSOP-8 Surface Mount			150			*		$^{\circ}C/W$
SO-8 Surface Mount			150			*		$^{\circ}C/W$

* Same as INA155E, U.

NOTES: (1) For further information, refer to typical performance curves on common-mode input range. (2) Operation above $(V+) - 1.8V$ (max) results in reduced common-mode rejection. See discussion and Figure 6 in the text of this data sheet. (3) Does not include error and TCR of additional optional gain-setting resistor in series with R_G , if used.

PIN CONFIGURATION



ELECTROSTATIC DISCHARGE SENSITIVITY

This integrated circuit can be damaged by ESD. Burr-Brown recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

ABSOLUTE MAXIMUM RATINGS⁽¹⁾

Supply Voltage, V+ to V-	7.5V
Signal Input Terminals, Voltage ⁽²⁾	(V-) - 0.5V to (V+) + 0.5V
Current ⁽²⁾	10mA
Output Short-Circuit ⁽³⁾	Continuous
Operating Temperature	-65°C to +150°C
Storage Temperature	-65°C to +150°C
Junction Temperature	+150°C
Lead Temperature (soldering, 10s)	+300°C

NOTE: (1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those specified is not implied. (2) Input terminals are diode-clamped to the power supply rails. Input signals that can swing more than 0.5V beyond the supply rails should be current limited to 10mA or less. (3) Short circuit to ground.

PACKAGE/ORDERING INFORMATION

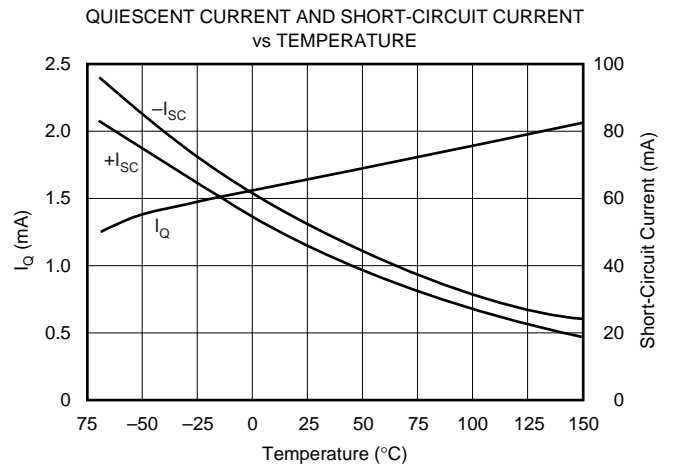
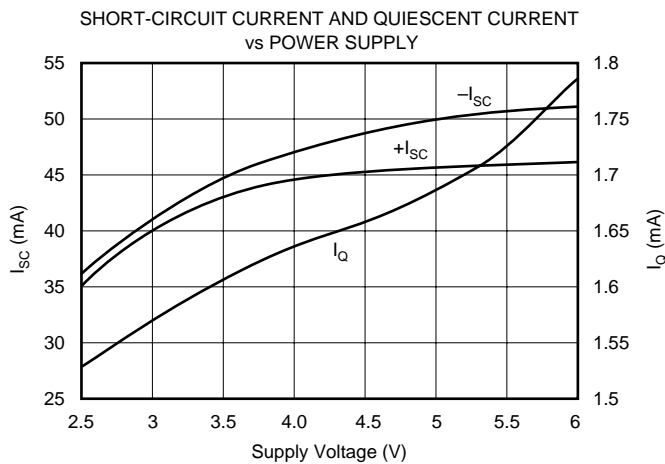
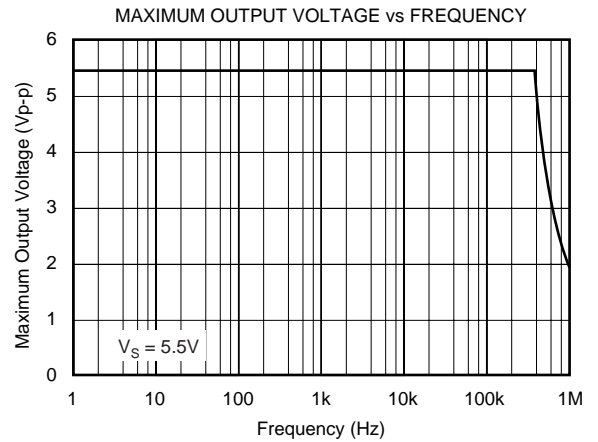
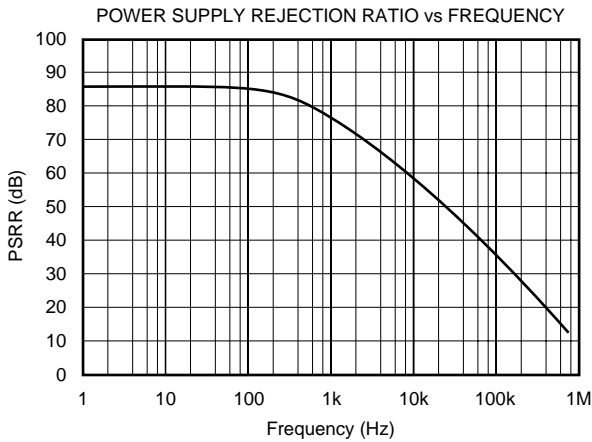
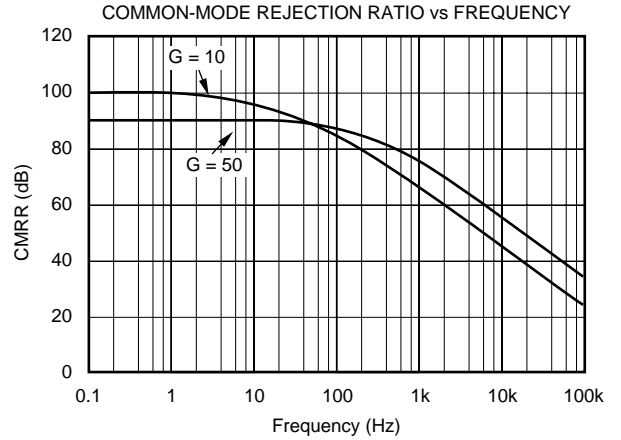
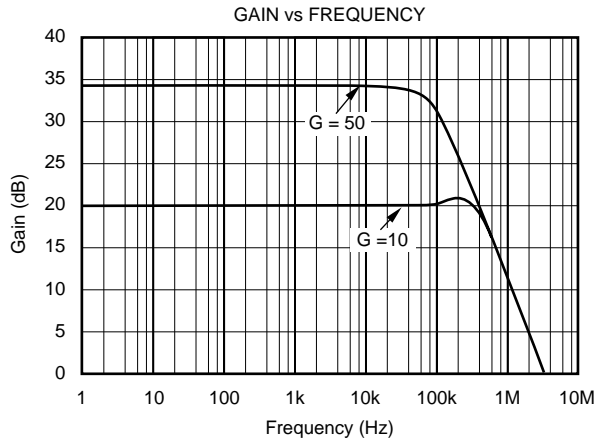
PRODUCT	PACKAGE	PACKAGE DRAWING NUMBER	SPECIFIED TEMPERATURE RANGE	PACKAGE MARKING	ORDERING NUMBER ⁽¹⁾	TRANSPORT MEDIA
INA155U	SO-8	182	-55°C to +125°C	INA155U	INA155U	Rails
"	"	"	"	"	INA155U/2K5	Tape and Reel
INA155UA	SO-8	182	-55°C to +125°C	INA155UA	INA155UA	Rails
"	"	"	"	"	INA155UA/2K5	Tape and Reel
INA155E	MSOP-8	337	-55°C to +125°C	A55	INA155E/250	Tape and Reel
"	"	"	"	"	INA155E/2K5	Tape and Reel
INA155EA	MSOP-8	337	-55°C to +125°C	A55	INA155EA/250	Tape and Reel
"	"	"	"	"	INA155EA/2K5	Tape and Reel

NOTES: (1) Models with a slash (/) are available only in Tape and Reel in the quantities indicated (e.g., /2K5 indicates 2500 devices per reel). Ordering 2500 pieces of "INA155UA/2K5" will get a single 2500-piece Tape and Reel.

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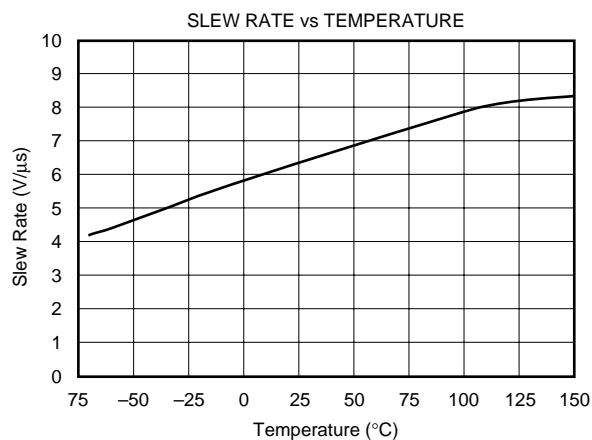
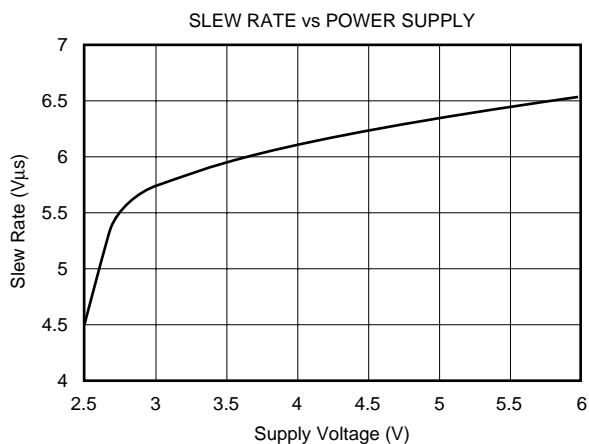
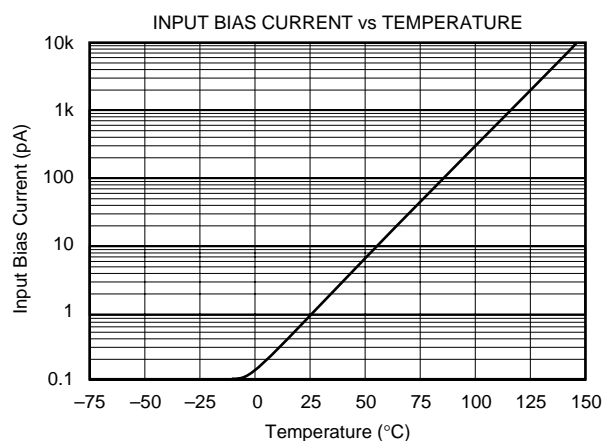
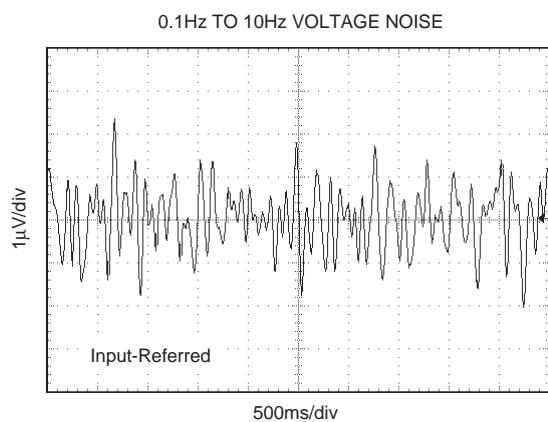
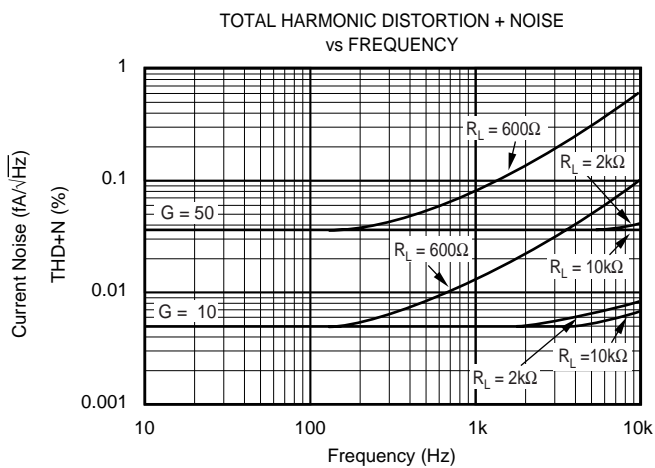
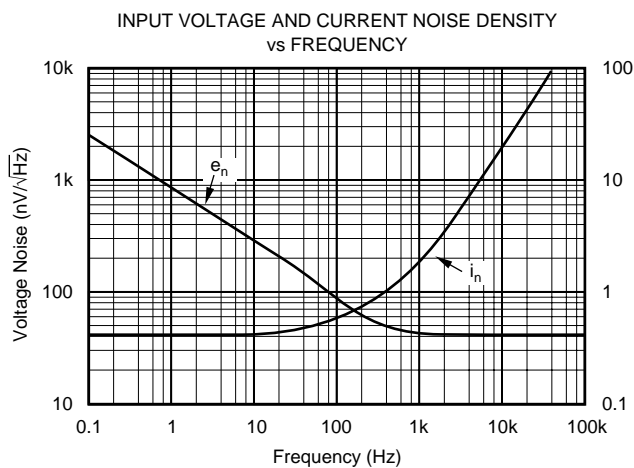
TYPICAL PERFORMANCE CURVES

At $T_A = +25^\circ\text{C}$, $V_S = 5.5\text{V}$, $R_L = 10\text{k}\Omega$ connected to $V_S/2$. R_G pins open ($G = 10$), and $\text{Ref} = V_S/2$, unless otherwise noted.



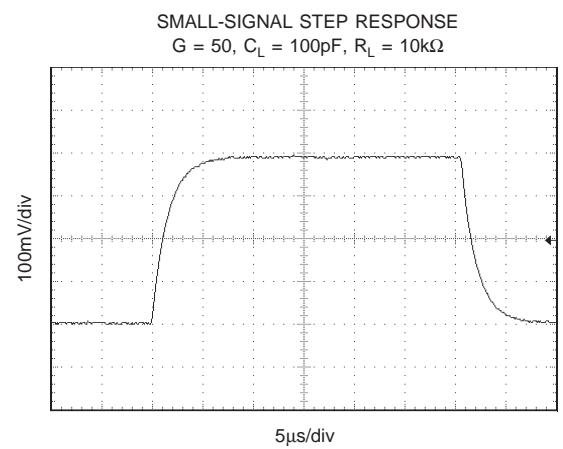
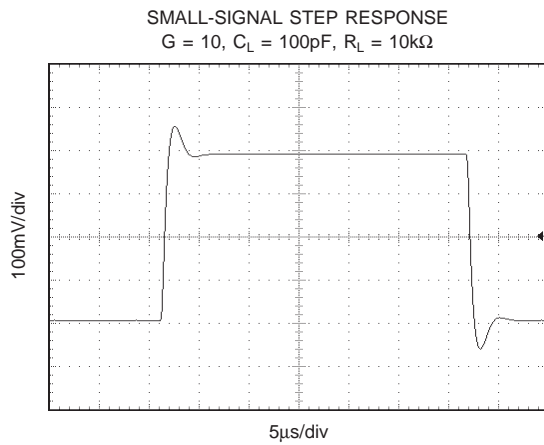
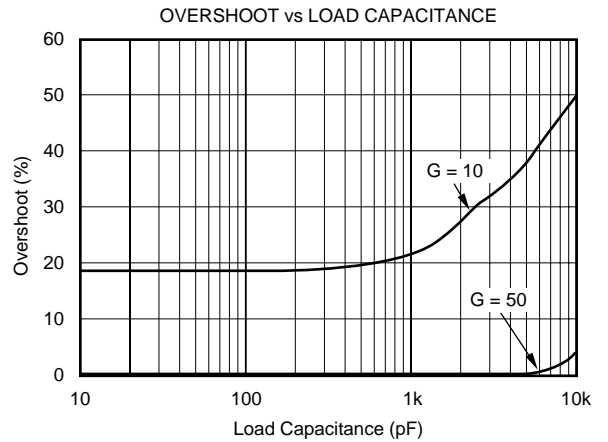
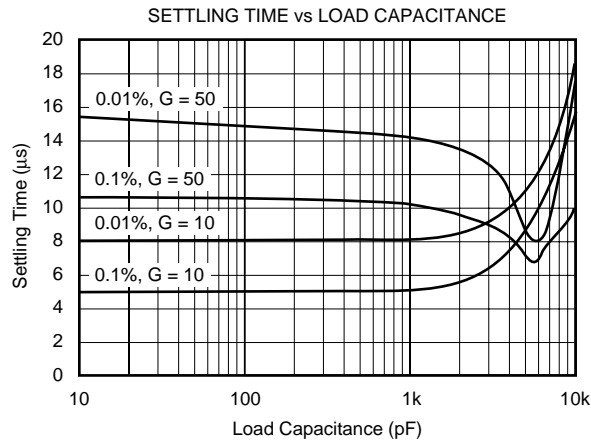
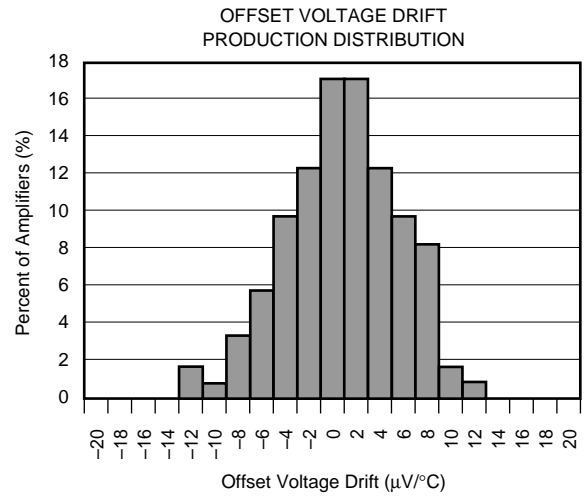
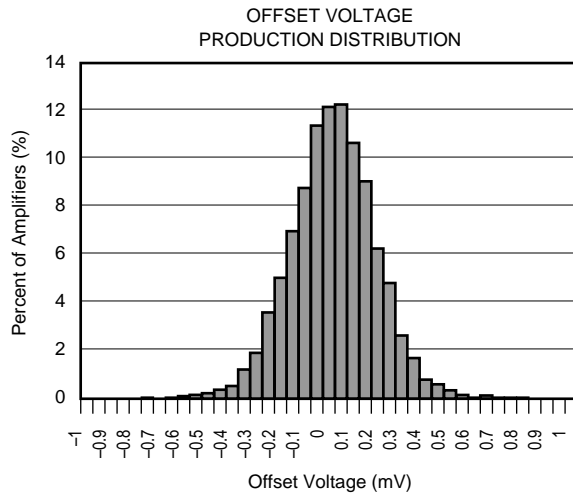
TYPICAL PERFORMANCE CURVES (Cont.)

At $T_A = +25^\circ\text{C}$, $V_S = 5.5\text{V}$, $R_L = 10\text{k}\Omega$ connected to $V_S/2$. R_G pins open ($G = 10$), and $\text{Ref} = V_S/2$, unless otherwise noted.



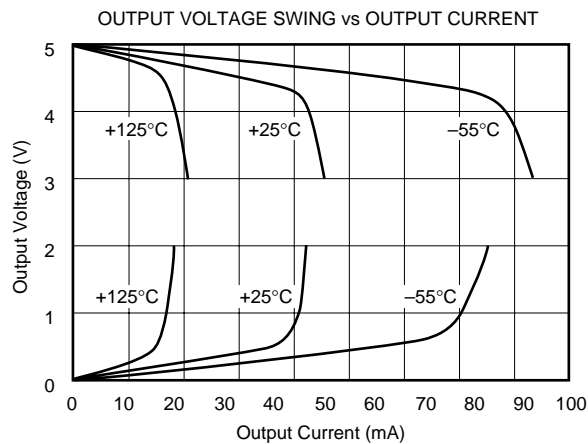
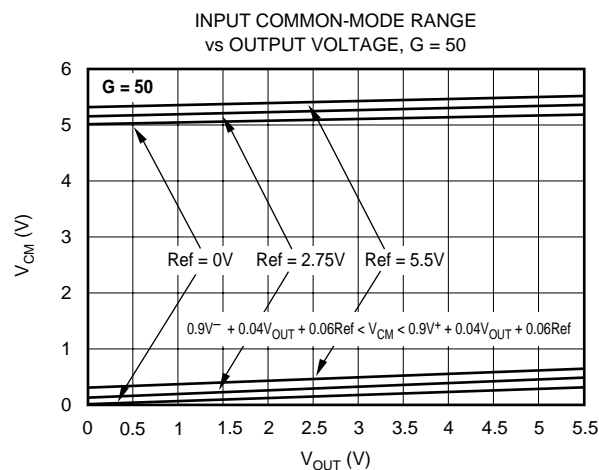
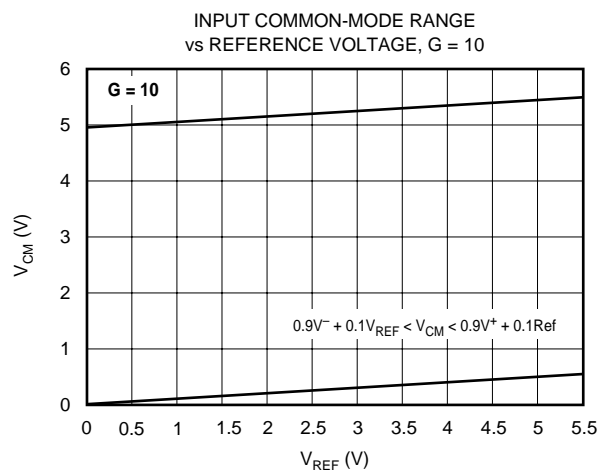
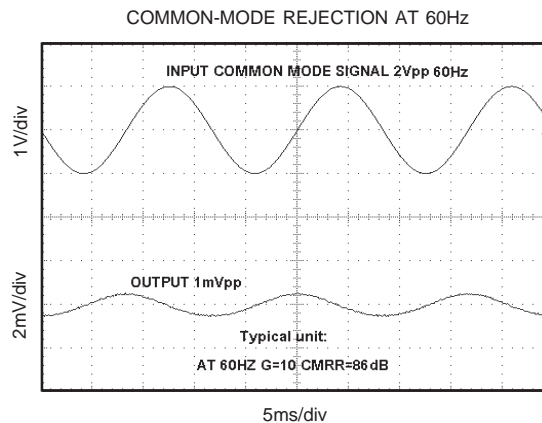
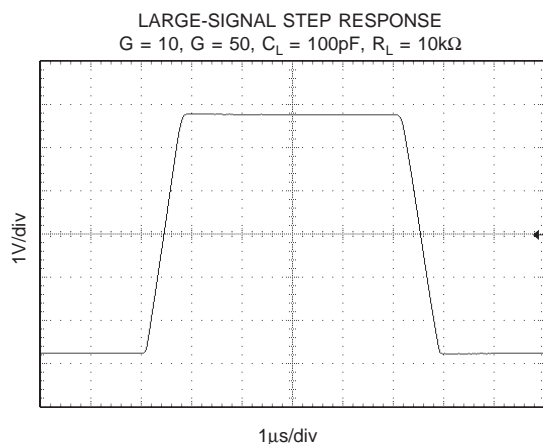
TYPICAL PERFORMANCE CURVES (Cont.)

At $T_A = +25^\circ\text{C}$, $V_S = 5.5\text{V}$, $R_L = 10\text{k}\Omega$ connected to $V_S/2$. R_G pins open ($G = 10$), and $\text{Ref} = V_S/2$, unless otherwise noted.



TYPICAL PERFORMANCE CURVES (Cont.)

At $T_A = +25^\circ\text{C}$, $V_S = 5.5\text{V}$, $R_L = 10\text{k}\Omega$ connected to $V_S/2$. R_G pins open ($G = 10$), and $\text{Ref} = V_S/2$, unless otherwise noted.



APPLICATIONS INFORMATION

Figure 1 shows the basic connections required for operation of the INA155. Applications with noisy or high impedance power supplies may require decoupling capacitors close to the device pins as shown.

The output is referred to the output reference terminal, Ref, which is normally set to $V_S/2$. This must be a low-impedance connection to ensure good common-mode rejection. A resistance of 200Ω in series with the Ref pin will cause a typical device to degrade to approximately 80dB CMRR.

In addition, for the $G = 50$ configuration, the connection between pins 1 and 8 must be low-impedance. A connection impedance of 20Ω can cause a 0.2% shift in gain error.

OPERATING VOLTAGE

The INA155 is fully specified and guaranteed over the supply range $+2.7V$ to $+5.5V$, with key parameters guaranteed over the temperature range of $-55^\circ C$ to $+125^\circ C$. Parameters that vary significantly with operating voltages, load conditions or temperature are shown in the Typical Performance Curves.

The INA155 can be operated from either single or dual power supplies. By adjusting the voltage applied to the reference terminal, the input common-mode voltage range and the output range can be adjusted within the bounds shown in the Typical Performance Curves. Figure 2 shows a bridge amplifier circuit operated from a single $+5V$ power supply. The bridge provides a relatively small differential voltage on top of an input common-mode voltage near $2.5V$.

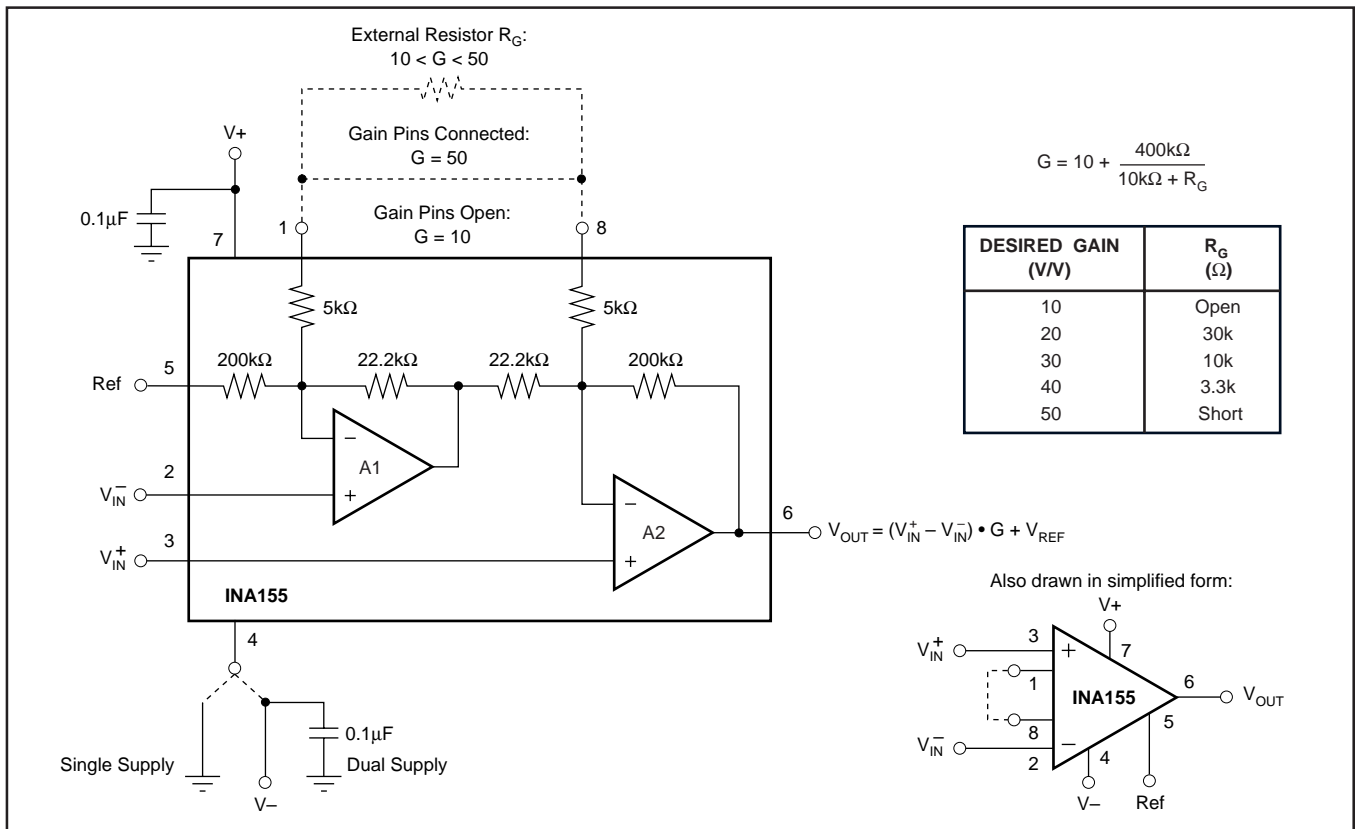


FIGURE 1. Basic Connections.

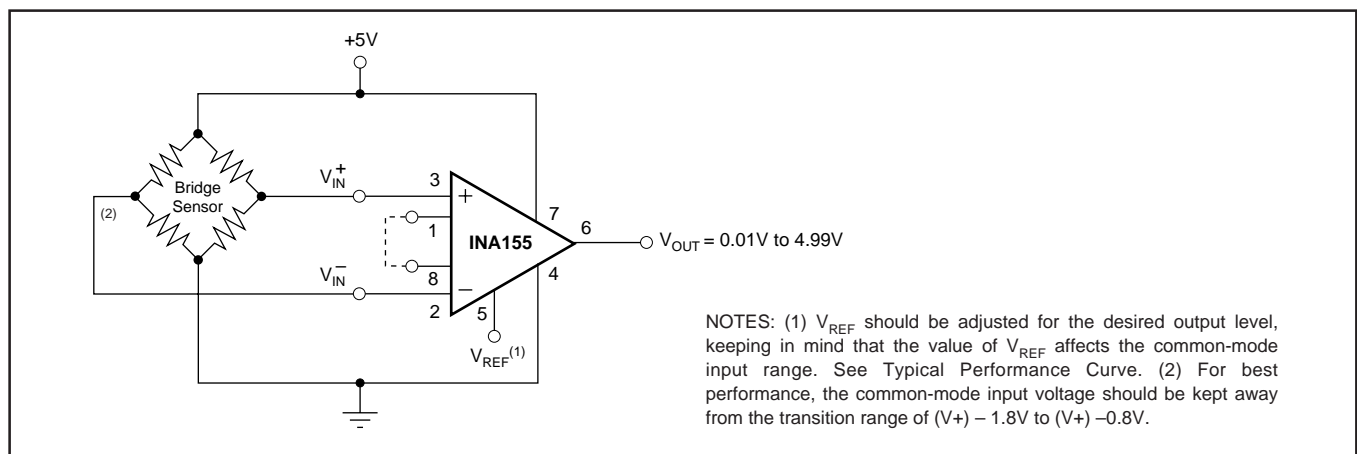


FIGURE 2. Single-Supply Bridge Amplifier.

SETTING THE GAIN

Gain of 10 is achieved simply by leaving the two gain pins (1 and 8) open. Gain of 50 is achieved by connecting the gain pins together directly. In the $G = 10$ configuration, the gain error is less than 0.1%. In the $G = 50$ configuration, the gain error is less than 0.25%.

Gain can be set to any value between 10 and 50 by connecting a resistor R_G between the gain pins according to the following equation:

$$10 + 400\text{k}\Omega / (10\text{k}\Omega + R_G) \quad (1)$$

This is demonstrated in Figure 1 and is shown with the commonly used gains and resistor R_G values. However, because the absolute value of internal resistors is not guaranteed, using the INA155 in this configuration will increase the gain error and gain error drift with temperature, as shown in Figure 3.

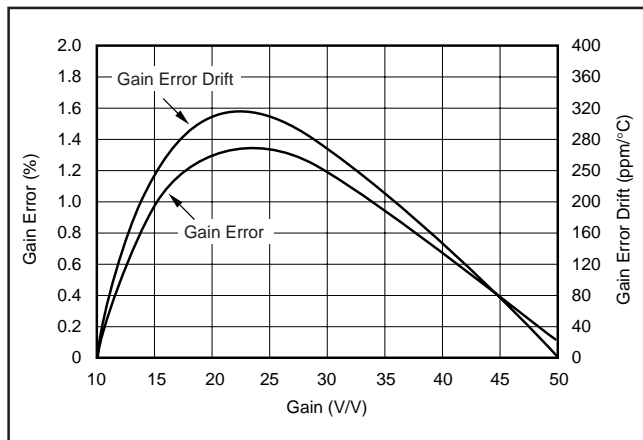


FIGURE 3. Typical Gain Error and Gain Error Drift with External Resistor.

OFFSET TRIMMING

The INA155 is laser trimmed for low offset voltage. In most applications, no external offset adjustment is required. However, if necessary, the offset can be adjusted by applying a correction voltage to the reference terminal. Figure 4 shows

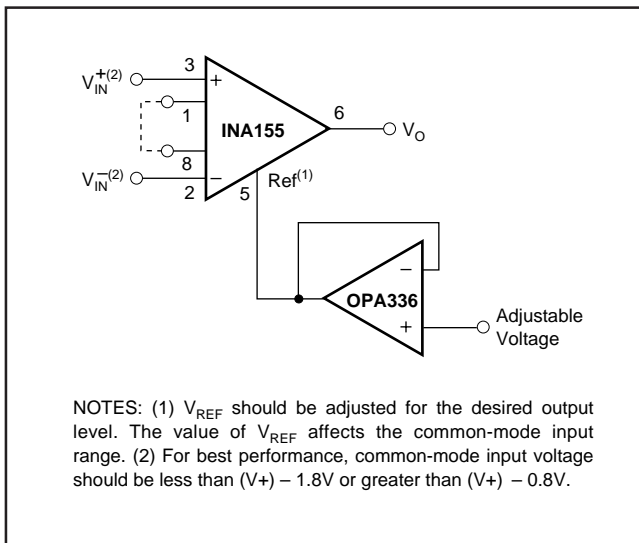


FIGURE 4. Optional Trimming of Output Offset Voltage.

an optional circuit for trimming the output offset voltage. The voltage applied to the Ref terminal is added to the output signal. An op amp buffer is used to provide low impedance at the Ref terminal to preserve good common-mode rejection.

INPUT BIAS CURRENT RETURN

The input impedance of the INA155 is extremely high—approximately $10^{13}\Omega$, making it ideal for use with high-impedance sources. However, a path must be provided for the input bias current of both inputs. This input bias current is less than 10pA and is virtually independent of the input voltage.

Input circuitry must provide a path for this input bias current for proper operation. Figure 5 shows various provisions for an input bias current path. Without a bias current path, the inputs will float to a potential that exceeds the common-mode range and the input amplifier will saturate.

If the differential source resistance is low, the bias current return path can be connected to one input (see the thermocouple in Figure 5). With higher source impedance, using two equal resistors provides a balanced input with advantages of lower input offset voltage due to bias current and better high-frequency common-mode rejection.

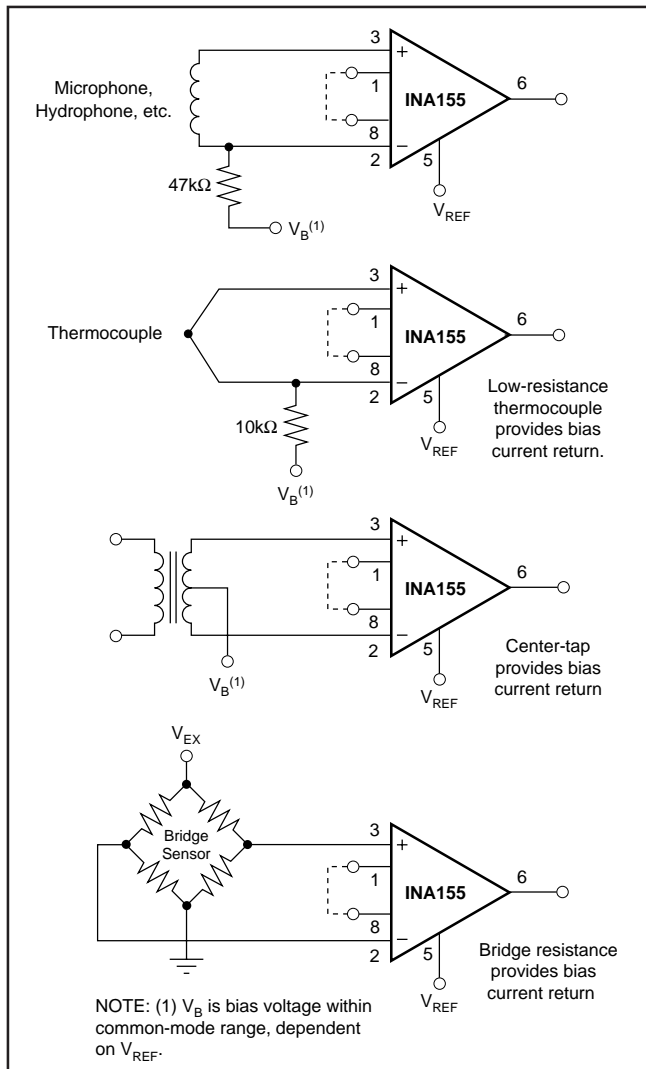


FIGURE 5. Providing an Input Common-Mode Current Path.

INPUT COMMON-MODE RANGE

The input common-mode range of the INA155 for various operating conditions is shown in the Typical Performance Curves. The common-mode input range is limited by the output voltage swing of A1, an internal circuit node. For the $G = 10$ configuration, output voltage of A1 can be expressed as:

$$V_{OUTA1} = -\frac{1}{9}V_{REF} + (1 + \frac{1}{9})V_{IN-} \quad (2)$$

Using this equation given that the output of A1 can swing to within 10mV of either rail, the input common-mode voltage range can be calculated. When the input common-mode range is exceeded (A1's output is saturated), A2 can still be in linear operation and respond to changes in the non-inverting input voltage. However, the output voltage will be invalid.

The common-mode range for the $G = 50$ configuration is included in the Typical Performance Curve, "Input Common-Mode Range vs Output Voltage."

INPUT RANGE FOR BEST ACCURACY

The internal amplifiers have rail-to-rail input stages, achieved by using complementary n- and p-channel input pairs. The common-mode input voltage determines whether the p-channel or the n-channel input stage is operating. The transition between the input stages is gradual and occurs between $(V+) - 1.8V$ to $(V+) - 0.8V$. Due to these characteristics operating the INA155 with input voltages within the transition region of $(V+) - 1.8V$ to $(V+) - 0.8V$ results in a shift in input offset voltage and reduced common-mode and power supply rejection performance. Typical patterns of the offset voltage change throughout the input common-mode range are illustrated in Figure 6. The INA155 can be operated below or above the transition region with excellent results. Figure 7 demonstrates the use of the INA155 in a single-supply, high-side current monitor. In this application, the INA155 is operated above the transition region.

RAIL-TO-RAIL OUTPUT

A class AB output stage with common-source transistors is used to achieve rail-to-rail output. For resistive loads greater than $10k\Omega$, the output voltage can swing to within a few millivolts of the supply rail while maintaining low gain error. For heavier loads and over temperature, see the typical performance curve "Output Voltage Swing vs Output Current." The INA155's low output impedance at high frequencies makes it suitable for directly driving Capacitive Digital-to-Analog (CDAC) input A/D converters, as shown in Figure 9.

INPUT PROTECTION

Device inputs are protected by ESD diodes that will conduct if the input voltages exceed the power supplies by more than 500mV. Momentary voltages greater than 500mV beyond the power supply can be tolerated if the current on the input pins is limited to 10mA. This is easily accomplished with input resistors R_{LIM} as shown in Figure 8. Many input signals are inherently current-limited to less than 10mA, therefore, a limiting resistor is not required.

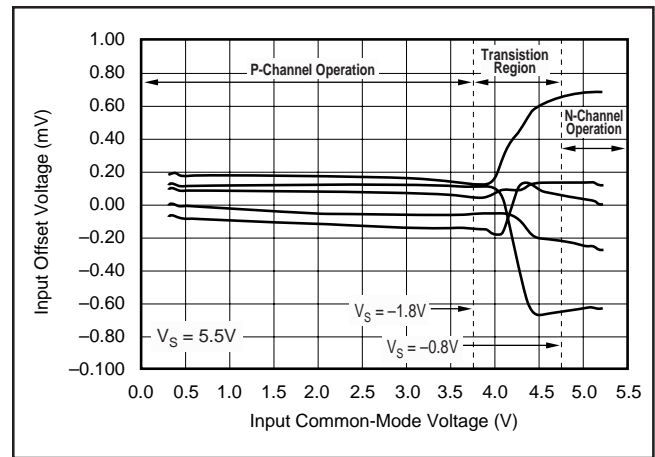


FIGURE 6. Input Offset Voltage Changes with Common-Mode Voltage.

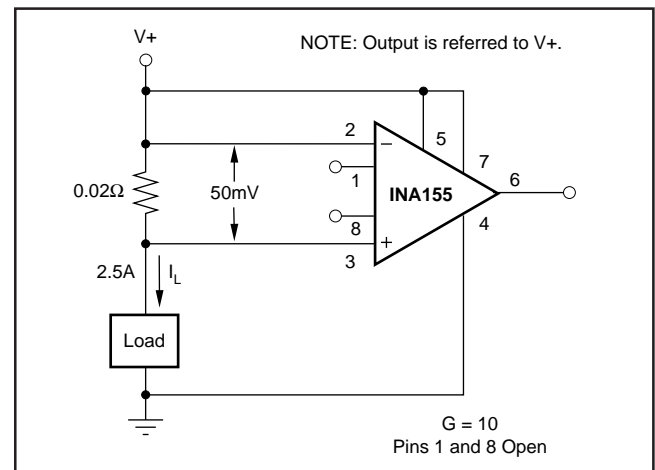


FIGURE 7. Single-Supply, High-Side Current Monitor.

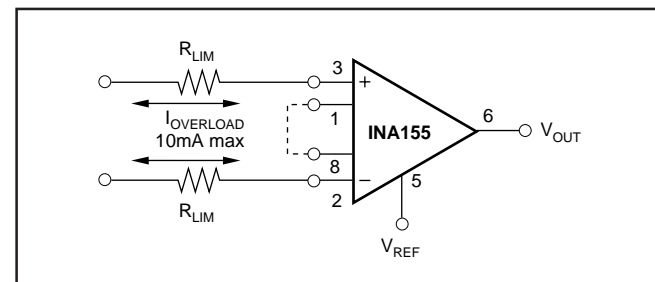


FIGURE 8. Input Current Protection for Voltages Exceeding the Supply Voltage.

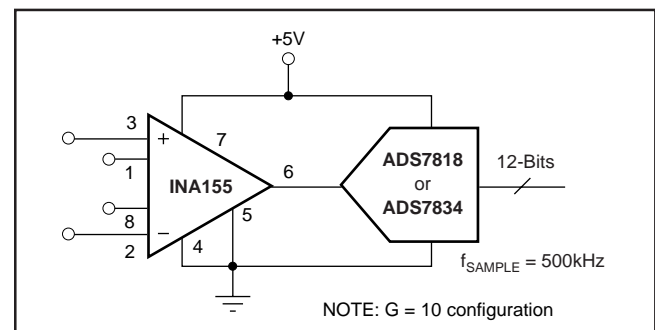


FIGURE 9. INA155 Directly Drives Capacitive-Input, High-Speed A/D Converter.

PACKAGING INFORMATION

Orderable part number	Status (1)	Material type (2)	Package Pins	Package qty Carrier	RoHS (3)	Lead finish/ Ball material (4)	MSL rating/ Peak reflow (5)	Op temp (°C)	Part marking (6)
INA155E/250	Active	Production	VSSOP (DGK) 8	250 SMALL T&R	Yes	Call TI	Level-2-260C-1 YEAR	-40 to 85	A55
INA155E/250.B	Active	Production	VSSOP (DGK) 8	250 SMALL T&R	Yes	Call TI	Level-2-260C-1 YEAR	-40 to 85	A55
INA155EA/250	Active	Production	VSSOP (DGK) 8	250 SMALL T&R	Yes	Call TI	Level-2-260C-1 YEAR	-	A55
INA155EA/250.B	Active	Production	VSSOP (DGK) 8	250 SMALL T&R	Yes	Call TI	Level-2-260C-1 YEAR	-40 to 85	A55
INA155EA/2K5	Active	Production	VSSOP (DGK) 8	2500 LARGE T&R	Yes	Call TI	Level-2-260C-1 YEAR	-	A55
INA155EA/2K5.B	Active	Production	VSSOP (DGK) 8	2500 LARGE T&R	Yes	Call TI	Level-2-260C-1 YEAR	-40 to 85	A55
INA155U	Active	Production	SOIC (D) 8	75 TUBE	Yes	Call TI	Level-2-260C-1 YEAR	-	INA 155U
INA155U.B	Active	Production	SOIC (D) 8	75 TUBE	Yes	Call TI	Level-2-260C-1 YEAR	-40 to 85	INA 155U
INA155U/2K5	Active	Production	SOIC (D) 8	2500 LARGE T&R	Yes	Call TI	Level-2-260C-1 YEAR	-	INA 155U
INA155U/2K5.B	Active	Production	SOIC (D) 8	2500 LARGE T&R	Yes	Call TI	Level-2-260C-1 YEAR	-40 to 85	INA 155U
INA155UA	Active	Production	SOIC (D) 8	75 TUBE	Yes	Call TI	Level-2-260C-1 YEAR	-	INA 155U A
INA155UA.B	Active	Production	SOIC (D) 8	75 TUBE	Yes	Call TI	Level-2-260C-1 YEAR	-40 to 85	INA 155U A
INA155UA/2K5	Active	Production	SOIC (D) 8	2500 LARGE T&R	Yes	Call TI	Level-2-260C-1 YEAR	-	INA 155U A
INA155UA/2K5.B	Active	Production	SOIC (D) 8	2500 LARGE T&R	Yes	Call TI	Level-2-260C-1 YEAR	-40 to 85	INA 155U A
INA155UG4	Active	Production	SOIC (D) 8	75 TUBE	Yes	Call TI	Level-2-260C-1 YEAR	See INA155U	INA 155U

⁽¹⁾ **Status:** For more details on status, see our [product life cycle](#).

(2) Material type: When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

(3) RoHS values: Yes, No, RoHS Exempt. See the [TI RoHS Statement](#) for additional information and value definition.

(4) Lead finish/Ball material: Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

(5) MSL rating/Peak reflow: The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

(6) Part marking: There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

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TAPE AND REEL INFORMATION



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
INA155U/2K5	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
INA155UA/2K5	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1

TAPE AND REEL BOX DIMENSIONS



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
INA155U/2K5	SOIC	D	8	2500	353.0	353.0	32.0
INA155UA/2K5	SOIC	D	8	2500	353.0	353.0	32.0

TUBE



*All dimensions are nominal

Device	Package Name	Package Type	Pins	SPQ	L (mm)	W (mm)	T (μm)	B (mm)
INA155U	D	SOIC	8	75	506.6	8	3940	4.32
INA155U.B	D	SOIC	8	75	506.6	8	3940	4.32
INA155UA	D	SOIC	8	75	506.6	8	3940	4.32
INA155UA.B	D	SOIC	8	75	506.6	8	3940	4.32
INA155UG4	D	SOIC	8	75	506.6	8	3940	4.32

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